

VR241/02/10 VR242/02 VR243/01/13 VR247/01/02/06 VR347/02/10 VR447/02

VR2410/19 2SB41/11 VR2419/39 2SB410/18 VR2469/39 2SB419/38 VR3419/39 2SB469/38 VR3469/39 3SB419/38 VR3479/39 3SB469/38 VR4469/39 3SB47/11 VR4479/39 24DV10/19





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Safety regulations require that the set be restored to its original condition and that parts which are identical with those specified be used.

Survey of versions

	Survey	of versions :
	/01	PAL B/G
	/02	PAL B/G (with VPS)
	/05	PAL I England
\	_/06	PAL B/G & SECAM L (with VPS) Swiss
	/07	PAL I Ireland
	/08	PAL B/G Italy
	/10 /11	PAL B/G Belgium
	/13	PAL B/G Nordic
	/16	PAL B/G Spain
	/18 /19	SECAM L
	/38 /39	SECAM L & PAL B/G
	/59	PAL/SECAM B/G, D/K
	/60	PAL/SECAM D/K

Survey of remote controls:

our vey or remote c	CHUOIS.	
VR241/02	RT143/124	4822 218 30744
VR241/10	RT140/114	4822 218 30748
VR242/02	RT242/124	4822 218 30747
VR247/02, VR347/02	RT941/124	4822 218 30749
VR243/01, VR243/13	RT431/415	4822 218 30702
VR247/01, VR347/10	RT943/114	4822 218 30751
VR247/06	RT940/114	4822 218 30758
VR447/02	RT941/124	4822 218 30749
VR2410/19 VR2419/39	RT141/144	4822 218 30754
VR2469/39 VR3469/39	RT740/144	4822 218 30755
VR3419/39	RT141/144	4822 218 30754
VR3479/39 VR4479/39	RT942/144	4822 218 30756
VR4469/39	RT740/144	4822 218 30755
2SB410/18 2SB419/38	RT140/244	4822 218 30753
24DV10/19 3SB419/38	RT140/244	4822 218 30753
2SB41/11	RT140/214	4822 218 30757
2SB469/38 3SB469/38	RT740/244	4822 218 30759
3SB47/11	RT940/214	4822 218 30761
Tape deck :		

VR24./ VR34./ 3SB/ VR44./ 2.DV./	WD-T-P2/0 WD-T-P3/0 WD-T-P4/0 WD-T-P2/0	(2 heads) (3 heads) (4 heads) (2 heads)
Z.DV./	WD-1-P2/0	(2 heads)
2SB/	WD-T-P2/0	(2 heads)

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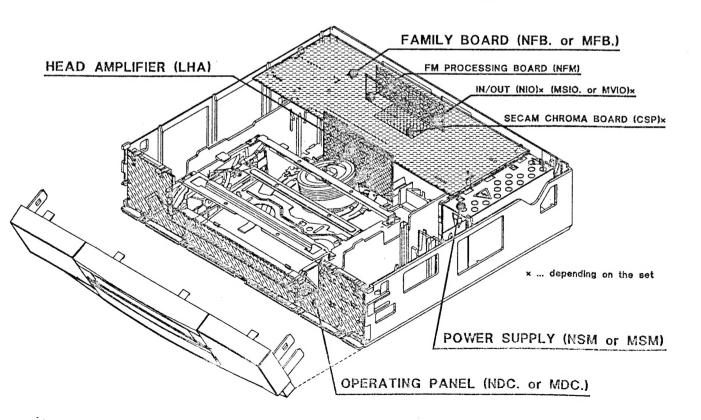
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FEATURES

	PAL B/G	PAL B/G & SECAM L	SECAM L	PLL tuning system	2 Videoheads	3 Videoheads	4 Videoheads	VPS	LCD-Remote	Teletext / VPT	Audio dubbing	Studio picture control	Show View / Video Plus	Video Longplay	16:9 switching	Follow TV	Synchroedit	Real time download	Title recording	VISS	6 Timer blocks	2 Scart connectors
VR241/02 VR241/10 VR242/02 VR247/01 VR247/02 VR247/06 VR347/02 VR347/10 VR2410/19 VR2419/39 VR3469/39 VR3469/39 VR3469/39 VR3479/39 2SB41/11 2SB410/18 2SB419/38 3SB419/38 3SB419/38 3SB419/38 3SB419/11 24DV10/19	•				• • • • • • • • • • • • • • • • • • • •	,			•				•									
VR447/02 VR4469/39 VR4479/39	•						•	•	•		•							•	•	•	•	
N5 VR243/01 VR243/13					•				•	•										•		

SURVEY OF SETS

	N	N2	NB	Ŋą	N5
VR241/02/10			+		
VR242/02			*		
VR243/01/13					•
VR247/01/02/06			•		
VR347/02/10			•		
VR2410 /19			*		
VR2419 /39			+		
VR3419 /39			•		
VR3479 /39			*		
VR2469/39			*		
VR3469 /39			*		
VR447/02				•	
VR4469/39				*	
VR4479 /39				*	
2SB41/11			♦		
2SB410/18			*		
2SB419/38			*		
2SB469/38			•		
3SB419 /38			•		
3SB469 /38			•		
3SB47/11			•		
24DV10 /19			*		



SURVEY OF SETS AND PCB'S

A TABITS BUNDALY		OPERATING PANEL				FAMILY BOARD	- VS-Videoprocessing	- IO-Input/Output	- FV-Frontend	- AL-Audio linear					SECAM L BOARD	IN/OUT BOARD	IN/OUT, VPS BOARD	IN/OUT, TXT BOARD	IN/OUT, VPS BOARD	IN/OUT BOARD	FMBOARD	HEAD AMPLIFIER			TAPE DECK		
WSN WSW		NDCP2/UG	MDCP3/VPT	NDCB1/UG	NDCP4/UBG	NFB3/2GV	NFB3/2G	NFB3/2GL	NFB3/2GLV	MFB2T/2L	MFB3T/2GV	NFB4/4FG	NFB4/4FGVLP	and the state of t	CSP	MSIO	MSIO/VPS	MVIO	NIO/VF	NIO/F	NFM	LHA2/0	LHA3/0	LHA4/0	DM2/0	DM3/0	DM4/0
VR247/02 VR247/06 VR347/02 VR347/10 VR2410/19 VR2419/39 VR2469/39 VR3419/39 VR3469/39 VR3479/39 2SB41/11 2SB410/18 2SB419/38	•		•	• • • • • • • • • • • • • • • • • • • •					•	•		•	•									•	•••••••	•		••••••	

MFB1T/	VST-Tuning, 1 Scart	
NFB1/	VST-Tuning, 1 Scart	
NFB2/	PLL-Tuning, 1 Scart	FAMILY BOARD
NFBE/	ECO, VST-Tuning, 1 Scart	
NFBC/	ECO, PLL-Tuning, 1 Scart	
MFB2T/	PLL-Tuning, 2 Scart	
MFB3T/	PLL-Tuning, 2 Scart, TXT	FAMILY BOARD NS N5
NFB3/	PLL-Tuning, 2 Scart	FAMILY DOARD MAN AND AND AND AND AND AND AND AND AND A
NFBO/	ECO, PLL-Tuning, 2 Scart	
NFB4/	PLL-Tuning, 2 Scart, Audio dub.	FAMILY BOARD







TECHNICAL DATA

TECHNISCHE DATEN

ECHNICAL DATA	TECHNISCHE DATEN	CARACTERISTIQUES
lains voltage	.Netzspannung	Tension secteur
	.Netzfrequenz	
	Laigtungaaufnahma	

Mains voltage	Netzspannung	Tension secteur	180 - 240 V
Mains frequency	Netzfrequenz	Fréquence	45 - 65 Hz
Power consumption	Leistungsaufnahme	Puissance absorbée	15 W
			+10°C - +35°C
Relative humidity	Relative Luftfeuchtigkeit	Humidité relative	20 - 80%
Dimensions	Abmessungen	Encombrement	380 x 86 x 338 mm
Weight	Gewicht	Poids	~ 4,6 kg
			typ. 95s (260s ECU) E180 cass.
			horizontally, max 15°
			>234 lines
Audio	Audio	Audio SP :	80Hz - 10kHz (≤8dB)
			80Hz - 5kHz (<8dB)





DATI TECNICI

TECHNISCHE GEGEVENS

DATOS TECNICOS

Netspanning	Tensión de red	.Tensione di alimentazione	180 - 240 V
Netfrequentie	Frecuencia de red	.Frequenza di rete	45 - 65 Hz
Opgenomen vermogen		.Potenza assorbita	15 W
Omgevingstemperatuur	Temperatura ambiente	.Temperatura ambiente	+10°C - +35°C
Relatieve vochtigheid	Humedad relativa	.Umiditá relativa	20 - 80%
Afmetingen	Dimensiones	.Dimensioni	380 x 86 x 338 mm
Gewicht	Peso	.Peso	~ 4,6 kg
Vooruit/terugspoeltijd	.tiempo de (re-)bobinado	.Tempo di (ri-)avvolgimento	typ. 95s (260s ECU) E180 cass.
Gebruikspositie	.Posición de uso	.Posizione di funzionamento	horizontally, max 15°
Oplossend vermogen	.Resolución video	.Risoluzione video	>234 lines
Audio	Audio	.Audio SP :	80Hz - 10kHz (≤8dB)
		LP:	80Hz - 5kHz (≤8dB)

SAFETY INSTRUCTIONS

- Safety regulations demand that the set be restored to its original condition and that components identical with the original types be
 - Safety components are marked by the symbol
- All IC s and many other semi-conductors are susceptible to electrostatic discharges (ESD). Careless handling during repair may reduce life drastically. When repairing, make sure that you are conneted with the same potential as the mass of the set via a wrist wrap with resistance. Keep components and tools on the same
- A set to be repaired should always be connected to the mains via a suitable isolating transformer.
- Never replace any modules or any other parts while the set is switched on.
- Use plastic instead of metal alignment tools. This in order to prelude short-circuit or to prevent a specific circuit from being rendered unstable.

REMARKS

- The direct voltages and oscillograms ought to be measured relative to the set mass.
- The direct voltages and oscillograms mentioned in the diagrams ought to be measured with a colour bar signal and the picture carrier at 503.25 MHz (C25).
- The oscillograms and direct voltages have been measured in RECORD or PLAY mode.
- The semiconductors, which are mentioned in the circuit diagram and in the parts lists, are fully exchangeable per position with the semiconductors in the set, irrespective of the type designation of these semiconductors.

SICHERHEITSHINWEISE

- Die Sicherheitsvorschriften erfordern es, daß sich das Gerät nach der Reparatur in seinem originalen Zustand befindet und daß die zur Reparatur benutzten Ersatzteile mit den OriginalErsatzteilen identisch sind.
 - Sicherheits-Bauteile sind mit der Markierung versehen /!
- Alle IC's und Halbleiter sind empfindlich gegen elektrostatische Entladungen (ESD). Unvorschriftmässige Behandlung von Halbleitern im Reparaturfall, kann zur Zerstörung dieser Bauteile oder zu einer drastischen Reduzierung der Lebensdauer führen. Sorgen Sie dafür, dass Sie sich im Reparaturfall über ein Armband mit Widerstand auf dem gleichen Potential, wie die Masse des Gerätes befinden. Alle Bauteile, Werkzeuge und Hilfsmittel sind auf das gleiche Potential zu legen.
- Ein zu reparierendes Gerät ist immer über einen Trenntransformator an die Netzspannung anzuschliessen.
- Bei eingeschaltetem Gerät dürfen keine Module oder sonstige Einzelteile ausgetauscht werden.
- Zum Abgleich sind ausschliesslich Kunststoffwerkzeuge zu benutzen (keine Metallwerkzeuge verwenden). Dadurch wird vermieden, dass ein Kurzschluß entstehen kann oder eine Schaltung instabil wird.

ANMERKUNGEN

- Die Gleichspannungen Oszillogramme und sind aeaen Gerätemasse zu messen.
- Die Gleichspannungen und Oszillogramme angeführt in den Schaltbildern sollen unter folgenden Bedingungen gemessen werden: Farbbalkensignal, Bildträger auf 503.25 MHz (C25)
- Die Oszillogramme und Gleichspannungen sind in RECORD oder PLAY gemessen.
- Die in den Stücklisten aufgeführten Bauteile sind positionsweise voll auswechselbar gegen die Bauteile in dem Gerät, ungeachtet der etwaigen Typenbezeichungen.



AVERTISSEMENTS

- Les normes de sécurité exigent qu'aprés réparation l'appareil soit remis dans son état d'origine et que soient utilisées les piéces de rechange identiques à celles spécifiées.
 Les composants de sécurité sont marqués
- Tout les IC et beaucoup d'autres semi-conducteurs sont sensibles aux décharger statiques (ESD). Leur longévité pourrait être considérablement écourté par le fait qu'aucune précaution n'est prise à leur manipulation. Lors de réparations s'assurer de bien être relié au même potential que la masse de l'appareil et enfiler le bracelet serti d'une résistance de sécurité. Veiller à ce que les composants ainsi que les outils que l'on utilise soient également à ce potentiel.
- Toujours alimenter un appareil à réparer à travers un transfo d'isolement.
- Ne jamais remplacer les modules ni d'autres composants quand l'appareil est sous tension.
- Pour l'ajustage, utiliser des outils en plastique au lieu d'instruments métalliques. Ceci afin d'éviter les court - circuits et exclure l'instabilité dans certains circuits.

OBSERVATIONS

- La mésure des tensions continues et des oscillogrammes doit se faire par rapport à la terre de l'appareil.
- La mésure des tensions continues et des oscillogrammes figurant sur le schéma doit se faire dans un signal de barre couleur porteuse image sur 503.25 MHz (C25).
- Les oscillogrammes et tension sont mésurées en mode RECORD ou PLAY.
- Les semi-conducteurs indiqués dans le schéma de principe et à la liste des compostants, sont interchangeables par repère sur ce chassis avec les semi-conducteurs de l'appareil quelle que soit la désignation de type donnée sur ces semi-conducteurs.



VEILIGHEIDSINSTRUCTIES

- Veiligheidsbepalingen vereisen, dat het apparaat in zijn oorspronkelijke toestand wordt teruggebracht en dat onderdelen, indentiek aan de oorspronkelijke, worden toegepast. De veiligheidsonderdelen zijn aangeduid met het symbool //.
- Alle IC's en vele andere halfgeleiders zijn gevoelig voor elektrostatische ontladingen (ESD). Onzorgvuldig behandelen tijdens reparatie kan de levensduur drastisch doen verminderen. Zorg ervoor, dat U tijdens reparatie via een polsband met weerstand verbonden bent met hetzelfde potentiaal als de massa van het apparaat. Houd componenten en hulpmiddelen ook op ditzelfde potentiaal.
- Sluit een apparaat dat gerepareerd wordt altijd via een scheidingstransformator aan op de netspanning.
- Verwissel nooit modules of andere onderdelen terwijl het apparaat is ingeschakeld.
- Gebruik voor het afregelen plastic i.p.v metalen gereedschap. Dit om mogelijke kortsluiting te voorkomen of een bepaalde schakeling instabiel te maken.

OPMERKINGEN

- De gelijksspanningen en oscillogrammen dienen gemeten te worden ten opzichte van de apparaat aarde.
- De gelijksspanningen en oscillogrammen vermeld in de schema's dienen gemeten te worden met een kleurbalkensignaal beelddraaggolf op 503.25 MHz (C25).
- De oscillogrammen en gelijksspanningen zijn in RECORD of PLAY mode gemeten.
- De halfgeleiders, die in het pricipeschema en in de stuklijsten, zijn vermeld, zijn per positie volledig uitwisselbaar met de halfgeleiders in het apparaat, ongeacht de typeaanduiding op deze halfgeleiders.



AVISOS

- Las instrucciones de seguridad exigen que, después de la reparación, el aparato se encuentre en el estado original y que las piezas de repuesto, utilizadas para la reparación, sean idénticas a las originales.
 - Los componentes de seguridad están marcados con
- Todos los IC y semiconductores son sensibles a descargas electrostáticas (ESD). Un tratamiento no conforme a las instrucciones de semiconductores, en caso de reparación, podría llevar a la destrucción de estos componentes o a una reducción drástica de la duración. En caso de reparación tenga cuidado de que esté al mismo potencial que la masa del aparato, por una pulsera con resistencia. Ponga todos los componentes, herramientas y recursos al mismo potencial.
- Para reparar un aparato hay que conectarlo siempre a la alimentación a través de un transformador de aislamiento.
- Cuando un aparato está en marcha no pueden ser cambiados módulos u otras piezas de repuesto.
- Para los ajustes hay que utilizar exclusivamente herramientas de plástico (nunca herramientas metálicas). Así se evitarán cortocircuitos y circuitos inestables.

NOTAS

- Hay que medir las tensiones continuas y los oscilogramas contra la masa del aparato.
- Las tensiones continuas y los oscilogramas mencionados en los esquemas tienen que ser medidos de la manera siguiente: señal barra de color portadora de imagen en 503.25MHz (C25)
- Los oscilogramas y las tensiones continuas son medidas en "RECORD" y "PLAYBACK"
- Los componentes mencionados en las listas se los puede cambiar por los componentes en el aparato, a pesar de eventuales designaciones de tipos.



AVVERTIMENTI

- Le prescrizioni di sicurezza richiedono che l'apparecchio sia ricondotto alle condizioni originali e che siano usati ricambi originali. Componenti di sicurezza sono marcati con
- Tutti gli IC e semiconduttori sono sensibili a scariche elettrostatiche (ESD). Noncuranze durante la riparazione di semiconduttori possono danneggiarli o condurre ad una riduzione drastica della durata. Durante la riparazione assicurarsi di essere collegati allo stesso potenziale attraverso un bracciale di protezione contro scariche elettrostatiche. Inoltre tenere anche tutti i componenti e gli attrezzi a questo potenziale.
- Apparecchi da riparare bisogna collegarli sempre via un trasformatore isolante (separatore) alla tensione normale.
- Non scambiare moduli o altri componenti quando l'apparecchio è in funzione.
- Per l'accordo usare soltanto attrezzi di plastica (non usare attrezzi metallici). Cosi si evitano cortocircuiti e collegamenti instabili.

OSSERVAZIONI

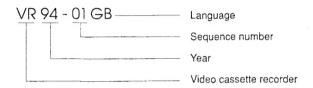
- Misurare le tensioni continue e gli oscillogrammi riferiendosi alla massa dell'apparecchio.
- Le tensioni continue e gli oscillogrammi indicati negli schemi di collegamento devono essere misurati secondo le condizioni seguenti: segnale barre colore, portante dell'immagine su: 503.25 MHz (C25).
- Gli oscillogrammi e le tensioni continue sono misurati in RECORD o PLAYBACK,
- I semiconduttori che sono menzionati negli schemi e nelle liste sono intercambiabili con quelli di pari tipo nonostante siano montati in posizione diversie.

MODIFICATIONS

Description of the system used for publishing modification data and supplements to the service manual.

All modification data and supplements to the Service Manual are published by means of Service Information bulletins.

Each Service information has a number, for example :



A Service Information bulletin concists of a front sheet, as the case may be followed by supplementary and/or replacement sheets.

Replacement sheets serve to replace existing sheets in the Service Manual. These sheets are identified by an additional letter after the page number, for example 5-1a. Page 5-1a then takes the place of page 5-1.

Supplementary sheets are inserted between the existing sheets in the Service Manual. These sheets can be identified by an additional figure following the page number, for example 5-1-1.

Sheet 5-1-1 should be inserted after page 5-1.

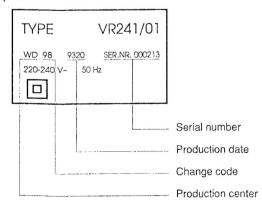
Description of the system by means of which modifications are indicated in the recorder.

All important parts of the recorder, such as tape deck, p.c. boards and modules, are provided with a sticker. These stickers specify a number of product data. The meaning of this data will now be explained for the most important sections.

The complete recorder

The type plate is located at the back of the recorder, below an example of such a type plate is given.

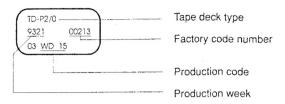
Type plate:



Note:

In the case of an important modification to the recorder the productionscode on the type plate is increased by one. E.g. 00 becomes n1

· Tape deck



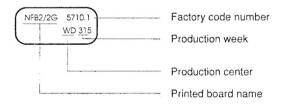
Note:

The production code and the serial number on the tape deck need not correspond to the production code and the serial number on the type plate.

· Printed panels

The stickers are generally located on the track side of the module.

Example:



Remarks:

The production status number will not always be mentioned. In case of an important modification, the last figure of the factory code number (point number) is increased by one. E.g. 5710.1 becomes 5710.2.

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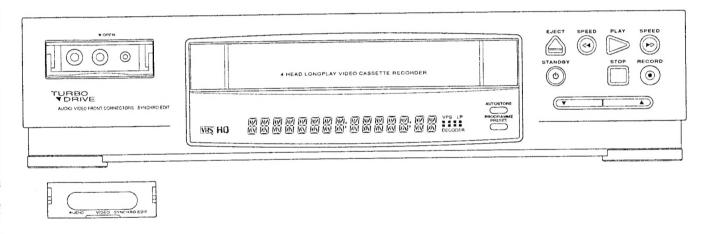
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Symbol on front of video recorder

AUTOSTORE Autostore PROG. PRESET Programme preset Cassette eject EJECT **⊲** SPEED Rewind/Reverse scanning PLAY Playback SPEED ▶▷ Forward wind Forward scanning STANDBY U Switch off STOP Stop RECORD • Record ~ Down / Minus, programme number Up / Plus, programme number AUDIO Audio chinch Video chinch VIDEO SYNCHRO EDIT Synchro edit



Symbol on back of video recorder

Aerial input socket

Aerial output socket

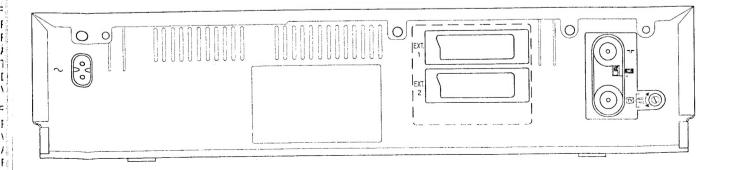
Mains socket

EXT 1 AV-Euro socket (Scart)

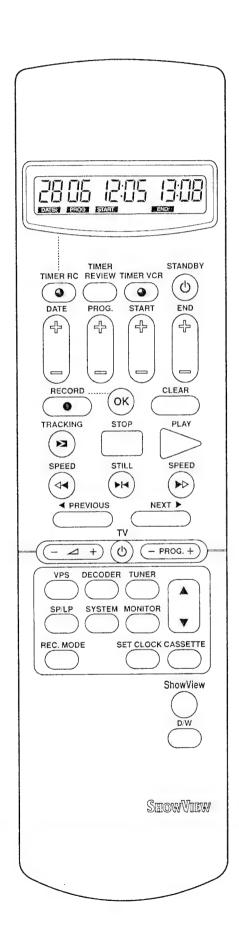
EXT 2 AV-Euro socket (Scart)

MOD. FREQ. Channel control

SIG Attenuator switch



The remote control



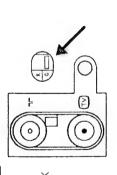
TIMER RC 3	Timer RC
TIMER REVIEW	Timer Review
TIMER VCR 9	Timer VCR
STANDBY O	Standby
DATE +/-	Timer Date +/-
PROG. +/-	Timer Programme +/-
START +/-	Timer Start Time +/-
END +/-	Timer Stop Time +/-
RECORD •	Record (OK and
	RECORD button simultaneously)
OK	OK
CLEAR	Reset/clear
TRACKING >	Tracking/optimum setting
STOP	Stop
PLAY	Playback
⊲ SPEED	Rewind/Reverse scanning
STILL M	Pause/Still picture
SPEED ▶▷	Wind/Forward scanning
◆PREVIOUS	Previous picture
NEXT ▶	Next picture
VPS	VPS On/Off
DECODER	Decoder
TUNER	Tuner
A	Up
Y	Down
SP/LP	Tape speed selection (SP/LP)
SYSTEM	System
MONITOR	Monitor
REC.MODE	Record mode
SET CLOCK	Set clock on video recorder
CASSETTE	Cassette
SHOW VIEW	'ShowView' programming
D/W	Daily/weekly programming
	Additional TV functions:
- 🗸 +	Up/Down TV
(b)	Switch On/Off
-PROG. +	Up/Down programme number

Note: Only works on televisions with the same remote control code.

The G/K switch

You can connect this video recorder with a TV set equipped either with a West European PAL-B,G TV-standard ('G') or with an East European SECAM-D,K TV-standard ('K').

If you don't hear the sound from the video recorder during playback, switch to the other TV-standard. The switch is positioned at the back. In our factory the switch GIKI is set to position 'G'.



Setting the display language and the wide screen format

You can select from ten languages for the display on your video recorder and select the screen format.

- Ensure that there is no cassette in the cassette slot. With the video recorder switched off, press the [KASSETTE and [WIEDERGABE buttons simultaneously.
- ② Use the ▼ or ▼ button to select the required display language, e.g.: ENGLISH'.



Press the OK button twice.

• 4:3' appears in the display. If you have one of the new wide screen TV sets, use the <a>¬► button to switch over to the '16:9' format. Otherwise, leave the setting at '4:3'.

◆ Finally, press the BEREITSCHAFT ◆ button.

Connecting a decoder

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Some TV broadcasters transmit encoded television signals which you can only see with a purchased or rented decoder. You can connect such a decoder (descrambler) to this video recorder.

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O Connect the decoder to the video recorder with an AV-Euro cable (<u>LEXTZ</u>) socket).

You will find a description of how to store TV channels with the decoder in the chapter 'Storing TV channels'.

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O Connect the TV set to the EXT I socket.

Note:

- * You cannot use the decoder with your video recorder and your TV set simultaneously.
- * On your video recorder select the programme number that you allocated to the decoder function when storing the channel numbers. The video recorder will then automatically use the decoder.

Setting the clock and date on the video recorder

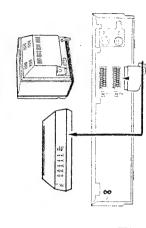
In order to be able to programme recordings, you must first ensure that the video recorder clock is correctly set. To do this, use the <u>DOWN</u> or <u>UP</u> buttons on the remote control in all the following steps.

- Press the <u>SET CLOCK</u> button on the remote control. 'TIME' will appear in the video recorder display. Set the current time.
- Press the OK button. 'YEAR' will appear in the display. Set the current year.
- Press the OK button again. 'MONTH' will appear in the display. Set the current month.
- Press the <u>OK</u> button again. 'DATE' will appear in the display. Set today's date.

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• Press the <u>OK</u> button again. The time and date have now been set. In confirmation, 'READY' will appear briefly in the display.





Still picture/Super-slow motion

♥ Press the PLAY► button on the remote control.

② Press the STILL ► → button. The picture will stand still

Each time you press STILL MY again the picture will move on one step at a time.

Hold down the STILL HE button. The picture will be played in super-slow motion.

• Press the PLAY* button to continue playing back at the normal speed.

Special note:

* If the still picture vibrates vertically, keep pressing the TRACKING Dutton until the vibration disappears.

If you pass the optimum setting, repeat this step with the <u>TRACKING</u> button.

You only have to find the optimum setting for your TV set once as the video recorder will store it automatically.

Please note, however, that interference may still occur with poor quality hired cassettes. This is not a fault in your video recorder.

Searching for a tape position

Sometimes you may have recorded two or more TV programmes on one cassette.

So that you do not have to spend time searching, your video recorder offers you an automatic search facility.

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every time you start to record. You can search for these code marks on the tape using the PREVIOUS and NEXT buttons on your remote control.

The video recorder marks the tape with code marks

Press the <u>NEXT</u> button to select the next code mark or the <u>PREVIOUS</u> button for the previous code mark.

recorder display.

Once the video recorder finds the code mark it will automatically switch to playback.

Either 'NEXT' or 'PREVIOUS' will appear in your video

Note:

* When you press any tape transport button (e.g.: the <u>PLAYF</u> or <u>PAUSE/STOP !! *</u> button) this search function will be stopped.

* You cannot use this function with recordings made on another video recorder that does not have this function.

How do I eliminate picture interference?

Every time a cassette is loaded the video recorder will automatically set the correct tracking position. For recordings made on another video recorder you may be able to improve on the automatic setting as follows:

● Press the PLAY button on your remote control.

Press the <u>TRACKING</u> button on your remote control.

Press the PLAY* button as soon as the playback quality is at its best. This setting will remain until you remove the cassette.

THAT KIND

Allocating channel numbers (Programme

Automatic channel search

The video recorder will search for all TV channels at the same time.

Switch on the TV set.

Many TV sets switch automatically to the programme number for the video recorder at step ②. However, this will only function if your video recorder is connected to the TV set with a scart cable. Otherwise, select the video programme number on your TV set.

With the video recorder switched off, press the <u>[AUTOSTORE</u>] button on your video recorder for a few seconds. The automatic channel search function

starts. 'AUTOSTORE' appears in the display.

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- Wait until all the TV channels have been found. This can take several minutes.
- All the TV channels have been found.

 If the video recorder recognizes that the TV set has been connected via a scart cable, the allocation of the TV channels will begin automatically.

 Otherwise, the automatic channel search will stop here. Then read further in the next section.
- 'SELECT TV P 01' appears in the display.

for the video recorder. The video recorder compares the TV channels on the TV set and the video recorder.

If the video recorder has the same TV channel (e.g.,

Confirm with the OK button on the remote control

If the video recorder has the same TV channel (e.g.: 'P 01') as the TV set, then it stores it.

- Wait until, e.g., 'SELECT TV P 02' appears in the display.
- Select, either on the TV set or with the remote control for the TV set, the next programme number, e.g.: '2'.
- for the video recorder.

 Repeat steps to until all the TV channels have

Confirm with the OK button on the remote control

been allocated.
If you wish to finish prematurely, press the LERELISCHAFT © button.
Finally, check again that all the TV channels are in the same sequence on both the video recorder and

the TV set.

You can read about how to change the sequence in the next section, 'Allocating channel numbers'.

Noto:

- * After any subsequent activation of the search function, the newly-found TV channels will be added at the end of those previously stored.
 - * You can store up to 42 TV channels.
- * When you activate the 'Automatic channel Search' function, any TIMER blocks which have been programmed will be cleared.

Allocating channel numbers (Programme Preset)

You can allocate any desired programme number to the For example, so that they are in the same sequence as TV channels stored by the Automatic Channel Search. on the TV set

- Switch on your TV set.
- However, this will only function if your video recordgramme number for the video recorder at step 🕗 Otherwise, select on your TV set the programme er is connected to the TV set with a scart cable. Many TV sets switch automatically to the pronumber for the video recorder.
- Press the PRESET ALLOG. button on your video recorder for a few seconds.

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A frequency number and 'STORE P 01' will appear in the display.

You will find a 'Frequency Table' on the last page of this Operating Manual

Use the 🔼 or 🔻 button on your remote control to select the TV channel to which you wish to allocate programme number 'P 01'.

- reception. This is the easiest way for you to allocate the same TV channels to the same programme num-* Have you used a scart cable to connect the video ton on the remote control? If so, you can switch to spond to the switch-over with the MONITOR butand fro between TV reception and video recorder recorder to your TV set and does your TV set rebers on both appliances.
- O Confirm the allocation by pressing the OK button. on the remote control. 'STORED' will appear briefly in the display.
- If you wish to delete an unwanted TV channel, press the CLEAR button.

- gramme number, 'P 02'. Repeat steps 🛭 and 🔾 until • The video recorder will now display the next proyou have numbered all the TV channels.
- When you have finished, press the PRESET ALLOC. button on your video recorder.

lected.

- * Unused (free) programme numbers cannot be se-
- * If you wish to delete an unwanted TV channel, press the Press the PRESET ALLOC. Dutton. Then, select the associated programme number and press the
- name can only be displayed when it is indeed being * By pressing the VPS button you can display the name of the TV channel, e.g.: BBC 1, while you are allocating the channel numbers. Of course, the CLEAR button.
 - and your video recorder is connected to a decoder, press the <u>DECODER</u> button on the remote control tion from now on when you select this TV channel * If a TV station broadcasts encoded programmes The video recorder will activate the decoder funcat step ② . 'DECODER' appears in the display. transmitted by the TV station.
- SECAM standard or vice versa. Press the SYSTEM * If you want to fine tune the automatic TV channel stripes on the picture in cable-TV systems. Howev-* If the picture quality is poor, e.g.: negative or roll Now you can use the +/- function to vary from the er, the picture and sound quality may deteriorate. Important: Such fine re-tuning is only necessary and useful in special cases, e.g. when there are setting (step 3), press the TRACKING button. standard, e.g.: the PAL standard instead of the button at step (1) to change the TV standard. standard value '0' within a range of +4 to -4. ing, you might have selected the wrong TV number (= this programme number).
- C)

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Manual channel search

In certain cases the Automatic Channel Search may not be able to find all of the TV channels (e.g. coded TV channels). You can then use this manual method to set the channels.

- Switch on the TV set and select the programme number for the video recorder.
- Press the TUNER button on the remote control.

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- Press the <u>PRESET ALLOC</u> button on the video recorder for more than five seconds.
- ♣ Hold down the ▼ or ► button on the remote control until you have found the right TV channel. A changing frequency number will appear in the display. If you know the frequency number of a TV channel, you can also directly enter the frequency (4 digits) with the digit buttons [0-9].

If the TV channel you have found is transmitted encoded and your video recorder is connected to a deceder, press the <u>DECODER</u> button on your remote control. 'DECODER' appears in the video recorder display.

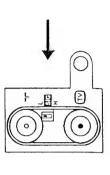
The video recorder will activate the decoder function from now on when you select this TV channel number (= this programme number).

- C Press the OK button on your remote control.
- Use the a or a button on your video recorder to select the programme number that you wish to allocate to this TV channel.

Attenuator switch - SIG

You should normally leave the attenuator switch at the back of the appliance in the $\overline{\text{LL}}$ position.

Only use the Hoposition if there is a great deal of interference when receiving TV stations with strong signals.



Auto-assembling

You can use the auto-assembling function to join individual recordings without any annoying flickering between the recordings.

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,<u>DB</u>,

- Press the PLAVE button. Search for the correct position on the tape and then stop the playback by pressing the PAUSE/STOP II Dutton. 'PAUSE' will appear in the display.
- O Now start recording as usual by pressing the <u>RECORD</u> button.



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Synchronous Editing (Synchro-Edit)

You can ed.t synchronously with a suitable connection cable between an appropriately equipped camcorder and the video recorder.

Both appliances are started at the right time with the help of a synchronized pulse and the adjustable 'switchon' (pre-roll) time.

Two different operating and cabling variations are possible. Connect both appliances when they are switched off.

Pay attention also to the operating instructions for your camcorder. The video/audio signal is transmitted via the (AUDIO) and VIDEO sockets in the video recorder. These sockets can be found at the front of the video recorder, behind a flap on the left.

- Press the REC.MODE button several times until 'EDIT' appears in the display.
- ◆ You can change the 'switch-on' (pre-roll) time with the volume of volume of the display will show, e.g. 'START 1:56' (seconds).
- Confirm your setting with the PAUSE/STOP button.
- On the video recorder, locate the right tape position for the recording. Press the <u>PAUSE ISTOP</u> button again.
- Cocate the right tape position on the camcorder.
- Press the 'PAUSE' button on the camcorder.

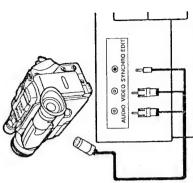
Corresponding to the above cabling, there are two ways to start editing.

Variant 1:

- Start the editing process with the <u>LAUFNAHME</u> button on the video recorder. The camcorder starts with 'PLAYBACK' and the video recorder starts synchronously with 'RECORD'.
- Stop the recording with the <u>PAUSE ISTOP</u> button on the video recorder.
- ⑤ Switch off the video recorder with the [BEREITSCHAFT ○] button.

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- Variant 2:
- Start the editing process, e.g. with the 'EDIT' button on the camcorder. The camcorder starts with 'PLAYBACK' and the video recorder starts synchronously with 'RECORD'.
- Stop the recording with the 'PAUSE' button on the camcorder.
- Switch off the video recorder with the BEREITSCHAFT 3 button.

lote:

* If the start of the scene to be edited is missing, then the setting for the 'pre-roll' time is too long. Simply set a shorter time (e.g. 1:40) as described in step ②. If the recording starts before the scene to be edited, the setting for the pre-roll time is too short. Then you must increase the set time, e.g. to

Long play function

You can reduce the recording speed by half. This makes it possible to record, for example, 8 hours instead of 4 hours on an E240 cassette.

- cording speed by pressing the SPAP button. 'LP' Before recording, select the 'LP' (=Long Play) rewill appear in the display.
- Q During playback the video recorder will automatically select the correct playback speed.

* You will obtain the best picture quality by recording at the standard speed ('SP').

What is 'Programme Delivery Control PDC)?

corder is switched on and off. This means that the video TV programme you have programmed begins earlier or recorder switches on and off at the right time even if a finishes later than expected - provided that the TV sta-With PDC, the TV station controls when the video reilon is actually transmitting PDC.

Not all TV stations transmit a PDC code.

Some notes:

- * The PDC function is only possible with the function 'Programming with TXT/VPT'
 - missing if the switch-on command sent by the TV * The first few seconds of a recording may be station is late.
- ception. When reception is poor, some programmed * PDC only functions faultlessly with good TV rerecordings with VPS may not function correctly. This is not a fault in the video recorder.

TODAY programming on the video recorder

It is very easy to programme a recording for TODAY. You only need to enter the programme number and the start Please note that only one TODAY programming is possible

The recording will be made until the end of the loaded cassette. Of course, if you activate the VPS function, the recording will be made only until the end of the selected TV programme.

Note:

- You cannot use the PDC function with TODAY programming.
- selected before programming with button [SP.LLP] on * The tape speed (standard play or long play) can be the video recorder.
 - * Make sure a cassette without recording protection is inserted.
- Press button TODAY on the video recorder.

The programme number currently selected flashes.

programme number of the TV channel from which you O Use button [=] or [±] (recorder) to select the want to record.

PROGRAM

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20:00

Use button [SELECT] (recorder) to switch VPS on or off. Press button TODAY again. The start time flashes in Use button [=] or [+] (recorder) to select the start the display. 0

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START

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=0:03

- The data are recorded in the video recorder. TODAY READY appears in the video recorder's display. • Have you entered all the data correctly? If so, press button TODAY

READY TODAY

> To finish programming, press button [STANDBY &]. ATOUTY appears in the video recorder's display.

This concludes the programming procedure.

o Io Iinish programming, press button STANDBY & and appears in the video recorder's display.

THIS IS NOT A TAULT IN THE VIGEO RECORDER

This concludes the programming procedure.

What is VPS (Video Programming System)?

corder is switched on and off. This means that the video recorder switches itself on and off at the right time even if a TV programme you have programmed begins earlier With VPS, the TV station controls when the video reor finishes later than expected. Assuming, that is, that the TV station actually transmits

PO 1 PRUSE

Not all TV stations transmit a VPS signal.

code by the indication 'VPS' that appears in the display You can see if a TV station is transmitting a VPS time in the 'STOP' or 'PAUSE' mode.

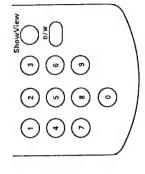
Please pay close attention to the VPS information alonge.g.: '20.15 (VPS 20.14)', you have to enter '20.14' as the Usually the start time is the same as the VPS time code. gramme's start time, a different VPS time code is given, side the individual TV programmes in your TV guide. This is because a VPS recording will only function if If, however, in the TV guide, in addition to a TV prostart time when programming the VPS time code. you set the VPS time code exactly to the minute.

* With VPS you can only programme two chronologically successive TV programmes as two separate TIMER blocks.

* VPS only functions faultlessly with good TV reception. When reception is poor, some programmed recordings with VPS may not function correctly. This is not a fault in the video recorder

Programming with ShowView

ShowView code' (3 to 9 digits) printed in your TV guide dialing a telephone number. You only have to enter the that a cassette without erase protection has been load-With this method the programming will be as easy as next to the start time of a TV programme. Make sure ed. Press the <u>[SHOWVIEW]</u> button on the remote control.



The video recorder display shows a series of dashes. To exit from this function press the CLEAR button.

Enter the entire 'ShowView code' with the digit buttons [0-9]. This code (3 to 9 digits) is found next to the start time of a TV programme in your TV guide. If you entered an incorrect code number, clear it with the CLEAR button. If you want to repeat programming at daily or weekly display shows an additional 'DLY' (= daily) or 'WLY' intervals, press the D/W button once or twice. The (= weekly). The 'daily' function can only be used for recordings to be made from Mondays to Fridays inclusive. O Press the SHOWVIEW button. The programmed code correctly, the display shows the resultant data. The is now decoded. If the video recorder has decoded time is shown in the 24 hour mode.

Alle duhhine

Note: * If the programme number flashes, e.g.: 'E 2' (e.g.: for BBC 1) with the programme number in the the video recorder cannot connect the TV channel identification contained in the 'ShowView code' video recorder (e.g.: 'P 02').

number with the PROG. +/- button and press the allocation (e.g.: BBC 1 = P 02). From now on, it will use it for all ShowView programming for this chan-SHOWVIEW button. The video recorder stores this When this occurs, select the correct programme

If necessary the data can now be changed with the buttons [DATE +/-], [PROG. +/-], [START +/-] and STOP

Use one of the two available programming methods

SAT channel has been selected.

Connect the tuner to the EXT 2 scart socket. Make

You can also programme recordings from an external

satellite tuner.

How do I record from a satellite tuner?

sure that the tuner is switched on and that the cor-

to programme a TIMER block. Use programme num-

ber 'E 2' in the programming procedure for this ex-

ternal recording source.

How do I check or correct a TIMER block?

Press the TIMER REVIEW | button on the remote

control.

If you use VPS, 'VPS' must appear in the display. Switch VPS on or off with the $\overline{\text{LVPS}}$ button.

One of the squares on the right-hand side of the display The data has been stored in a TIMER block. Programming is now complete.

Confirm the correct data with the SHOWVIEW butg S

ights up for each TIMER block that is occupied

ellite tuner)? Confirm the 'E 2' programme number * Do you want to use programme number 'E 2' in displayed in step • with the SHOWVIEW button. order to record from an external source (e.g. sat-

* if 'CODE ERROR' appears in the display, the code was incorrect or incorrectly entered. Repeat the entry or end with the <u>[BEREITSCHAFT &]</u> button.

utes are automatically added to the end time. Check * When the programmed code is decoded, 15 minquent recording. If it does, reset the end time manthat this does not overlap the start of any subseually.

* If 'SET CLOCK' appears in the display, the internal clock is not set. Set the clock.

 With 'Daily/Weekly' programming, the first recordings to be made from Mondays to Fridays inclusive. * 'Daily' programming can only be used for record-

ing must take place within a week

TIMER FERTIG

The TIMER blocks will appear in chronological order

on the display

or correct appears in the video recorder display.

Now press the TIMER REVIEW button as often as necessary until the TIMER block you want to check FERTIS

Finally, press the OK button. If you have made any changes, the data will now be up to date. 'TIMER

You can select from daily/weekly/date programming

with the D/W button.

START +/- J. STOP +/- to change the recording

date, programme number, start time or stop time.

You can switch VPS on and off

Now press the buttons DATE +/-), PROG. +/-

* With 'Daily/Weekly' programming, the first recordings to be made from Mondays to Fridays inclusive. * 'Daily' programming can only be used for recording must take place within a week.

READY' will appear in he video recorder display.

とに記れ

* With 'Daily/Weekly' programming, the first recording must take place within a week

Audio dubbing

track of an existing recording with another sound track To do this, connect an audio source (e.g.: a CD-player) This function enables you to replace (dub) the sound to the AUDIO socket.

- Press the <u>WIEDERGABE</u> button to locate the position at which the audio dubbing is to start.
- Press the PAUSE/STOP button.
- 'DUB-PAU' will appear in the video recorder display. Press the REC.MODE button (remote control).
- transmitted from the audio source. The sound track is re-recorded. The sound level is controlled auto-The video recorder will start to record the sound Press the <u>AUFNAHME</u> button. matically.
- 6 Stop the recording with the PAUSE / STOP button.
- ⑤ Switch off with the BEREITSCHAFT ⓒ button.

O Press the TIMER RC | button.

control display. Change the data in any sequence you The last data you entered will appear in the remote choose:

ZdV

- Set the date of the recording with the DATE +/button.
 - Set the programme number with the PROG. +/-
- Switch the VPS on or off with the VPS button.
 - Set the start time with the START +/- button.
 - Set the stop time with the STOP +/- button.
- remote control at the video recorder and press the 8 Have you set all the data correctly? Now point the OK button.

The data is transmitted to the video recorder. TIMER READY' will appear in the video recorder display to confirm that the data has been received.

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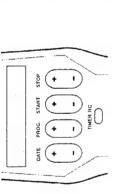
One of the squares on the right-hand side of the display lights up for each TIMER block that is occupied The data has been stored in a TIMER block. Programming is now complete.

If you want to programme more, start again at step The video recorder can store up to six programmes

How do I clear a TIMER block?

- Press the TIMER REVIEW | button on the remote control
- necessary until the TIMER block you want to clear Press the button TIMER REVIEW as often as appears in the video recorder display.
- cleared. 'TIMER CLEARED' will appear in the video recorder display.





simultaneously. To enter the data, use the four buttons

The remote control display will indicate all data

Make sure that you have not loaded a cassette with

erase protection.

+/- under the display on the remote control.

You can enter data for one recording at a time in the

remote control

Programming on the remote control

The automatic use of the TXT clock by the

To enlarge the print

How can I read TXT?

- Switch on your TV set and select the programme number you have stored for video playback
- Switch on the video recorder with button STOP
- press button 🛨 or 🗀 or the digit buttons 0-9 Now select on the video recorder the TV channel whose TXT pages you want to read. To do this, on the remote control.
- O Use button [on the remote control to switch on the TXT decoder. The video recorder is now receiv-'TXT' will appear in the video recorder display. ing the TXT of the TV channel you chose.

C. ...

2

On the TV screen you will see the first TXT survey page, which is usually the index. The TXT information line will be included on the upper edge of the TV screen.

Use the digit buttons 0-9 to enter the number of the O Now you can call up the TXT pages that you want to desired TXT page. You must always enter this number as three digits.

seconds search time, the page you wanted will appear. upper information line on the TV screen. After a few If you want to read another page, just enter the new The page number you enter appears in green in the page number.

remote control) to select directly the TXT page that you want to read. On the lower edge of the TV screen you You can use the coloured TXT buttons [] (on the will find the corresponding coloured notes.

If you want a survey of the entire range of information on offer with TXT, press button []. The TXT index defined for the TV channel then appears.

- You will then return to the normal TV channel. Switch off the TXT decoder with button
- Switch off the video recorder with button

* When you switch on TXT, most of the other functions of your video recorder are blocked. An important note:

Can I store my 'favourite TXT pages'?

For each TV channel you can store up to four TXT page page here, programming with TXT will be much easier numbers, e.g. the news headlines or the daily TV programme survey. If you store the programme survey for you.

- select the page you want to have as a 'favourite TXT Switch on the TXT as usual with button and
- rupt another page number, press the corresponding already stored. If you want to select, delete or inter-Press button SEL/ VPS . The TV screen will show you a survey of the 'favourite TXT pages' you have coloured button
- To conclude, press the green button []. You have now stored the page numbers.
- Return to the TXT mode with the blue button <a>
 \begin{align*}
 \exists \text{}
 \exists \text{}
 \tex

How can I call up my 'favourite TXT pages'?

- favourite TXT page will appear on the TV screen.
- favourite TXT pages. You can enter the page number O Use the blue or red button (to select other directly with buttons PAUSE / STOP
- Press button [] to return to the normal video recorder mode.

Switch off the video recorder with button STANDBY 6.

The automatic use of the TXT clock by the video recorder

Store a TV channel that transmits TXT on programme number 'P 1'. From now on, the video recorder takes the correct time from TXT.

Note:

* The date is not corrected automatically.

How can I record TXT subtitles?

- Switch on the TXT with button (remote control) and select the page number of the subtitles page.
- Then start the recording as usual with button

Some notes:

- * You can only record the subtitles from the TXT information. If you select a normal TXT page and press button [RECORD], the TXT information will be deleted.
- * While you are recording you cannot use TXT. You first have to interrupt the recording with button STOP \(\big| \).

Switching off TXT temporarily

You can use button alon the remote control to switch off TXT temporarily. When you press button again, the TXT will appear again on the TV screen without you having to call it up.

Fo enlarge the print

D Press button 🔳 to return to the normal video re-

You can use button () to double the print height on the TXT screen. Thus, you can, for example, still read TXT from further away.

- Press button [] the upper half of the page will appear enlarged on the TV screen.
- O If you press button all again the lower half of the page will appear enlarged on the TV screen.
- Press button [] again when you want to view the whole TXT page in normal print height.

TXT turns over pages automatically:

If a TXT page takes up more space than is available on the TV screen, this page is divided into sub-pages which are automatically turned over.

- By pressing button HOLD ♣ on the remote control you can stop the pages from being turned over, e.g., if you wish to take your time reading.
- **②** This sub-page will remain on the TV screen until you press button HOLD 中国 again. Then the pages are turned over automatically again.

Calling up concealed information:

Some TXT pages (e.g.: the quiz page) contain questions with concealed answers or information.

- Press button _____to make the concealed information appear on the TV screen.
- Press button Tonce more to make the information disappear again.

STANDBY Dutton when you no longer want to

watch television.

Switch the video recorder off by pressing the

In the last section we talked about interference that

General notes:

* There are no page numbers beginning with '0' or '9', If you accidentally select '0' or '9' as the first digit, this error will be indicated by 'P?--' on the information line.

tally press a wrong digit button, you must neverthethen can you enter the correct page number again. less complete a combination of three digits. Only * If, while selecting a page number, you acciden-* If the number of a chosen page remains green,

this means that the TV channel is not transmitting

this page at the moment.

* If you wish to change from one TXT channel to another, you first have to switch off the TXT decoder with button

whose TXT pages you want to see and switch on Then select the number of the other TV channel the decoder again with button 🗐 funer mode. Your video recorder as an extension of your TV set You can also use your video recorder as a TV receiver channels than the number of TV channels it could actuner). This is handy if your TV set does not have remote control or if it has fewer storage places for TV ually receive.

This is how you go about it:

Switch on the TV set. Select the programme number you have earmarked for playback on the video recorder.

'TUNER' ' and a programme number will appear in Press the TUNER button on the remote control. the display.

Choose the required programme number with the + or = button.

Channel name display

When you are watching or recording from TV channels that transmit the VPS signal, you can display the name of the TV channel (e.g.: 'BBC1') on the TV screen.

Press the VPS button.

* You can use this function in the Pause, Stop, Record, Programme Preset and Tuner modes.

Changing the TV standard manually

If you play back recordings made on other video recorders or if you record from an external source (such as a camcorder via the AV-Euro socket [EXT 2]), the automatic TV standard switch-over between PAL and SECAM may not always work properly. If you are recording, first select the standard of your recording source by pressing the SYSTEM button. You can also switch over during playback. The display will show for a few seconds:

'PAL' for the PAL-B,G standard Press once

'SECAM' for the SECAM-L standard Press twice

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0. 0. 7.

'MESECAM' for the MESECAM standard Press three times

 If you interrupt a recording or playback by pressing the STOP B button, the video recorder switches back to 'automatic'.

Switching the built-in modulator on or off

PCS 74696 GB

View Mode

View Mode

socket you will be able to use the following additional recorder, a decoder, a satellite receiver or a CD video When you connect another appliance to the EXT 2 functions. The appliance can be a second video player.

- signal, for example, during playback, the (switched Thus, if the second appliance transmits a control You can switch View Mode on and off with the on) video recorder will recognize it and automatically switch to 'View Mode'. MONITOR | button.
 - AV-Euro connection from the TV set to the second Even when the video recorder is switched off, the appliance is still operational.

- * If you have selected the programme number 'E 1 numbers for which the decoder function has been or 'E 2', the video recorder cannot switch over to View Mode. This also applies to programme activated.
- AV-Euro cable to connect your video recorder to the * The function reacts only if your TV set also has this switch-over function and you are using an TV set.

TV monitor function

recorder on the TV screen. Your video recorder must be (Audio/Video input) by pressing the MONITOR button. Switch your TV set to the 'AV' programme number This enables you to see the picture from the video switched on

seconds. Press the button again to switch the monitor VCR MONITOR' will appear in the display for a few function off again.

Note:

- * The monitor function will respond only if your TV set also has this switch-over function and you are using an AV-Euro cable to connect your video recorder to the TV set.
 - * The monitor button does not respond during

playback

O Press the __EJECT ▲ _] and [PAUSE / STOP II ■]

picture or sound interference using the above method

you can switch off the built-in modulator.

You can only do this if you have connected the video

recorder to the TV set using an AV-Euro cable.

could occur on the television. If you cannot eliminate

In the last section we talked about interference that

Switching the built-in modulator on or off

buttons on the video recorder simultaneously. The switch's current position will appear in the display as 'MODULATOR ON'

buttons simultaneously again for more than five seconds. This will switch to 'MODULATOR OFF' Switch the modulator back on in the same way. Press the EJECT ▲ and PAUSE/STOP II ■

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2. SERVICE TEST PROGRAM

2.1.Introduction

A service test program has been included in the software program of the micro processors. This service test program is divided into four operating modes:

- Checking the tape drive functions/control μP mask number
- Checking the sensors in the tape drive+tape drive status/deck µP mask number
- Operating hours meter/console μP mask number
- Continous running test

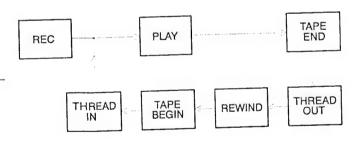
2.2 Calling the service test program

Simultaneous pressing the "STOP" key on the remote control and the "PLAY" key on the unit for at least 5 seconds calls the service test program. The display will then show the information on the display.

The service test program can be called from any operating state of the VCR other than the station search, install, set clock and cassette select. While it is operating in the service mode, the VCR remains fully operational for all tape drive functions. Pressing the "STAND-BY" key or disconnecting the unit from the mains switches the test program off.

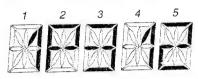
2.3. Continuous running

While in the service test program the unit can be submitted to continuous running. For this purpose, insert a cassette and select one of the following modes "PLAY", "REC" or "REWIND". The functions are then carried out continuously. This test serves to find intermittent errors. The last occuring error is stored in the EEPROM (the stored error is saved even in case of mains failure). The continuous running test is terminated by quitting the service test program.



2.7 Indication of the $\,\mu P$ mask numbers

The indication is five-figured, numbers 1, 2, 3, 4, 5.



Deck µP mask number (NTD2-1U) Digit 1:

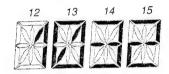
Mask indication Digit 2: Digit 3: Family (3 ... Nora 3)

Control µP mask number (NDCP1-1U) Digit 4:

Display µP version (only TXT)

2.5. Operating hours meter

The tape counter disply then indicates the number of hours the head disc has been running



2.6. Monitoring the tape deck functions

If there is no bellow mentioned signal, the unit tries to move the lift to "EJECT".

2.6.1. Threading in and threading out duration

The sensor signal used to check the threading in and out duration is (FTA) obtained from the butterfly sensor which monitors the revolutions of the threading motor.

2.6.2. No rotation of the left or the right reel disc

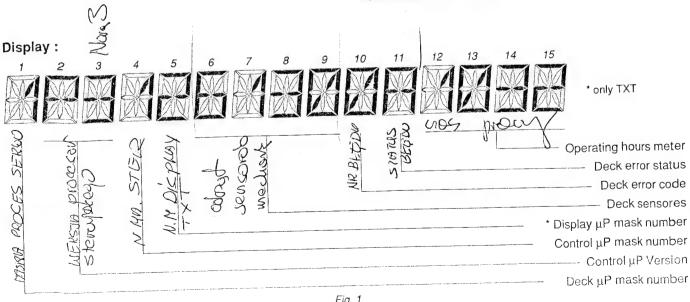
The signals sensed to check rotation are the tachometer signals from the left (WTAL) and the right (WTAR) reel disc.

2.6.3. No rotation of the head disc motor

The PG/FG signal is used for this disc rotation. It is derived from the e.m.f. of the non-current-carrying coil of the head disc motor and indicates the position of the head disc.

2.6.4. Error of the capstan motor

For this control the FGD-signal is used.



STOP + PLAY

2.7 EEPROM

2.7.1 Erasing the EEPROM

- Remove the mains supply to the VCR.
- Press the "WIND", "REWIND" and "DOWN" keys simultaneously and while the keys are held down reconnect the mains supply.

This erases and initializes all data in the EEPROM (with the exception of deck parameters and options), including the TV stations programmed by the customer. The internal processor RAM is also erased

2.7.2 "Studio like picture control" compensation (only N4)

If a new EEPROM has to be installed during a repair, it will have to be re-initialised for the "studio like picture control" feature.

- Feed in video signal via the SCART socket or the aerial.
- Insert a cassette (not a SVHS tape)
- Call up the service test program by pressing the STOP button on the remote control and the PLAY button on the machine simultaneously for at least 5 seconds. (A message such as the following will appear on the display: See fig. 1)
- Press first the PLAY button on the remote control and the RECORD button on the machine simultaneously. The machine will thread in the tape and make a recording in SP (about 4 seconds) and then another recording in LP (about 4 seconds).
- When compensation has been completed the VCR will rewind, play back the recording, and switch to STAND BY. (In the event of an error the cassette will be ejected).

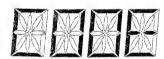
2.7.3 Initialising the EEPROM

If a new EEPROM has to be installed during repair work, it will have to be re-initialised.

Initialisation:

Call up the servicing test program by simultaneously pressing the STOP button on the remote contro and the PLAY button on the video recorder for at least 5 seconds. (A message such as in Fig. 1 will appear in the display.)

When you press the STOP button on the remote control and the PLAY button on the video recorder again, the following display will appear: $\frac{1}{2} \left(\frac{1}{2} \right) = \frac{1}{2} \left(\frac{1}{2} \right) \left(\frac{1}{2} \right)$



Enter a three-figure code (see code table) to set the right options. Confirm the code input by pressing the OK or the PROGRAMME PRESET or STORE or the CODE button. If you make an incorrect entry the video recorder will switch to stand-by.

reserve eposin +- prenale, progra CODE TABLE FOR OPTIONS:

				(1	<i></i>	
not turbo	16*9	audio dubbing synchro edit	follow me	gemstar *	showview *	2 scart	CODE
0	0	0	0	0	0	00	20
0	0	0	0	0	0	1	154
0	0	0	0	0	1	0	288
0	0	0	0	0	1	11	119
0	0	0	1	0	0	0	183
0	0	0	1	0 0 0 0	0	1	14
0	0	0	1		1	0	148
0	0	0	1	0	1		570
0	0	1	0	0	0	0	39 173
0	0	1	0	0	0	1	4
0	0	1 1	0	0	1	0 - 1	138
0	0		1	0	0	0	202
0	0	1	1	0	0	1	33
0	0	1	1	0	1	0	167
0	0	1	1	0	1	1	589
0	1	0	0	0	0	0	58
0	1	0	0	0	0	1	192
0	1	0	0	0	1	0	23 157 221
0	1	0	0	0	1	1	157
0	1	0	1	0	0	0	221
0	1	0	1	0	0	1	52
0	1	0	1	0	1	0	186
0	1	0	1	0	1	11	305
0	1	1	0	0	0	0	77
0	1	11	0	0	0	1	211
0	1	1	0	0	1	0	42
0	1	1	0	0	1	1	176 240
0	1	1	11	0	0	0	71
0	1	1 1	1	0	0	0	205
0	1	1	1	0	1	1	324
1	0	0	0	0	0	0	96
1	0	0	0	0	0	1	230
1	0	0	0	0	1	0	61
1	0	0	0	0	1	1	195
1	0	0	1	0	0	0	259
1	0	0	1	0	0	1	90
1	0	0	1	0	11	0	224
1	0	0	1	0	11	1	343
1	0	1 1	0	0	0	0	115
1	0	1	0	0	0	1	249
1	0	1	0	0	11	0	80
1	0	1	0	0	1 1	11	214
11	0	1	1	0	0	0	278
1	0	1	1	0	0	1	109
1	0	1	1	0	1	0	243
1	0	1	1	0	1	0	362 134
1	1	0	0	0	0	1	268
1	1	0	0	0	1	0	99
1	1	0	0	0	1	1	233
1	1	0	1	0	0	0	297
1	1	. 0	1	0	0	1	128
1	1	0	1	0	1	0	262

not turbo 16*9 audio dubbing synchro edit follow me gemstar * showview *									
1	1	0	1	0	1	1	381		
1	1	1	0	0	0	0	153		
1	1_	1	0	0	0	1	287		
1	1	1	0	0	1	0	118		
1	1	1	0	0	1	1	252		
1	1	1	1	0	0	0	13		
1	1	1	1	0	0	1	147		
1	1	1	1	0	1	0	281		
1	1 1 1 1 0 1 1 400								
THE GEMSTAR REPAIR CODE MAY ONLY BE USED ON GEMSTAR MACHINES! (COPYRIGHT ON MODEL PLATE)									

1 0 1 0 1 1 0 10 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	not turbo	16*9	audio dubbing synchro edit	follow me	gemstar *	showview *	2 scart	CODE
1 0 1 1 1 0 0 49 1 0 1 1 1 0 1 32 1 0 1 1 1 1 0 46 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	1	0		0	1	1	0	10
1 0 1 1 1 0 1 32 1 0 1 1 1 1 1 0 46 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1<	1	0	1	0	11	1	1	144
1 0 1 1 1 1 0 46 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 </td <td>1</td> <td>0</td> <td>11</td> <td>1</td> <td>1</td> <td>0</td> <td>0</td> <td>496</td>	1	0	11	1	1	0	0	496
1 0 1 1 1 1 1 59 1 1 0 0 1 0 0 64 1 1 0 0 1 0 1 190 1 1 0 0 1 1 0 29 1 1 0 0 1 1 1 1 160 1 1 0 1 1 0 0 519 1 1 0 1 1 0 1 340 1 1 0 1 1 1 0 480 1 1 0 1 1 1 1 31	1	0	1	1	1	0	1	327
1 1 0 0 1 0 0 64 1 1 1 0 0 1 0 1 19 1 1 0 0 1 1 0 29 1 1 0 0 1 1 1 16 1 1 0 1 1 0 0 51 1 1 0 1 1 0 1 34 1 1 0 1 1 1 0 48 1 1 0 1 1 1 1 31	1	0	1	1	1	1	0	461
1 1 0 0 1 0 1 19 1 1 0 0 1 1 0 29 1 1 0 0 1 1 1 16 1 1 0 1 1 0 0 51 1 1 0 1 1 0 1 34 1 1 0 1 1 1 0 48 1 1 0 1 1 1 1 31	1	0	1	11	1	1	1	595
1 1 0 0 1 1 0 29 1 1 0 0 1 1 1 16 1 1 0 1 1 0 0 51 1 1 0 1 1 0 1 34 1 1 0 1 1 1 0 48 1 1 0 1 1 1 1 31	1	1	0	0	1	0	0	64
1 1 0 0 1 1 1 16 1 1 0 1 1 0 0 51 1 1 0 1 1 0 1 34 1 1 0 1 1 1 0 48 1 1 0 1 1 1 1 31	1	1	0	0	1	0	1	198
1 1 0 1 1 0 0 51: 1 1 0 1 1 0 1 34: 1 1 0 1 1 1 0 48: 1 1 0 1 1 1 1 31:	1	1	0	0	1	1	0	29
1 1 0 1 1 0 1 34 1 1 0 1 1 1 0 48 1 1 0 1 1 1 1 31	1	1	0	0	1	1	1	163
1 1 0 1 1 1 0 480 1 1 0 1 1 1 1 31	1	1	0	1	1	0	0	515
1 1 0 1 1 1 31	1	1	0	1	1	0	1	346
	1	1	0	1	1	1	0	480
	1	1	0	1	1	1	1	311
1 1 1 0 1 0 0 83	1	1	1	0	1	0	0	83
1 1 1 0 1 0 1 21	1	1	1	0	1	0	1	217
1 1 1 0 1 1 0 48	1	1	1	0	1	1	0	48
1 1 1 0 1 1 18	1	1 .	1	0	1	1	1	182
	1	1	1	1	1	0	0	534
1 1 1 1 1 0 1 36	1	1	1	1	1	0	1	365
	1	1	1	1	1		0	499
1 1 1 1 1 1 33	1	1	1	1	1	1	1	330

")	
gemstar=0	gemstar off
gemstar=1, show view±1	gemstar on (show view)
gemstar=1, show view=0	gemstar on (video plus)

	(COI	YHIG	HION	MODI	ELPLA	11=)	
0	0	0	0	1	0	0	253
0	0	0	0	1	0	1	84
0	0	0	0	1	1	0	218
0	0	0	0	1	1	1	49
0	0	0	1	1	0	0	401
0	0	0	1	1	0	1	535
0	0	0	1	1	1	0	366
0	0	0	1	1	1	1	500
0	0	1	0	1	0	0	272
0	0	1	0	1	0	1	103
0	0	1	0	1	1	0	237
0	0	1	0	1	1	1	68
0	0	1	1	1	0	0	420
0	0	1	1	1	0	1	554
0	0	1	1	1	1	0	385
0	0	1	1	1	1	1	519
0	1	0	0	1	0	0	291
0	1	0	0	1	, 0	1	122
0	1	0	0	1	1	0	256
0	1	0	0	1	1	1	87
0	1	0	1	1	0	0	439
0	1	0	1	1	0	1	573
0	1	0	1	1	1	0	404
0	1	0	1	1	1	1	538
0	1	1	0	1	0	. 0	7
0	1	1	0	1	0	1	141
0	1	1	0	1	1	0	275
0	1	1	0	1	1	1	106
0	1	1	1	1	0	0	458
0	1	1	1	1	0	1	592
0	1	1	1	1	1	0	423
0	1	1	1	1	1	1	557
1	0	0	0	1	0	0	26
1	0	0	0	1	0	1	160
1	0	0	0	1	1	0	294
1	0	0	0	1	1	1	125
1	0	0	1	1	0	0	477
1	0	0	1	1	0	1	308
1	0	0	1	1	1	0	442
1	0	0	1	1	1	1	576
1	0	1	0	1	0	0	45
1	0	1	0	1	0	1	179

Reverse

Fast forward

Fast reverse

Slow

Slow

Slow

Tuner eject

Stand by eject

Index previous

not used

not used

2.8 Explanation of the error codes (see fig. 2 & 4)

The last error code that occurred is stored in the EEPROM and is saved even if the unit is disconnected from the mains. To erase this error code, press the "CLEAR" key on the remote control while in the service mode.

2.9 Tape deck status (see fig. 3)

The signal (FTA) from the butterfly sensor indicating the revolutions of the threading motor is used in conjunction with the init switch to identify the position of the tape deck. A check of the deck status is given by the two left digits of the display.

	Deck error code							
no error error of right tac								
M	threading error		blocked headdrum					
	по capstan	图	not used					
N/ W	teared tape	盟	not used					
	error of left tacho reel		not used					

		Fig. 4
		1 1g. 4
क्रा	и	
VI_	_[]	1

Deck error status

Stand by

Eject on

Stop

Still

Play

Tuner

Record

Play & Tracking

Scan forward

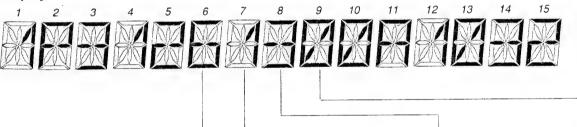
Scan reverse Wind

Rewind

Pause

Fig. 2 15 Wells 2 COURT 3 teacht

Display:



Tape de	eck status	
Eject		图 图
Stop threaded out	14 A	A H
Play position	X	圖 图
Play reverse		

Fig. 3

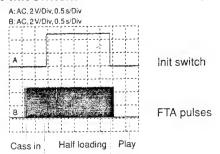
Tape deck senso	ors
Left winding tacho	(+1/-1)
Init switcher	(+2/-2)
Threading tacho (FTA)	(+4/-4)

Tape deck sens	ors
End of tape detection	(+1/-1)
Begining of tape detec	tion
	(+2/-2)
Record protection	(+4/-4)
Right winding tacho	(+8/-8)

Fig. 5

Function of the Init switch:

The diagram shows the function of the Init switch dependent on the tape deck position. The number of FTA pulses is important for the position of the tape deck.



Cass down Threaded in

2.10. Checking the sensors (see fig. 3 & 5)

The indication of the deck sensor control is four-figured. The output of the bits on the display is hexadecimal (0, 1, 2, 3, 4, 5, 6, 7, 8, 9, A, B, C, D, E, F). Several sensors are indicated per bit. If one of these sensors is activated, the value will change by the increments described below, e.g. tape end (TAE) by +1 or -1.

SERVICING OF SMDs

(Surface Mounted Devices)

1. General cautions on handling and storage.

Oxidation on the SMDs terminals results in poor soldering. Do not handle SMDs with bare hands.

Avoid for storage places that are sensitive to oxidation such as places with sulfur or chlorine gas, direct sunlight, high temperatures or a high degree of humidity. As a result the capacitance or resistance value of the SMDs may be affected.

Rough handling of circuit boards containing SMDs may cause damage to the components as well as the circuit boards. Circuit boards containing SMDs should never be bent or flexed. Different circuit board materials expand and contract at different rates when heated or cooled and the components and/or solder connections may be damaged due to the stress. Never rub or scrape chip components as this may cause the value of the component to change. Similarly, do not slide the circuit board across any surface.

Removal of SMDs

Heat the solder (for 2-3 seconds) at each terminal of the chip. Small components can, by means of litz wire and a limited horizontal force, be removed with the soldering iron. They can also be removed with a solder sucker (see Fig. 1A) or

While holding the SMD with a pair of tweezers take it off gently using the soldering iron's heat applied to each terminal (see Fig. 1B).

Remove the excess solder on the solder lands by means of litz wire or a solder sucker (see Fig. 1C).

Caution on removal:

When handling the soldering iron, use suitable pressure and be

When removing the chip, do not use undue force with the pair of tweezers.

SOLDERING IRON SOLDER WICK B.G. A PAIR OF TWEEZERS HEATING SOLDER WICK SOLDERING IRON C C CLEANING Fig. 1

The soldering iron to be used (approx. 30 W), must preferably be provided with a thermal control (soldering temperature about 225 to 250°C).

The chip, once removed, must never be used again.

3. Attachment of SMDs

Locate the SMD on the solder lands by means of tweezers and solder the component at one side. Ensure that the component is positioned well on the solder lands (see Fig. 2A).

Next complete the soldering of the terminals of the component (see Fig. 2B).

MOUNTING

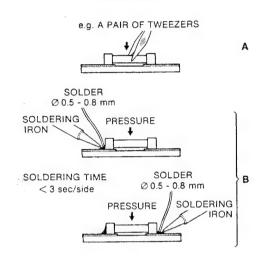


Fig. 2

Caution on attachment:

When soldering the SMD terminals, do not touch them directly with the soldering iron. The soldering must be as quick as possible; care must be taken to avoid damage to the terminals and the body itself.

Keep the SMD's body in contact with the printed board when soldering.

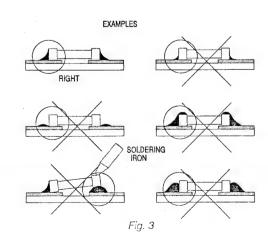
The soldering iron to be used (approx. 30 W) must preferably be provided with a thermal control (soldering temperature about 225 to 250°C).

Soldering should not be done outside the solder land.

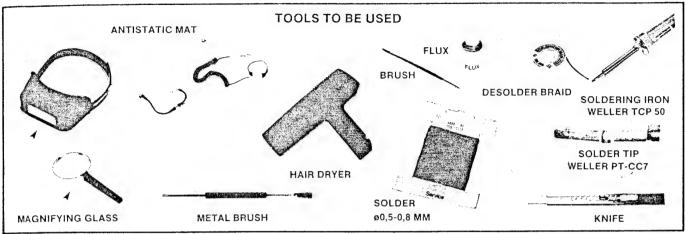
Soldering flux (of rosin) may be used but should not be acidic.

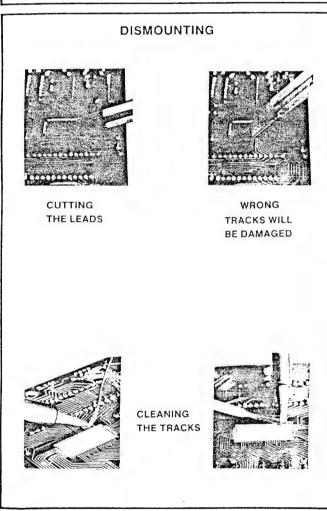
After soldering, let the SMD cool down gradually at room temperature.

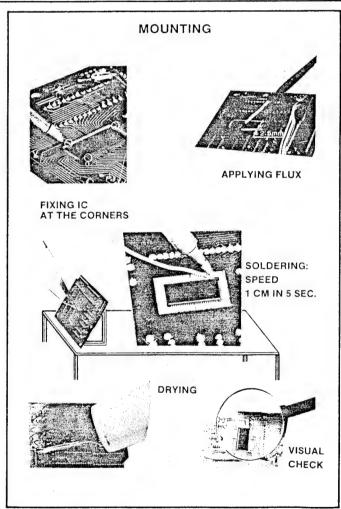
The quantity of solder must be proportional with the size of the solder land. If the quantity is too great, the SMD might crack or the solder lands might be torn loose from the printed board (see Fig. 3).

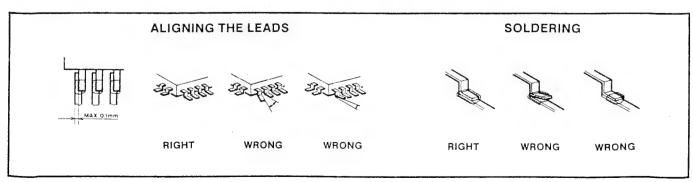


FLATPACK REPLACEMENT









С

REMOVAL OF CASE COMPONENTS AND SERVICE POSITIONS OF PRINTED CIRCUIT BOARDS

1. The casing cover

Dismontling:

- Unscrew the screws A, B, C, D, E, F and G (see fig. 1).
- Pull back the casing cover for appr. 1 cm, and when the side panels are being slightly pressed outward, the cover can be taken off.

Assembly:

- Place the front groove tightly on the front panel. Then carry out the assembly in reverse order.

2. The bottom plate

- Place the unit with the bottom side up.
- The bottom plate can be lifted off by releasing the six snap hooks (see fig. 2).

3. The front panel

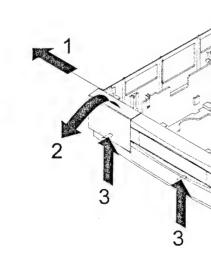
- Remove the casing cover (see point 1).
- Press the two snap hooks on the left and the two snap hooks on the right at the front outward.
- Press the front at the top slightly forward, release the 3 snap hooks at the bottom side of the front and pull forward (see fig. 3).

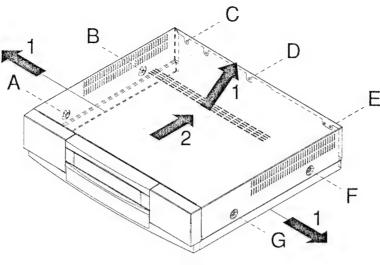
Note:

For assembly, the front panel has to be slipped on in parallel to the control print. For this purpose, the lever which serves to open the lift flap has to be pushed into the flap guide.

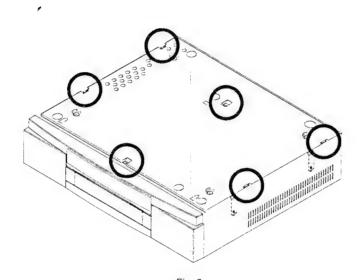
4. Power supply MSM, NSM

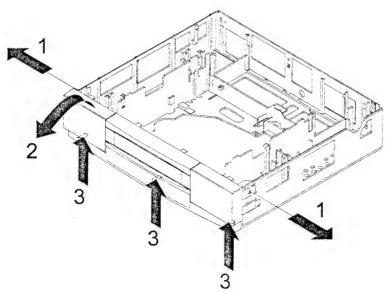
The MSM, NSM can be removed from the unit by releasing the two snap hooks (see fig. 4).











5. Control print MDC, NDC

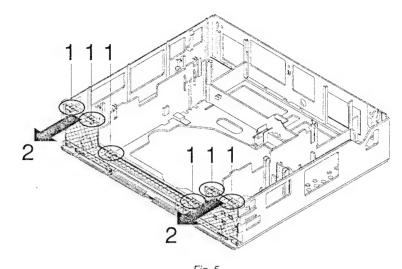
- Remove the front panel see point 3.
- The control print can be removed by releasing the snap hooks (see fig. 5).

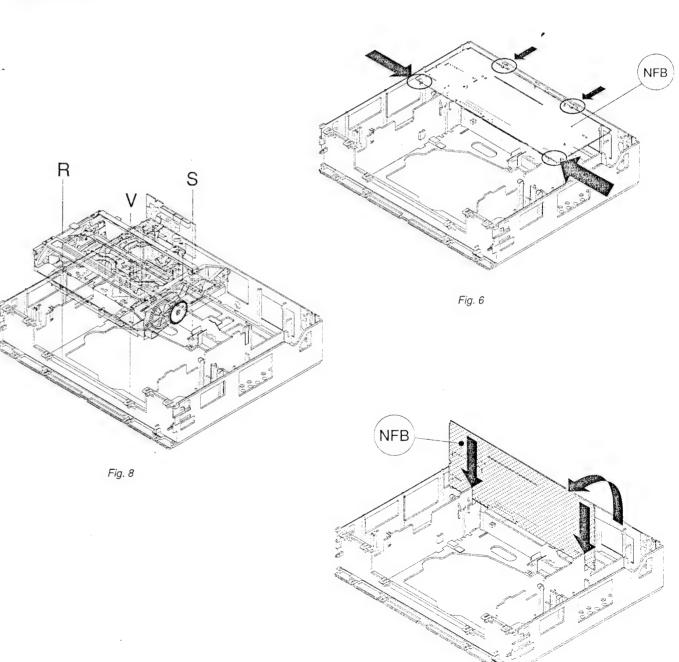
6. Family board MFB, NFB

- Release the 4 snap hooks (see fig. 6).
- Now lift the MFB, NFB turn it into the service position (see fig. 7) and place it into the slots provided.

7. The Tape deck

- Remove front panel and cover, see point 1 and 3.
- Unlock the 2 lift locks and manually move the lift 5 cm to the rear.
- Unscrew the 3 screws V,R,S (see fig. 8).
- The whole tape deck can now be removed from the frame.





LIST OF ABBREVIATIONS

+12A	+12V analog supply
+14A	
	+14V for capstan and threading mot
	+33V Tuning supply
	+5V analog for I/O SE and LHA
	+5V digital after fuse
+5V2D	
+5VAS	+5V analog after fuse
+8M1	Switched capstan motor supply
-28V	
-7V	
5VPB	
8SC1	
8SC2	
	Audio from scart 1 or 2
AEH12	Audio erase head
	Automatic frequency control analog
	Automatic frequency control digital
	Audio mono from frontend
	Automatic gain control
AIN1	
	Audio mono linear playback
AMLR	Audio mono linear record
AOUT1	Audio output scart 1
	Audio playback head
ARH	
	Blanking-pulse RGB-loopthrough
	Blue signal scart 1/2
CAP	
CHRS	SECAM record-current
CIN	SECAM chroma-signal
CKPAL	Colour-killer PAL
CLKD1	
CREV	
CROT	
	Colour system information
CSYNC	
CTL12	Control track signal
CVBS	Videosignal
DATD1	=
	Drop-out compensation ON
	Envelope comparator
ES2	
	Ext.source and PB = high
	Feature frame pulse
FG	Position info capstan
	Position info capstan digital
	FM playback video signal
	Luminance record-current
	FM record video signal
FP	
FP_PAL	
FSC	Colour subcarrier
FTA	Threading tacho
	Threading tacho digital
	Ground analog audio
	Ground analog video
GNDA	
GNDD	
GNDM1	Ground capstan motor
	Ground head drum motor
	Green signal scart 1/2
	Half line frequency on
HFHI/HFI O	Displaytube heater HI LO
HMO	
11P1	Head pulse video (audio)
HSC	
I/R	Init + record switch

ICSI3 Inverse colour system informati	io
ILEDInverse LED-tower supply	
INIT Deck switch	
INTInterrupt for display μP	
IPALInverse playback audio linear	
IPBVInverse playback video	
IRECInverse record audio linear	
LED tower supply	
LPLongplay on	
MEH1 Main erase head	
MEH2 Main erase head GND	
MES Middle east SECAM	
MODON Modulator ON	
MTA Mute audio	
NC Not connected	
PALPAL-standard	
PG/FGScanner position/-speed	
<i>PIN10</i>	
POR Power on reset	
POS Position pulse head disc	
PSSPAL or SECAM-L	
RECP Record protect	
REDRed signal scart 1/2	
REELScanner control	
REVRecord video	
SB1 SECAM band 1	
SCL SDA12C bus	
SCRTVScrambled TV	
SECSECAM-Standard	
SEL-V/H Syncpulse selection	
SH1/2Video heads	
SHCVideo heads common	
STRShiftregister strobe	
SYNC Control track pulse	
TAE End of tape detection	
TAS Beginning of tape detection	
THIOThreading in/out signal	
TMOThreading motor ON	
TMO 12Threading motor	
TRIVTracking information video	
TXTCVBSVideosignal	
V/H SYNCFrame or linepulse	
VBSVideo to SE	
VE12 Video from scart 1 or 2	
VFVVideo from frontend	
VIDOUT Video out	
VOUT1 Video output scart 1	
VSBVideo from SE	
VTXVideo output TXT	
W/RControl track write/read	
WINDWind/Rewind signal	
WTL Wind tacho left	
WTLDWind tacho left digital	
WTRWind tacho right	
WTRD Wind tacho right digital	
** ** ***	

C

C

2-12

CIRCUIT DESCRIPTIONS

KEYBOARD CONTROL UNIT

The microcomputer IC7101 is the heart of the keyboard control unit and takes over the following functions with the corresponding function groups:

- · Evaluation of the keyboard matrix.
- Decoding of the remote control commands from the infrared receiver IC7103.
- Quartz clock
- Integrated RAM for storing the timer data.
- · Driving the display data communication between
- Bi-directional serial interface for the keyboard control computer and the sequence control computer
- 1²C bus interface (SDA Pin 79, SCL Pin 23) to the EEPROM, IC7412, on the chassis board. It is also used as a serial data bus output in connection with STROBE Pin 27.
- Generation of the tuner tuning voltage by pulse-width modulation at Pin 80 (5V level) for coarse tuning with 8-bit resolution (VST-sets).
- Generation of the tuner fine tuning voltage with 6-bit resolution and band selection (2 bits) in connection with the serial interface SDA, and STROBE.
- The drifting of the tuner or the aerial signal generates the AFC control voltage in the front end circuit on the chassis board. This voltage is supplied to Pin 18 and the keyboard control computer readjusts the tuner tuning voltage.
- In the case of power failures < 7h the 0.22 F gold capacitor C2999 at Pin 33 supplies the clock and the RAM. The diode D6099 prevents C2999 from discharging. During this period a LOW level exists at Pin 2 so that further functions of the IC are switched off by the system quartz Q1001 at Pin 13 / 14.

SWITCH MODE POWER SUPPLY MSM, [NSM]

The power supply is designed for two alternative layouts (MSM, NSM)

In both vernsions, the power transistor can either be integrated in the driver IC (SPH 4690) or it can be external (TDA 4605). Either the MSM or the NSM can be installed. This description refers to the MSM version with an external power transistor (TDA 4605). Components in square brackets (e.g. [3619]) refer to the NSM version.

Typical data:

Mains voltage: 175[196] - 265[265] V_{rms} / 45-65 Hz
Max. power: 40 W
Switching frequency: 20[30]-120[220] kHz
all outputs are short circuit protected
efficiency 78% at max. load

Function description (blocking oscillator principle):

During the forward phase energy is transferred from the mains into the transformer. This energy is then supplied to the load during the off-time of the switching transistor. By control of the switch-on time the energy which is transferred in each cycle is regulated so that the output voltages are independent of load or input voltage variations. The power transistor is controlled by the by the integrated circuit TDA4605[Y7005].

Description of different load-conditiones:

NO LOAD :

The SMPS works in **burst mode** (polling operation mode). That means, it will start up. After some cycles the SMPS is switched off because the output-voltage becomes too high. After the output-voltage has been reduced the SMPS will start up again

REGULATION-RANGE:

The switching frequency is reduced with increasing load. The duty cycle is mainly controlled by the mains voltage. The output-voltage is not very much load controlled.

POINT OF REVERSAL:

At this point of the output characteristics the transferred power is at maximum

OVERI OAD

The SMPS also works in **burst mode**. The energy in each cycle is limitted so that the output voltage is reduced.

Circuit description:

C2114[2030] becomes charged via R3112[3052, 3054, 3056, 3058] and R3119 and provides the supply for the start-up phase of IC7110[Y7005/Y7007]. After this start-up phase the supply is provided by the transformer's winding 3-4[1-9] via R3127[6027], D6115. BUZ90A[Y7007]/[7035extern!] is the switching transistor of the SMPS. The inductivity of the primary winding 6-9[1-9] determine the system frequency of the circuit.

During forward phase the switching transistor is conductive, and current will flow from the positive supply at pin 6 through the transformer's primary winding and the transistor to ground. As the positive voltage at pin 6[7] of the transformer is constant, the current will increase linearly and create a ramp dependent on the mains voltage and the inductance of the primary winding. A certain amount of energy is stored in the transformer in the form of a magnetic field. The polarity of the voltages at the secundary windings are such that the diodes are non-conducting. Pin 2[2] of the IC sources a constant current during the switch on time. This current charges C2118[2015] and creates a sawtooth voltage which represents the primary current. At the same time the voltage is checked and the switching transistor is turned off when the voltage reaches a certain value which is dependent on the regulating voltage on pin 1[1]. The values of C2118[2015] and R3122[3011] are chosen to ensure that the transformer core cannot become saturated.

When the switching transistor is switched off energy is no longer supplied to the transformer. The inductance of the transformer now tries to maintain the current which has been flowing through it at a constant level (v=L*di/dt). The polarity of the voltage from the transformer therefore becomes reversed. This results in a current flow through the transformer's secondary winding via the diodes and electrolytic capacitors and the load. This current is also ramp shaped (but decreasing).

When the whole of the energy stored in the transformer has been supplied to the load and the magnetic field has disappeared, the voltage from the secondary windings falls bellow the output voltage - which is held constant by the electrolytic capacitors - plus the threshold voltage of the diodes. The current in the secondary winding therefore ceases. At this point the drain-source voltage of the switching transistor is not yet zero because capacitor C2120 contains a certain charge. This charge will start a sine-shaped ringing together with the transformer's self-induction. When the sine-wave passes through zero IC4605[Y7007]/[7005, Pin8] detects this at pin 8[18]. The switching transistor is now switched on again and a new cycle starts.

Regulation of the SMPS is done by altering the conduction time of the switching transistor so that either more or less energy is transferred from the mains into the transformer. The control information is derived from the reference component 7253[7085] which monitors the output voltage of the SMPS. The result is fed to pin 1[1] of the TDA4605[Y7007]{[7005] via an opto-coupler for electrical isolation. The TDA4605[Y7007]{[7005] compares the voltage against an internal reference. The resulting value shifts the reference with which the voltage at pin 2[2] (the image of the primary current) is compared.

IC7253[7085] is a reference-component with an internal 2,4[2,5] V reference voltage and a nominal/ actual-value compare circuit. C2116. R3129, R3130[3040, 3042, 3044, 2040] and D6114[6040] from a snubber network which limits the peak voltage at switch-off.

The ringing seen in both voltages and currents is caused by stray selfinductances in the transformer. Therefore a passage through zero at pin 8[18] will be ignored after T7135[Y7007]/[7035 extern!] has been switched off (4[4] μ s internally fixed). In addition R3125 and C2119 suppress over-shoots.

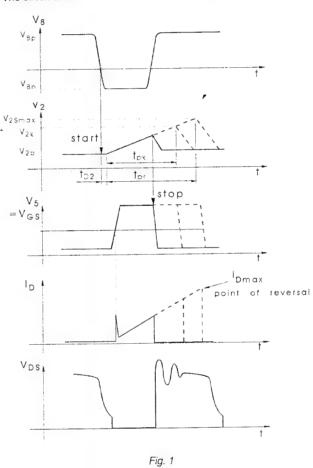
The voltage of pin 3[3] of TDA4605[Y7007]/[7005] is required for the point of reversal current which is an additional correction current for capacitor C2118[2015]. This current shortens the ontime of T7135[Y7007]/[7035 extern!] by charging C2118[2015]. The point of reversal is also stable at higher mains voltage.

Pin 7[17] is an option. By applying C2115[2023] the start-up phase will be carried out with shortened pulses so that the switching frequency is outside the audibility range.

On the secondary side there are 5 voltages present, rectified by D6201-D6209[6155-6180] and filtered by C2201-C2215[2102-2185]. In some cases two electrolytic capacitors in parallel are used to increase the ability to handle pulse currents.

Items 5203-5210[Y5123-5184] are RF-filter coils which block disturbances caused by clock frequencies of μPs.

The circuit around coil 5103 is a mains filter.



Description of the "start-up-phase":

After mains connection, at moment t0 following voltages at the pins of TDA4605[Y7007]/[7005] are increasing: (see Figure 2)

V₆ according to half wave charge via R3112,R3119[3058,3056,3054,3052]

V₂ rising to V_{2max} (typical 6,6[6,6] V) rising to the fixed value of voltage divider R3121, R3123[3005, 3007]

Current consumption in this case is 1,6[0,8] mA. The internal reference voltage is switched on at moment t1 ($V_6=V_{6E}$). Current consumption increases to 12[12] mA max.

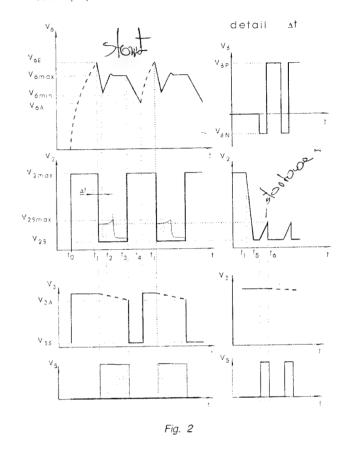
The primary current to voltage converter regulates V_2 at V_{2B} and at the moment t_5 - t_6 a start pulse is generated. Feedback at pin 8[18/8] starts the next pulse and so on.

All pulses, start-pulse included, are pulse width controlled by the regulation voltage at pin 1[1]. This voltage is in accordance with the "case of short circuit". Start up is realized by "short circuit pulses" which are extended depending on the regulation voltage.

At the moment t_2 the maximum pulse width is present (V_2 = V_{2Smax}) - TDA4605[Y7007](7005] is now in "point of reversal mode". V_2 peaks decrease rapidly because the circuit is inside the regulation range. The regulation-loop is locked.

If V_6 drops below limit value V6min before point of reversal is reached, start up will be stopped (pin 5[15/5] is switched low) and V_6 decreased to V_{6A} - IC is switched off. When V_6 increases by half wave charge (see moment t_4) a new cycle can be started at moment t_1 .

Start-up phase/ short circuit operation of SPH4690



Regulation-range, Overload and No-load:

After start up the IC is within the regulation range. Voltage at pin 1[1] is typically 400 mV. In case of an increasing load on the secondary side the switch on time will also be increased. The peak voltage value at pin 2[2] also rises to $V_{2Smax}.$ If the secondary load tries to increase again the overload amplifier would start to reduce the pulse width of V5. This point is called "point of reversal". The IC supply voltage V_6 is related to the secondary voltage value. V_6 therefore decreases with increasing load due to the reduced pulse width of $V_5.$

At the condition $V_6 \ll V_{6min}$ TDA4605[Y7007]/[Y7005] changes to burst mode (polling operation mode). Short circuit power is small, because the time delay of the half wave start up is high (from the mains frequency). In this case the overload amplifier reduces the pulse width to a certain value (tpk-mark). This minimum pulse width must be guaranteed because it is active in each start-up phase $V_{V_0} = 0.00$

With decreasing load on the secondary side the pulse width of the switch on pulse (V_5 =high) will also be decreased. Switching frequency will be increased to the system frequency of the circuit. If the output voltage is increased up to V_6 = V_6 max, the logic will be blocked and the TDA4605[Y7007]/[7005] will be in burst mode. SMPS works in open circuit operation.

Overtemperature :

TDA4605[Y7007]/[7005] includes an overtemperature circuit which blocks the logic if the chip temperature becomes too high. Start up is possible after reduction of the overtemperature.

AUDIO LINEAR - AL

Signal input for Record or EE Mode (AMLR) is pin 11 of LA7282 where it enters the ALC (<u>A</u>utomatic <u>f</u>evel <u>c</u>ontrol) stage. The signal goes via the Rec/Pb switch, an amplifier and mute stages to pin 13. This is the output pin to the I/O section (AMLP).

The attenuator chain on pin 13 sets the necessary levels for the ALC detector with its time constant on pin 10 and for the recording amplifier with the pre-emphasis components L 5601, R3616 and C 2613

The recording amplifier output is pin 17. The recording current is mixed with the bias current and passed via the head to pin 2 when the switch is closed.

In Pb mode the switch on pin 1 is closed. The PB signal is amplified in the equalizing stage (Time constant between pin 6 and pin 8) and adjusted with R 3606 to avoid influences of head sensitivity. Components 2600 and 3601 determine the head resonance during PB

In the LP mode the frequency characteristic is adapted by RC networks on pins 4, 5 and 15. The oscillator circuit oscillates at about 70 kHz is used for the erase heads and the bias current. To avoid clicks, the oscillator has to be switched on slowly (Switching stage T 7604, Time constant C 2617, R 3623, Current limiter R 3625). The record voltage for the headamplifier is generated without delay with the switch T7607.

An extra mule transistor on pin 24 switches off the output pin 13 in case of record ("Amtsblatt" requirement).

FRONT END - FV (N1) (VST-TUNING)

The receiving part consists of the following blocks

- 1.) Tuner
- 2.) IF amplifier and demodulator IC TDA 9800
- 3.) Band select and Tuning voltage generator
- 4.) 12 V Supply

1. Tuner:

The tuner family UV917/ U943 is used in the VST (<u>V</u>oltage <u>synthesized funing</u>) version that means without internal PLL circuit. The frequency range 43-158 MHz, 140-360 Mhz and 450-856 MHz can be selected on pin 7, pin 8, and pin 10. Tuning voltage input is pin 11, AGC input is pin 5.

2. IF amplifier and demodulator IC TDA 9800:

The IF out from the tuner pin 17 passes the SAW filter which type and frequency response depends on the TV system. The TDA 9800 is a PLL Type demodulator. The built in VCO operating at two times the picture carrier is adjusted by the coil 5703 and tuned internally by a varicap diode. The loop filter is connected to pin 6. The VCO control voltage is used for generating the AFC Voltage on pin 15.

The demodulated video signal passes internally a 12 MHz lowpass and comes to pin 13 with 1 V_{pp}. This level is controlled by an AGC circuit with internal reference level. The sound carrier is then suppressed in the trap 1722 and video is then available at pin 7 with 2 V_{pp}.

The Sound IF is filtered in the bandpass 1723 and / or 1724, and goes to the input of the adjustment free FM PLL sound demodulator. The audio signal output is at pin 9 with about 350 mV with a deviation of +/27mVpus.

mV_{RMS} with a deviation of +/ 27mV_{RMS}.

The operating point of the Tuner AGC can be adjusted with Resistor 3724 to obtain a good signal to noise behaviour together with optimum large signal behaviour.

Additionally the AGC voltage is fed to an analog input of the TVC microcontroller who sends an information about the signal strength to the microprocessor on the front panel. This is done for determining the order of storing the programs in autostore mode.

3. Band select and Tuning voltage generator:

The tuning voltage and band select are controlled by the microprocessor on the MDC, NDC panel. The interface circuit uses

a levelconverter to transform the 5 V logic to accurate 12 V levels and a shift register for seriell to parallel data conversion. Following control lines are used: SCL, SDA, and STROBE controls the Shift register and the PWM is a pulse width modulated signal with 4 kHz and 8 bit resolution. 6 bits of the shift register output are used for an D/A converter with a R/2R network (pos 1701). 2 bits are used for the band select logic to switch the tuner between band I III and U.

Remark: The tuner U 943 operates only in the UHF band so it does not need any bandselect logic.

To obtain the necessary resolution of the tuning steps of 60 kHz, 14 bits resolution of the D/A converter is made by adding the 6 bit of the R/2R network (LSB's) with the filtered 8 bit PWM signal (MSB's). The filtering is done by an active filter with about 15 dB suppression of the fundamental and amplified by about 3.5 to get the full tuning voltage range up to 28 V.

4. 12 V Supply:

The 12 V regulator is stabilized by a TL 431 regulator and is short circuit protected due to its fold back characteristic. Start up capacitor is C2750. The high stability and accuracy is needed for the performance of the tuning voltage. Furthermore this regulator supplies also the linear audio circuit and the record stage of the head amplifier.

FRONT END - FV (N2/3/4/5) (PLL-TUNING)

The front end is designed to receive:

N2:	PAL BG PAL I	= /01 = /05
N3/4/5 :	PAL BG PAL I SECAM LL SECAM LL/PAL BG	= /01 = /05 = /19 = /39

The receiving section consists of the following areas:

1. Tuner :

In N2 the tuner UV916E for /01, and the U943 for /05 both with internal PLL circuit are used.

In N3/4/5 the tuner UV 916E for /01,/19 and /39 and the U 944C for /05 both with internal PLL circuit are used.

In case of SECAM L' the intermediate frequency of the vision carrier is 33.9 MHz, that is why the AFC circuit has to be switched from 77.8 MHz to 67.8 MHz.

The surface wave filters for /19 and /39 have 2 Nyquist slopes. So both signals with 33.9 MHz and 38.9 MHz-SC are correctly offered to the demodulator-IC (TDA9802).

2. IF amplifier and demodulator IC:

The IF out from the tuner pin 17 passes via the SAW filter to the 3-stage IF amplifier.

The TDA 9800-9803 is a PLL - type demodulator. The built in VCO operating at the double vision carrier frequency is adjusted by the coils (AFC-Adj.) which are internally tuned by a varicap diode. The loop filter is connected to pin 6. The VCO control voltage is used for generating the AFC Voltage on pin 15.

The demodulated video signal is passed via a 12 MHz low pass filter to pin 13 with a level of 1 V_{pp} . This level is controlled by an AGC circuit with an internal reference level. The sound carrier is then suppressed in the trap and video is available at pin 7 with a level of 2 V_{pp} and after a voltage divider and emitter follower as VEV at V_{pp}

VFV at 1V_{pp}. The Sound IF is filtered in the bandpass Pin 13 and is passed to the input of the adjustment free FM PLL sound demodulator Pin 11. The audio signal output at pin 9 is approximately 350 mV_{RMS} with a deviation of +/-27 mV_{RMS}. The operating point of the Tuner AGC can be adjusted with AGC-

The operating point of the Tuner AGC can be adjusted with AGC-Adj. to obtain a good signal to noise behaviour together with optimum large signal behaviour.

The AGC voltage Pin 13 is also fed to an analogue input of the TVC micro controller which sends signal strength information to the

microprocessor on the front panel. This is done to determine the order of program storing in the autostore mode.

3. AM demodulator IC TDA 9830 :

(only N3/4/5)

In case of SECAM L the amplitude modulated sound carrier (32.4 MHz) arrives at pin 2 of the SAW filter L9453 and after selection is passed to the AM demodulator TDA 9830.

In case of SECAM L' because of exchanged PC and SC the sound carrier is at 40.4 MHz.

The control signal SECAM BAND 1 (SB1) is diode switched to pin 1 of L9453 the 40.4 MHz BPF.

The demodulated signal is passed to the integrated switch which in multi-standard versions selects between FM sound and AM sound. The selected signal is available on pin 8 (AFV).

4. Stabilization for the 12 V supply:

4.1 12 V supply (N2):

The 12 V regulator is stabilized by a TL 431 regulator and is short circuit protected due to its fold back characteristic. Start up capacitor is C2750. The high stability and accuracy is needed for the performance of the tuning voltage.

This regulator also supplies the linear audio circuit and the record stage of the head amplifier.

4.2 12 V supply (N3/5):

The output voltage is specified with 12 V +1.0/0.6V for a maximum load current of 400 mA. To ensure an uniform current division between the two series pass transistors BC636 (T7793, T7790) connected in parallel, a 6.8 Ω resistor is connected in series with each emitter. This reduces the effect of tolerances and temperature drift on the base emitter voltages. The circuit is short circuit protected, after a short circuit of the output voltage, reset has to be carried out by temporarily removing the mains plug. In this case, the electrolytic capacitor 2790 charges to start the circuit.

VIDEO SIGNAL PROCESSING -VS, -VSIO

N1/2: MF.. - VSIO NF.. - VSIO

N3/5: NF.. - VS MF.. - VS

N4: NF.. - VS

1. General:

Boards with extension /39 are for PAL/SECAM L.

Heart of the circuit is the IC LA7191 containing all luminance PAL chroma and SECAM BG chroma circuits in 42 pin shrinked dil case.

For the SECAM L processing the well known TDA4725 is used. The SECAM BG detector is the LA 7311 discriminator. CCD 1H delay line is the MSM 7403RS with 5 V only supply.

2. In/Out (N1/2):

The I/O circuit makes the selection between two signal sources, the scart input and the frontend. Audio and video signals are switched in the IC 7551. It is controlled by ESPBH and disabled via pin 6 in Play back mode. Scart 1video input is pin 20. The signal passes then the switching diode pos 6509 to pin 3 IC 7551. Scart 1 audio inputs are the pins 2 and 6. Left and right channel are added and fed to pin 2 IC 7551. Zener diodes on all inputs are used for ESD protection purpose. Capacitors are for Amtsblatt requirements. Frontend video comes from pin 7 IC 7702 via an attenuator and an emitterfollower to pin 5 IC 7551. Both the emitterfollower and the diode 6509 are biased by resistor 3513 if they should be on.

Frontend audio output signal from pin 7 IC 7702 (about 350 mV_{rms}) goes via a deemphasis network 3505/2500 to pin 2 IC 7551.

Video output emitterfollower is 7502 which drives pin 19 scart 1 and if necessary the RF Equalizer. The modulator may be switched on by the TVC microprocessor and T7500.

3. Record signal path (N1/2/3/4/5):

3.1 Luminance:

Pin 37 is the input of the video signal with about $1V_{pp}$. It is then controlled by an AGC amplifier (adjustment via pin 39, time constants pin 38 and pin 16), passes a 6 dB attenuator, a 3.5 MHz low pass filter, a clamp, some switches in the noise canceller/dropout compensator part and is output via an amplifier on pin 3. The signal then goes to an emitter follower, a low pass filter and a second emitter follower to pin 4. You have to adjust the E/E Level pot to obtain $0.5V_{pp}$ on this pin with a 100% white picture. This is necessary for the right values on pin 34 video out and the values of the white and dark clip levels.

Following the signal on pin 4 now without chrominance components it passes a clamp, a detail enhancer (time constant pin 8), a nonlinear emphasis (time constant pin 7, on/off is controlled by the DC level on pin 7) and the main emphasis with internal white and dark clip (time constants between pin 5 and 6).

The signal then goes via the deviation potmeter to pin 42 of the input of the FM modulator. The FM is then filtered, adjusted by the FM record current pot and goes to the summing stage and the bead amplifier.

The loop through path outputs the signal via a feedback clamp and an insert stage (control pin 33) to pin 34 and via an emitter follower to the I/O part of the VCR.

3.2 Chrominance PAL:

After the in 3.1 mentioned 6 dB attenuator the signal also comes to the 4.43 Mhz bandpass filter, an ACC (<u>A</u>utomatic <u>C</u>hroma <u>C</u>ontrol time constant pin 14), the main converter, a 1 Mhz low pass filter, a killer stage to pin 15 and via the chroma record current to the summing stage.

The 5.06 MHz for the main converter comes via the 5.06 MHz bandpass filter from the sub converter where 4.43 MHz from the VXO and 627 kHz from the Line PLL is mixed. The Line PLL is locked to the composite sync pulse from the sync separator. It uses a 321 x HH VCO (Loop filter on pin 23 and 24). The frequency is then divided by 8 in 4 different 90 degree shifted phases as it is necessary for the VHS standard. Phase shift control input is pin 41 which is also an SP/LP-input. The Line PLL part also produces the Burst Gate Pulse BGP. The VXO is locked to the incoming burst signal via the record APC detector (Loop filter pin 17).

This IC uses a special crystal for which no adjustment is necessary. An additional frequency doubler with the output on pin 21 supplies the 8.86 MHz for the CCD. The H/2 frequency is taken from pin 17 (only for N3/5). It is the information about the phase of chroma for making color inserts on teletext boards possible in the correct phase (option).

3.3.1 Chrominance SECAM BG:

Pin 27 H forces the IC to SECAM BG mode.

- Phase rotation off
- VXO fixed frequency
- · filter charcteristicof bandpass more wide
- The SECAM BG detector LA 7311 generates this switching voltage (only for N1/2).

3.3.2 Chrominance SECAM L (N3/4/5):

(see circuit description CSP)

4. Playback signal path:

4.1 Luminance:

The FM playback voltage goes via the necessary filters to pin 39 of IC 7051.

The FM then goes to a double limiter stage, a FM - demodulator and a sub low pass filter. Pin 3 has a high impedance in play so the connected R/C components act as a linear deemphasis. Pin 2 allows a correction of frequency response and the adjustment of

C

the YPB Level. Measuring point for this level is the output pin 34 while you play back a standard 100% white recording.

After correction of the frequency response in the external low pass filter now switched by the LM339 to a slightly different characteristic, the video passes, via pin 4, the 3.5 MHz low pass, the noise canceller and dropout compensator part. For both functions the 1 H CCD is necessary. Pin 12 supplies the video to the CCD and pin 10 receives the signal where a Voltage Controlled Amplifier VCA adjusts automatically the gain tolerances of the CCD. For this function the capacitor of pin 9 is important.

You can check CCD function by connecting pin 2 to 5V in E/E mode and then measure on pin 32 the difference signal of 2 lines. After the noise canceller the video passes the nonlinear deemphasis (time constant pin 7 as rec), a noise canceller (time constant pin 8), the picture control stage (controlled by DC on pin 13 2V = soft, 3V = sharp), the Y/Chroma mixing stage and the video output amp to pin 34.

4.2 Chrominance PAL:

627 kHz Chroma from tape goes through a 1 MHz low pass filter and an amplifier with group delay correction to pin 15 of the IC. The chroma is amplified, controlled in the ACC amplifier, mixed with 5.06 MHz and goes via the 4.43 MHz bandpass and an amplifier to the combfilter where crosstalk components from the neighbor tracks are removed. The chroma then comes back to the IC at pin 27 where it is amplified.

Chrominance SECAM BG:

Signal path is about the same as in PAL Differences:

- 321 fH VCO locked to sync
- no phase rotation
- Comb filter off
- internal bandpass filter more bandwith
- no colorkiller function color always on

4.3.2 Chrominance SECAM L (N3/4/5):

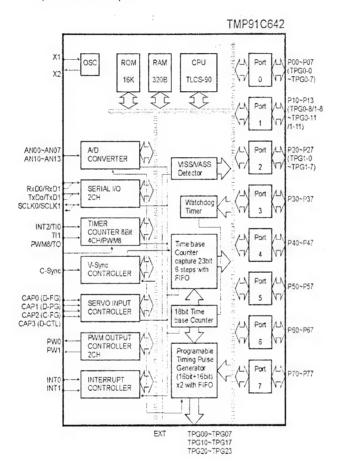
(see circuit description CSP)

DECKELECTRONIC - DE

1. General:

TVC (TMP91C642N-Maske: TMP91P642N-OTP) The TVC (Toshiba Video Controller) is a one chip microcontroller.

- 16k byte ROM
- 320 byte RAM
- 8-bit A/D converter
- 2 serielle Businterface
- 2 12-bit PWM outputs • 1 8-bit PWM output
- · Composite sync input
- spezielle Servo inputs



8 analogue inputs are available. The input signals are fed to the A/DC via a multiplexer. The resolution of the converter is 8 bit. The maximum permissable input voltage range is 0...5V (defined by the reference voltages AVSS and AVCC).

Four analogue outputs are available each with a 8 bit resolution. These outputs supply a signal of constant frequency. (appr. 21.5 kHz) with variable pulse/interval ratio. It is possible to obtain a resolution of 14 bit by software. To achieve this resolution, two PWM outputs are internally linked. This means loss of one PWM output pin. The second output pin can then only be used as an ordinary circuit output. The possibility of the increased resolution is used by the headwheel drive (REEL line).

2. SAA 1310 Deck mechanics interface :

2.1 CTL-stage:

The IC SAA 1310 contains a write/read stage for the CTL track, with the possibility of interference-free overwriting of an existing CTL track (e.g. if another index code (VISS or VASS) is to be written on the tape). The playback stage is equipped with a "digital", two-stage AGC. By comparators, this circuit logic identifies the size of the output signal supplied by the CTL head and then selects the best amplifier gain in the playback stage. Note: The playback signal from the head follows the law of induction $(d\Phi > /dt)$ and is therefore

largely - with the exception of the gap function - proportional to the tape speed. The CTL head voltage can therefore vary considerably from vmax when FAST SEARCH (index search) is used to vmin when the LP mode which has the slowest tape speed. The highest speed sets in with FAST WIND or FAST SEARCH (Index search). To ensure that under the above mentioned conditions the pulse/interval ratio of the tape sync is always correctly reproduced (it is important for the distinction of 1 and 0 for VISS or VASS marks), the amplifier must not be overdriven. The two-stage AGC alone cannot process the large dynamic range of the input voltage. The amplifier is therefore provided with an internal low pass feature (fg = 3 kHz typ.) and also the amplification is further reduced for all winding modes by means of transitor 7403. In this case, the signal WIND = low and T7403 is disabled. The transistor is deliberately polarity inverted, as the inverse operation has the better damping qualities for this application. If T7403 is disabled, the amplifier's feedback network, as well as the external resistor 3454 is also disabled. Alternative short-circuit of R3454 with T7403 permits to reduce the amplification in the proportion

g on / g off = 1 + R3454 / 100

In parallel to the CTL head, there is the R.C. circuit of C2411 (4n7) and R3453 (4k7). Together with the CTL head inductance, the capacitor causes a resonance step-up at approx. 8 kHz. The R3453 causes a steep fall of the frequency transmission characteristic beyond resonance, providing an effective suppression of high-frequency stray pickup. The CTL head signal amplitude in SP is approx. 1...2 mVpp. Therefore the gain of the playback amplifier has to be suitably high. To avoid offset problems, a 47 µF electroytic capacitor (C2410) is built into the negative feedback branch for DC decoupling. Together with the internal 100 Ω feedback resistor, this electrolytic capacitor causes a high-pass behaviour. It must be of sufficient capacity to ensure that the differentiating effect is beyond a cut-off frequency, where the distortions of the signal shapes at the lowest tape speed rates remain negligible. Otherwise overshoots occur after each change of magnetisation on the tape, resulting in faulty triggering of the internal logic and therefore in faulty sync signals. The polarity of the playback amplifier can be reversed with the Capstan-Reverse (CREV) voltage. This enables the TVC always to see the correct sync edge independent of the tape feed direction. The W/R (Write/Read) signal is used to change between record and playback.

W = high and R = low.

The SYNC line (pin 16) is a bidirectional line. In the case of write, a square-wave signal of 40 ms period is output by the TVC . The recording amplifier in the SAA 1310 converts this voltage into a square-wave recording current of approx. +/-2mA. Pin 3 of the SAA 1310 is the buffered output of the IC internal reference (2,5V +/-

2.2 POR (Power On Reset) - Generator :

The POR generator contained in the SAA 1310 requires only one external component. This is the capacitor 2414 which defines the length of the POR pulse. At 33 nF, t por is approx. 30 ms.

2.3 The sensor interface:

The four comparators in the SAA 1310 are used to convert sensor signals to logic levels. Two of these comparators have open collector outputs (pin 11 and 13), which can source a current of 100 mA. The outputs are overload protected by a current limiter and thermal shutdown.

Only the non-inverting input of each comparator is accessible from the outside. The other inputs are connected to the internal reference voltage of nom. 2,5 V. Also the hysteresis of the comparators of approx. 10 mV is set internally.

The following sensors are evalutated:

Comporator 1 (In Pin 5 Out Pin 15):

FTA threading tachometer. This signal comes from a forked light barrier in the deck. An infra-red light beam is interrupted by a fourblade impeller (butterfly). The output amplitude of the sensor has to have a minimum variation of between 2V and 3V to ensure correct evaluation. By means of R3451 and R3449, an additional hysteresis is obtained.

Comporator 2,3 (In Pin 6,7 Out Pin 14,13):

WTR/WTL (Winding tachometer right/left) comes from reflected light beams. The minimum output requirement is the same as the

Comporator 4 (In Pin 8 Out Pin 11):

FG = Capstan tachometer. This signal comes from the sensor print on the turbo drive via the pre-amplifier of the tacho-HALL sensor in the motor unit. The amplitude of the almost sine-shaped signal is typ. 1 Vpp. The minimum acceptable level is 300mVpp. The signal is AC-coupled via C2415. Therefore the input at pin 8 is connected to the reference voltage at pin 3 via the resistor 3452. The capacitor C2413 in parallel with 3452 is to remove HF noise.

3. Interface to the headwheel motor driver:

The connection to the HMO driver IC on the LHA print is via plug 1915. The speed regulating signal is

BEFL he resolution is 14 bit. PG/FG is the combined POS/tachometer signal of TDA5140 (HMO driver).

TRIV (Tracking Information Video) is the envelope information from the head amplifier.

It goes into one of the analogue inputs of the TVC . The current drawn from the +14M2 under ambient temperature conditions is typ. 70mA. During the run-up period of the motor approx. 0.5 A flow for a short time.

4. Interface to the Capstan motor:

4.1 Motor driver interface :

The Capstan motor in the turbo drive is driven via plug 1913. CAP is the Capstan speed signal, which can vary without load between 0 and 5 V. By means of CREV (Capstan reverse) the direction of the motor rotation is changed. The signal is fed via a diode to the motor driver, thus effectively preventing latch-up (otherwise the current limiting will fail). The maximum current consumption of the motor is limited to 1 A. Typical values in PLAY mode are 0,2 ...0,3

4.2 Voltage switch for capstan motor:

Due to the wide speed operating range of the deck mechanics a large power operating range is required of the CMO driver, in the DD-capstan motor module in the deck. To avoid high power dissipation in the circuit, 2 different voltages are provided by the power supply for the CMO, a 14,5V (typ) for the Modes where faster tape movement is required and a +8M2 for PLAY. The switch selection of the voltage is controlled by the WIND sigani and the switching transistor T7406.

4.3 Tachometer preamplifier :

The Tachometer preamplifier is located on the sensor print. It is a DC coupled differential amplifier using discrete components. AC and DC amplification are different to avoid problems with the offset of the Hall element.

5. Threading motor driver:

The TMO driver is provided in bridge circuit using a dual power opamp. L2722. This IC can supply an output current of +/-1A. It has diode protection (fly-back diodes) at the outputs.

Between the IC outputs (Pin 1 and 3) there is a Boucherot element (1 Ω , 100 nF) to suppress a 3 MHz spurious oscillation from the power amplifier. One half of the bridge is controlled via the TMO line and functions as comparator. The other half is an amplifierintegrator with gain 3.9 times. A variation of the input voltage (THIO) between 0 and 5V causes a voltage variation at the output between OV and nearly supply voltage. In case of a 50% modulation (THIO = 2,5V), there is approx. 7V at PIN3. The integration with C2102 serves to filter out the PWM frequency of approx. 21,5 Hz. The polarity of the comparator (noninvert.) and of the opamp (invert.) is selected as follows: In the event of a Power-On Reset, the TVC takes the THIO line "L", whereas TMO is "H". The above polarity must be observed to ensure that no current is applied to the motor during the POR pulse period. To avoid the

adverse conditions that would be applied to the motor in the event of the loss of the 5V supply a separate reference divider is provided for the comparator section. Both outputs of the L2722 are now "common-mode" in the event of the above mentioned failure.

6. Tape end - LED - control:

The LED current is controlled by transistor 7404. The ON time is approx. 1 ms. with an ON/OFF ratio of 0,09. C2404 slightly attenuates the slopes to avoid interference in the signal electronics. The LED current is at least 75 mA and supplied by the +14M1.

7. Analogue interface to the TVC:

The following analogue levels are supplied to the TVC internal A/D-C:

TRIV TAE/TAS Tracking information Video
Tape end/Tape Start Detection

I/R combined information from INIT and Record

protection

AGC Automatic Gain Control

8. Test picture generation for non TXT equipment

By means of the resistor network R3426, R3425, R3424, R3422 and the corresponding selection of lines IOFP and FFP by the TVC, the levels (Sync, black, white) for a test picture can be generated and inserted in the signal path by the signal processing IC (7051)

9. Sensing of the tape deck switch:

The contains two switches:

INIT initialisation switch RECP record protection

The states of these two switches is input with a single line (I/R) into an analogue input of the TVC (pin 57). Each switch output, the level of which can be "H" (5V) or "L" (OV), is coupled via a resistor-driver network. Each possible switch combination then provides a unique voltage level on the I/R line.

10. Version definition:

Only one ROM mask is used, therefore it is necessary to define the wanted version.

Pin 1 Longplay

Pin 24 2 or 3 Kopf (head amplifier) Pin 36 4 head yes/no (not N4

Pin 55 Pal 1 (VHF/SEC-LP (not N4)

11. 12 V supply (N4):

The output voltage is specified with 12 V + 1.0/0.6V for a maximum load current of 400 mA. To ensure an uniform current division between the two series pass transistors BC636 (T7423, T7424) connected in parallel, a 11 Ω resistor is connected in series with each emitter. This reduces the effect of tolerances and temperature drift on the base emitter voltages. The circuit is short circuit protected, after a short circuit of the output voltage, reset has to be carried out by temporarily removing the mains plug. In this case, the electrolytic capacitor 3431 charges to start the circuit.

12. EE - Prom :

An EEPROM is an electrical not volatil ROM, on which it is possible to save and delete information. The information is not lost if the mains is disconnected. The R/W cycle takes place as usual via the serial IIC-Bus SDA, SCL. It is now possible to save specific deck parameters, for example, X-distance, gap position, tuning limits (for Amtsblatt requiments) and possible also differences between TAE and TAS; left and right tolerance of the tape end light barrier (until now coupled photo transistor were used). The preset potentiometer for the gap position is no longer necessary. The adjustment occurs automatically when using a test cassette and pressing certain keys. The preset channels and some options are also saved in the FEPROM

13 . CMT - detection :

The CSYNC wire is connected with two TVC pins. One off this pins detects the 50 Hz (Pin 12 Port 33) and the other detects the 15.625 kHz (Pin 8 Port 47).

This is necessary to recognice video signals only and not other 50 Hz transmitter signals.

CSP - BOARD (N3/4/5)

Recording:

The CCVS signal (VBS) from the "IN/OUT" circuit stage is fed through the solder connection 0201 pin1 and the emitter follower T7240 to a stage for chroma selection (Q5102 / T7200). The selected chroma signal then passes through the trap circuit (L5203 / C2201 / L5204 / C2203 / R3206) to arrive at IC7520 pin25. The trap circuit increases the selective effect of the "gaussian filter circuit" (Q5102). Subsequently, the signal passes through a 15 dB amplifier and is then taken via pins 23 and 22 to a limiting amplifier with a following frequency divider. Dividing the chroma signal in a ratio of 1:4 this divider generates the necessary 1.1 MHz signal for recording which is applied to pin 19 of IC7520. The bandpass which follows then reduces the harmonics resulting from the frequency division and the signal is routed to pin 17 of IC7520. Afterwards it is subjected to a 10 dB amplifier and switched to pin 13. Between pin 13 and 12 the 1.1 MHz signal is fed through an "antigaussian filter circuit" (Q5207). The signal is limited then in IC7520 and passed via pin 15 and the solder connection 0202-1101 pin6 to the Video/Chroma circuit stage (Family Board) as "CHRS"- signal. It is then fed through an adjustment control for the SECAM chroma recording current, R3098 (CHROMINANCE WRITING CURRENT SECAM) to the junction R3098 i R3100 where the signal is added to the Y-signal. The sum signal (FMRV) is taken via the amplifier stage T7029 / T7030 and plug contact 1911 pin2 to the head amplifier.

Control of the switches in IC7520:

On recording, a LOW level (0.7V) is present at the collector of the transistor T7205. This transistor works like a diode, turns on and applies approximately 1.3V to IC7520 pin21. From this level, the following detection stage can identify the recording mode and switches all in circuit switches to record position. 3.2

Playback:

On playback, the "uncontrolled FM signals from the tape" (FMPV) is taken to pin 21 of the IC7520 and is then amplified by 6 dB. From pin 19, the signal is fed via a bandpass to IC7520 pin17. Between pins 17 and 16, the obtained 1.1 MHz signal passes through a 10 dB amplifier; via pin 14, it is fed to another amplifier in IC7520 whose feedback stage contains an "anti-gaussian filter circuit (Q5207)" which is connected between pin 12 and pin 14. In the AGC stage following the amplifier the signal undergoes an automatic gain control (AGC) and its frequency is doubled (2.2MHz) in the fullwave rectifier RECT. From IC7520 pin8 the 2.2 MHz signal is fed to the bandpass F5211 which removes disturbing harmonics from the wanted signal. In another doubling stage which obtains the 2.2 MHz signal from IC7520 pin6 a 4.4 MHz signal is generated. This signal is subsequently amplified by 10 dB and is fed to the colour killer via pin 27, the anti-gaussian filter circuit (Q5202) and pin 28. From IC7520 pin1 the 4.4 MHz signal is fed into the bandpass F5209 which separates disturbing harmonics from the wanted signal. The resulting SECAM chroma signal (CIN) is taken via the impedance converter T7203 and the solder connection 0203 pin2 / 1101 pin8 to t he Video/Chroma circuit stage (Family Board) and is then added to the CVS-signal in

IN/OUT - I/O, MSIO (N3/5)

(INCLUDING SUBPRINT MSIO)

The I/O circuit selects the different signal sources. Audio and video signals are switched by ICs (HEF 4053). Switching is controlled by the ESPBH line and on MSIO by ES2. Scart 1 video input signal VIN1 from pin 20 is passed by the switching diode (pos 6565, 6566) to pins 5 and 2 of IC7590 on MSIO.

From pin 5 it goes via pin 4 and T7560 to Video out on Scart 2. From pin 2 via pin 15, video VE12 returns to IC 7592 at pin 1 and via pin 15 is passed to the signal electronics section as VBS. Scart 1 audio inputs from pins 2 and 6 (left and right channel) are added to AIN1 and fed to pins 5 and 2 IC7591 on MSIO.From pin 5 it goes via pin 4 and T7580 to Audio out on Scart 2. From pin 2 via pin 15 audio AE12 returns to pin 13 of IC 7592 and is output at pin 14 as AMLR the audio part.

The Front end video VFV comes from pin 7 IC7720 via an attenuator and an emitter follower on the front end to pin 3 IC7590 on MSIO and then goes to Video out on Scart 2 via pin 3 and T7560. VFV returns as VTX to pins 2 and 5 IC7592. From pin 2 it is passed via pin 15 as VBS to the signal electronics section.

From pin 5 it is switched via pin 4 to a pull up resistor. The Front end audio output signal AFV from pin 8 IC 7840 on the front end goes to pin 12 IC7592 and via MSIO to pin 3 IC7591. From pin 12 IC7592 it goes via pin 14 to the audio section as AMLR. On MSIO pin 3 IC7591 can be connected through via pin 4 and an emitter follower to Audio out on Scart 2. Scart 2 video input signal on MSIO is passed via switching diodes to pin 1 and pin 13 IC 7590. From pin 1 via pin 15 VE12 goes the same way as described previously. From pin 13 it can be connected through via pin 14 as VOUT1 to pin 19 of Scart 1. Scart 2 audio inputs from pin 2 and 6 (left and right channel) are added to AIN2 and fed to pins 13 and 1 of IC7591 on MSIO. From pin 13 it arrives via pin 14 and T7540 as AOUT1. Audio out on Scart 1. From pin 1 via pin 15 audio AE12 goes the same way as described previously. Video signal VSB from signal electronics goes via MSIO as VIDOUT out to the modulator section. Also VSB at MSIO is passed via an attenuator, the emitter-follower 7509 to pin 12 IC7590. From pin 12 it can be connected via pin 14 as VOUT1 to Scart 1. VPS-Option: In this case VSB is also fed to IC7600 SDA 5642 where data from line 16 is detected and sent via IIC bus to the controller. Audio signal ALMP from the audio section is passed directly to the modulator. It is also passed to pin 12 IC7591 on MSIO and leaves via pin 14 as AOUT1 to Scart 1. There is also RGB loop through between Scart 1 and Scart 2. The linking of pin 16 (blanking) between Scart 1 and Scart 2 can be switched by T7550 using control signal SCRTV. Pin 8 (switching) of Scart 1 is controlled by 8SC1. Pin 8 (switching) of Scart 2 is controlled by 8SC2. Zener diodes are used at all inputs for ESD protection, the capacitors are "Amtsblatt" requirements.

I/O, TXT - BOARD MVIO (N3/5)

1. Controller (pos. 7000):

The controller consits of a 8032 micro processor with external 128K x 8 OPTROM and 8K x 8 RAM. The address lines to Port 0 are latched as this port supports both address and data lines. As the 8032 only supports 16 pins for address control, the highest address A16 must be generated with a "normal" Portpin. An RC circuit is used to provide a timing safeguard between the use of A16 pin as an address and as a data pin. The control processor is linked via the 1^2C -Bus with the display μP

The control processor is linked via the I⁻C-Bus with the display μ P and trough the UART-bus, in the shift register mode, with the deck- μ P. For operational speed the control processor triggers the display processor via the interrupt line (INT).

Operating in a high speed mode the controller runs all other I²C-Bus functions including the teletext decoder (SAA5246AGP/E). All non volatile data as for example programme data, source codes, preferred pages, etc. are saved in a 1K x 8 EEPROM on the familyboard.

2. Integrated Video-Processor and Teletext decoder (pos. 7200):

Within the processor, 27 MHz oscillation from the colpitts oscillator, is divided down to provide the teletext data clock of 6.93 MHz the display timing and the line frequency 15625 Hz.

The data slicer separates the teletext information from the vertical blanking interval of the video signal. The teletext data is saved in the RAM and if required converted to RGB signals in the display generator. The amplitude of the RGB signals is fixed by an external voltage divider. These RGB signals are encoded to a CVBS signal. Using the line pulse the teletext controller generates artificial Syncs for the TV (STTV). This STTV is not interlaced during full pagemode (312/312 lines). In subtitles-mode, due to association with the background picture, it is interlaced (312,5/312,5).

The BLANK output indicates at what time a teletext information is available. BLANK gives the opportunity to fade subtitles.

In order to increase the access speed of new page selection the teletext controller saves in the RAM a total of 4 or 8 pages depending on the method of execution.

3. Colour encoder (pos. 7300):

TV sets which are equiped with teletext, drive the colour picture tube from TXT-RGB signals. As VCRs do not normally provide an RGB output, and some TV sets are not provided with RGB input, a CVBS signal must be generated.

The colour encoder (MC1377) encodes this CVBS from the RGB signals, a Composit Sync. (STTV) and a 4.43 MHz oscillation (FSC). This colour subcarrier is brought into correct phase by shift circuit

The H/2 correction results from the selective amplification of a sample of the subcarrier-PLL in the signal electronics. The coil is not adjusted to the maximum amplification but for correct phase. With this generated H/2 sinewave, the encoder is synchronised via a transistor.

4. Video insertion and switching (pos. 7400):

For video insertion, the video switch BA7605N is used to clamp all the input signals to 2.0V and all the output signals to 0.6 V Sync tip. The frontend video VFV and the teletext information are offered with 1 V_{pp} to one of the 2 switches (on pin 8 and pin 10). The modified BLANK pulse inserts subtitles when available. This VTX signal is then fed to the signal electronics for recording or monitor. In case of Full page-mode the switch is blocked by FP line. For subtitles during MESECAM-standard transmissions the FP-PAL line activates a chroma bypass and turns the FSC off.

The output of the signal electronics VSB (on pin 1) and the teletext information ($2V_{pp}$ on pin 3) are connected to the second switch. This switch is directly controlled by the Full-page line (FP).

5. I/O section:

The I/O section is composed of 2 triple HEF switches; pos. 7590 for the video signals and pos. 7591 for the audio signals. The control of these switches is by the 2 lines ES-2 (external source) and SCRTV (scrambled TV).

In order to reduce the control lines, ES-2 and SCRTV each carry out two functions.

C

If an ext. source is chosen (E1 or E2), ES 2 selects either SCART-1 IN or SCART-2 IN, it also selects either the front end signal or the SCART-1 input signal to pass to the SCART-2 output.

SCRTV has the function in the decoder mode to connect the SCART-2 input to the SCART-1 output, and also permits the passive RGB-loop of through the blanking signal between SCART-2 and SCART-1.

6. VPS:

If the teletext decoder SAA5246 is exchanged with its substitute SAA5248, VPS will also be available. The SDA5642 is provided as backup solution.

NFM PANEL (N4)

1. Playback:

Lines SOFT1, SOFT2 and TAPE control the focus of the video picture during playback by influencing the FM processing frequency response.

TAPE is active (high) with tapes with a high playback amplitude (high TRIV signal) and increases the resolution by activating T7205 and T7206

This occurs in such instances as with SVHS tapes on which VHS signals have been recorded.

SOFT2 is active (high) with tapes with a low playback voltage and switches the picture one step "softer" with T7203.

SOFT1 is active (high) during LP playback and also switches the picture one step "softer" with T7104.

2. Recording:

When recording, lines SOFT1, SOFT2 and TAPE control an 8-stage attenuator (T7200, T7201, T7202), which provides "automatic write current optimisation" (optional).

The FMRV write current is written and read at 8 different levels (40 ms) when the tape is threaded and stationary.

The TRIV signal is measured during reading and the write current that achieves the highest playback amplitude is established.

This process is carried out 4 times. The optimum write current setting is stored once the results have been averaged out.

NIO - BOARD (N4)

Description of function:

General:

The fact that the family board can be used universally means that the input and output selection switches have to be split between the NFB4 and the N10. The various control line outputs are made by shift register 7413.

Input selection switch:

The front sockets, the front end and SCART1 / SCART2 are selected using selection switch 7592 (NFB4). The relevant control lines are IS1 and IS2. The selection of either SCART1 or SCART2 is done at the NIO by control line ES1 with ICs 7590 and 7591. The video signal selected (VBS) is now routed to the signal electronics section and the audio signal selected (ALMR) to the lin. audio section.

SCART2 output

Control line ES2 and ICs 7590 and 7591 at the NIO are used to determine whether the front end signal or SCART1-In should be at SCART2-Out.

OSD (optional)

The OSD information is overlaid onto the output signal in the signal electronic section (VSB) at the NIO. The signal is then known as VIDOUT.

SCART1 output

The SCRTV control line and ICs 7590 and 7591 (NIO) are used to determine whether SCART2-In or the output signal from the signal electronics section should be at SCART1-Out. The output signal from the signal electronics section can either be the playback signal or, if the signal electronics section is located in the look-through, the VBS signal (see Item 2).

MOD-out

The modulator signal is the output signal from the signal electronics section including the OSD information (VIDOUT).

16:9 (optional)

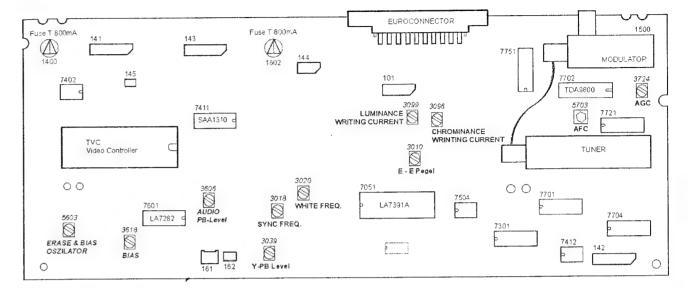
Control line 8SC1 switches pin 8 on SCART1 via transistors 7502 and 7501. Control line SC1HL along with transistor 7503 and Z diode 6505 determines whether the power output should be 6V or 12V.

Follow me (optional)

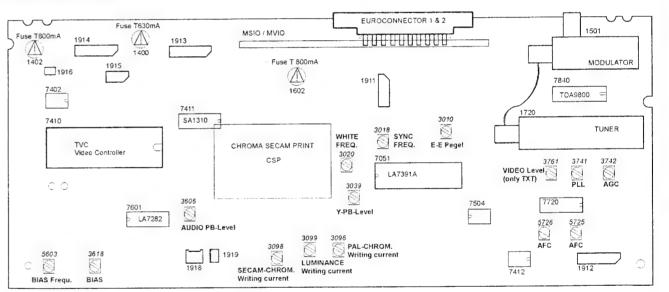
The video signal from the internal front end of the VCR (VFV) and the video signal from the television set connected to SCART1-in (VIN1) are digitised via comparators and then compared with one another. Low at the circuit output means that the picture contents from the two video signals are identical and that they must come from the same transmitter.

ADJUSTMENTS

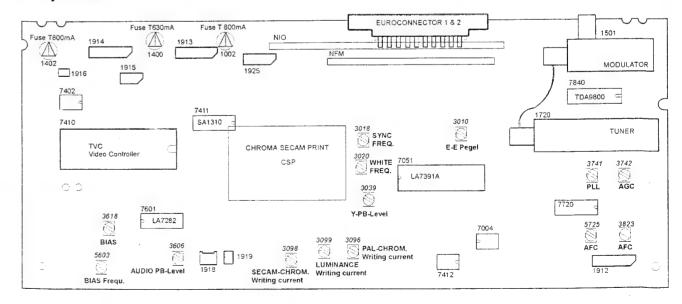
Family Board N1/2



Family Board N3/5:



Family Board 4:



VIDEOSIGNALPROCESSING

1. E-E level (3010):

- Connect a pattern generator and apply a 100% white
- picture to the Euroconnector (programme E1).
- Select 'Stop' mode.
- Connect an oscillocope to Pin 4 of IC7051.
- Adjust resistor 3010 until the amplitude of the output voltage is $0.52 \text{ V}_{pp} \pm 0.02 \text{ V}_{pp} \text{ (Fig.1)}.$
- Check that the voltage at the connector Scart1 pin 19

1,9 Vpp ± 0.1 Vpp

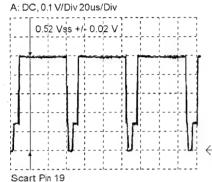


Fig. 1

2. Sync frequency (3018): SYNCHR W RUPP

- Select 'record' mode.
- Apply no signal (programme E1).
- Connect a frequency counter to the 'video current test pin
- (MP1) on the head amplifier.
- Adjust resistor 3018 until the frequency counter indicates revealed U 3.800 MHz ± 10 kHz.

3. White frequency (3020):

Before carrying out this adjustment, check point 1 and 2.

- Connect a pattern generator and apply a 100% white picture
- Select 'record' mode.
- Connect an oscilloscope to the 'video current test pin' (MP1) on Logic the head amplifier.
- Adjust resistor 3020 until the frequency counter reads 4.600 MHz ± 10 kHz.

4. Writing current adjustment:

N1/2: Adj. R3099 and R3096 balancing jointly! Mhundez N3/4/5: Adj. R3099 and R3096 and R3098 balancing

AHPL9260 WV [1-270] jointly!

4.1 Luminance writing current adjustment (3099):

- Select 'record' mode.
- Connect an oscilloscope to the 'video current test pin' (MP1) on the head amplifier.
- Apply no signal (programme E1).
- Adjust resistor 3099 for a signal amplitude of (Fig.2):
- 260 mV_{pp} standard play sets (2/0, 3/0)
- 230 mV₀₀ longply sets (4/0, 2/0LP)

20049

Play a 100% white picture from a cassette.

Adjust resistor 3039 until the amplitude of the output signal is

A: DC, 0.1 V/Div 20us/Div

260 mVss TESTPIN TP1 (Headamplifier)

4.2 PAL Chrominance writing current adjustment (3096):

- Select 'record' mode.
- Connect an oscilloscope to the 'video current test pin' (MP1) on the head amplifier.

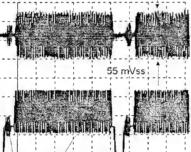
Fig. 2

Connect a pattern generator and apply a red (75%) signal (Burst:Chroma = 1:2,2) to the Euroconnector (programme E1). Connect pin 40 of IC7051 to + 5V.

- Adjust resistor 3096 for a signal amplitude of : 55 mVpp standard play sets (2/0, 3/0) 49 mVpp longply sets (4/0, 2/0LP)

(Fig. 3) (-13.5 dB relative to the luminance signal)

A: AC, 20 mV/Div 10 us/Div B: AC, 0.2 V/DIV 10 us/Div



Burst: Chroma = 1:2.2

Fig. 3

4.3 SECAM Chrominance writing current adjustment (3098) (only N3/4/5):

- Select 'record' mode.
- Connect an oscilloscope to the 'video current test pin' (MP1) on the head amplifier.
- Connect a pattern generator and apply a secam red signal to the Euroconnector (programme E1).
- Connect pin 40 of IC7051 to + 5V.
- Adjust resistor 3098 for a signal amplitude of : 35 mV_{pp} standard play sets (2/0, 3/0)
- 31 mV_{pp} longply sets (4/0, 2/0LP) (Fig. 4) (-17.4 dB relative to the luminance signal).

Luminance playback level (3039):

- Connect an oscilloscope to connector scart pin 19.
- $2.0 V_{pp} \pm 0.1 V_{pp}$ (Fig. 4).

A-DC 0.5 V/Div 20 us/Div

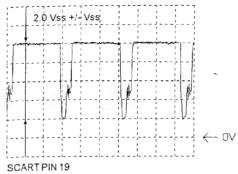


Fig. 4

FRONT END (N1/2)

1. Adjustment of the video demodulator (5703):

- Apply on tuner pos. 1720 Pin 17 100 mV_{eff} 38.9 MHz.
- Adjust with demodulator coil 5703 on Pin 15 IC 7702 $2.5 \text{ V} \pm 0.2 \text{ V} DC.$

2. Adjustment of the RF-AGC (3724):

- Supply a PAL white picture with an amplitude of 2.2 mV_{eff} (67 dB_{iiV}) with sound carrier but without ton modulation.
- Connect an oscilloskope to tuner 1701 pin 17.
- Adjust with 3724 on 550 mV_{pp} +50 mV/-0 mV.

3. Adjustment of the tuning limits:

(only for Amtsblatt requirements)

- Erase the EEPROM.
- Remove the mains supply and press WIND, REWIND and DOWN simultaneously and while the keys are held down reconnect the mains supply.
- Select SERVICE mode. Press approx. 5 sec. STOP on RC and PLAY on the recorder.
- Connect a pattern generator with colour bar and adjusted for channel E2 (48,25 MHz).
- Aktivat channel search until the recorder has found the channel.
- Press STOP on RC and REWIND on the recorder, Storing the lower tuning limit.
- Adjust pattern generator for channel E69 (855,25 MHz).
- Aktivat channel search until the recorder has found the channel
- Press STOP on RC and WIND on the recorder. Storing the upper tuning limit.
- Adjust pattern generator for channel S22 (311.25 MHz).
- Aktivat channel search until the recorder has found the
- Press STOP on RC and RECORD on the recorder. Storing the upper tuning limit Band III.
- Switch the set to STAND BY.

FRONT END (N3/4/5)

1. AFC-adjustment (5725):

- Supply via a 22 nF capacitor a 38.9 MHz sinewave signal with 100 mV_{eff} to pin 17 of tuner 1701
- Connect a voltmeter to IC7720 pin 15.
- Balance to 2,5 V ± 0,1 V by means of coil 5725.

2. AFC-adjustment for SECAM und MULTISTANDARD sets :

N3/5: coil 5726 N4 resistor 3823

 Supply via a 22 nF capacitor a 33.9 MHz sinewave signal with 100 mV_{eff} to pin17 of tuner

Connect signal PSS (plug 1912 pin 1) to ground (Secam aktive)

Connect signal SB1 (plug 1912 pin 5) to ground (Band 1 aktive)

Connect a voltmeter to IC7720 pin 15.

Balance to 2,5 V ± 0,1 V by means of coil 5726 (N4; resistor

Carry out the 5725 and 5726 balancing jointly ! (only N3/5)

3. PLL signal to noise ratio for SECAM und MULTISTANDARD sets (3741):

- Supply a PAL picture with sound carrier but without ton modulation
- Connect an oscilloskope to plug 1591 pin 16 (AMLP).
- Adjust to minimum amplitude by means of 3741.

4. Rf-AGC adjustment (3742):

- Supply a PAL white picture with an amplitude of 2,2 mV_{eff} (67 dB_{nV}) with sound carrier but without ton modulation.
- Connect an oscilloskope to tuner 1701 pin 17.
- Adjust to 550 mV_{ss} +50mV/-0mV by means of 3742.

5. Adjustment of the video output level (3761) (N3/5):

(only for teletext sets)

- Apply a standard video signal to the aerial input.
- Connect an oscilloskope to emitter of E- 7761.
- Adjust resistor 3761 for output voltage 0,9 Vss ± 0.05 V.

LINEAR AUDIO

1. Setting of the erasing frequency (5603):

- · Bring unit into mode "RECORD".
- · Connect frequency meter to resistor 3618.
- Set erasing frequency to 70 kHz ± 2.5 kHz with 5603

2. Setting of the bias current (BIAS) (3618) :

- Connect millivoltmeter to 3618 (differential measurement)
- Bring unit into mode "RECORD"
- Set voltage at 3618 to 16 mV_{eff} (70 kHz) with 3618.

Checking the bias setting:

After "BIAS" has been set with the indicated approximate value, make a music recording with linear audio alone. Use cassettes made by well-known manufacturers, but do not use chromium dioxide tape. When reproducing this recording, note whether the highs are sufficiently reproduced or whether the sound is not subject to distortion. If the share of the highs is not sufficient, the "BIAS" current must be reduced; if distortion is too great, it must be increased.

- 3. Playback amplitude setting (3606) :
- Record a signal 500 mVeff, 1 kHz.
- Connect millivoltmeter to the Scart1 pin1 (audio output).
 Reproduce this recording
- Set the playback amplitude to 500 mV $_{\rm eff}$ \pm 50 mV with 3606

DECKELECTRONIC:

Software adjustment of the GAP:

- Insert a test cassette with norm video signal (for ex. 4822 397 30103)
- Select SERVICE mode. (press approx. 5 sec. STOP on RC and PLAY on the recorder
- Press PLAY on RC and EJECT on the recorder.

Thereby the automatical adjustment is released and stored in the EEPROM.

If the adjustment has been successfuly done, the recorder gets automatically in STAND BY mode.

If the adjustment has not been made correctly, the recorder rejects the cassette

Reasons: The norm-video signal is out of order Bad scanner Microprocessor is defect.

POWER SUPPLY MSM, NSM

Adjustment of the output voltage.

MSM1: 3204 NSM: 3090

- Connect a voltmeter to connection 9 or 11 of the plug SM1
- Adjust to an output voltage of 5,4 V \pm 0,03 V .

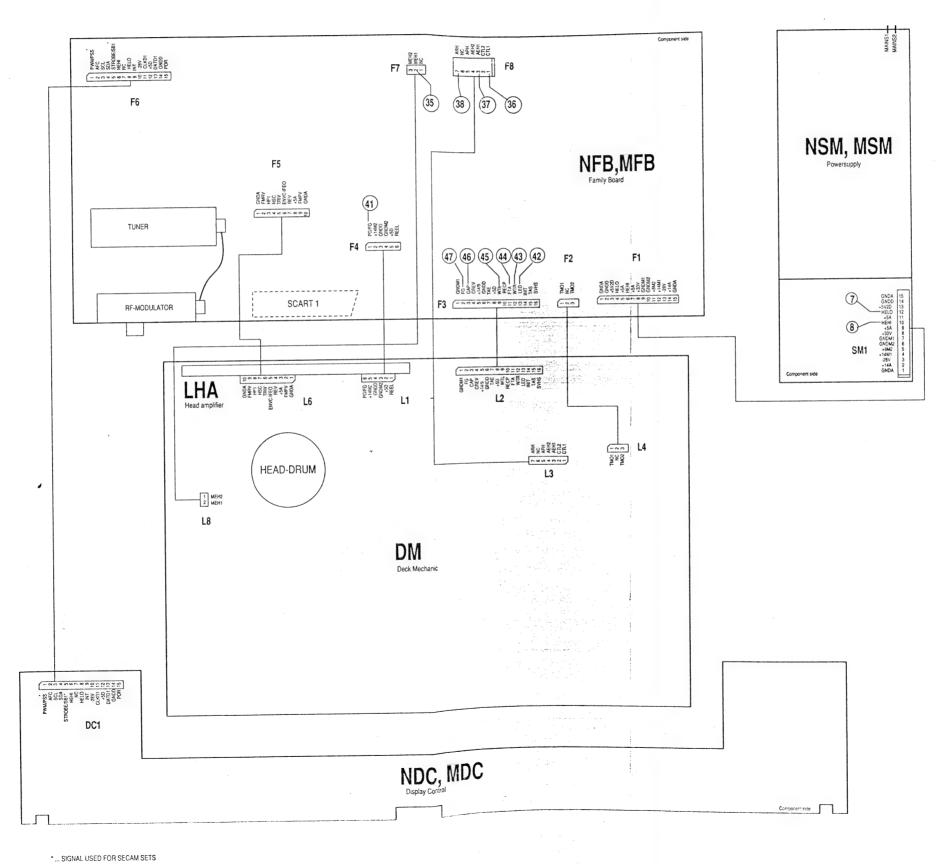
MDC., NDC ADJUSTMENTS:

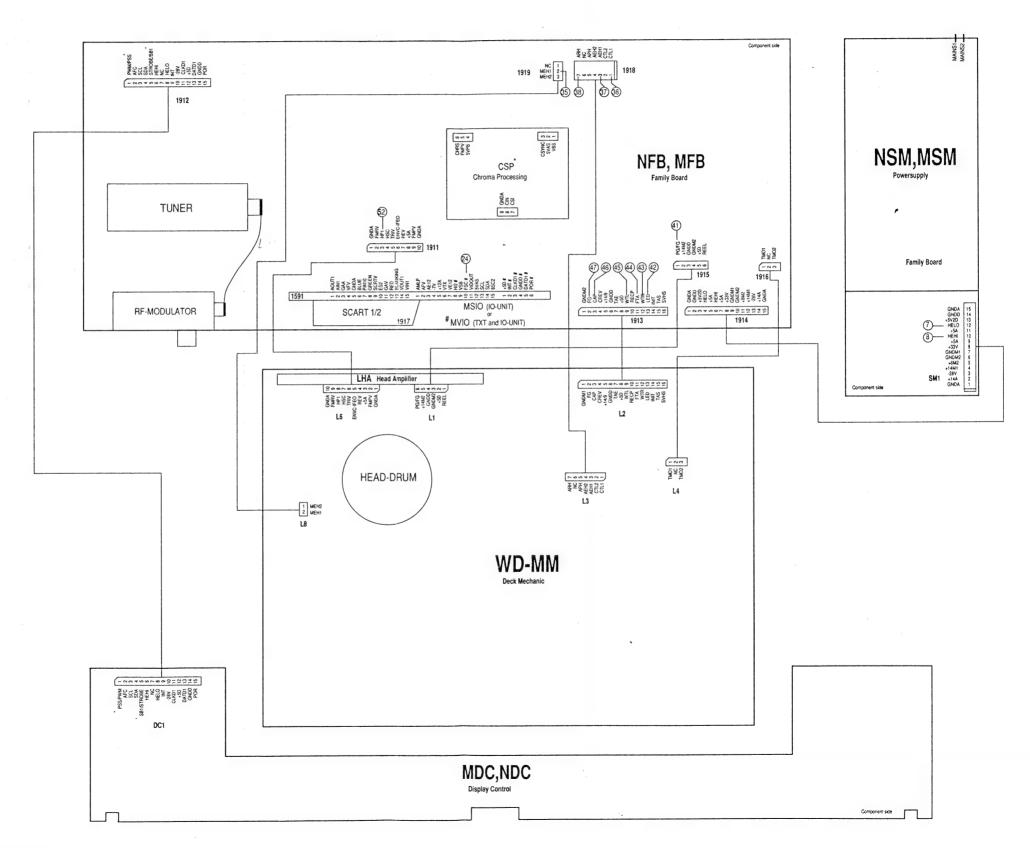
Setting the clock frequency (2005):

- Disconnect the set from the mains.
- Connect a frequency counter to plug 1101 pin 1.
- Press the keys UP, DOWN and PLAY simultaneous.
- Connect the set to the mains.
- Touch pin3 plug 1101 at least 7 times with the earth cable.
- A 5 V squarewave signal has been applied to the frequency counter.
- Set C2005 at 47.36328 ms ± 75 ns.

3-1

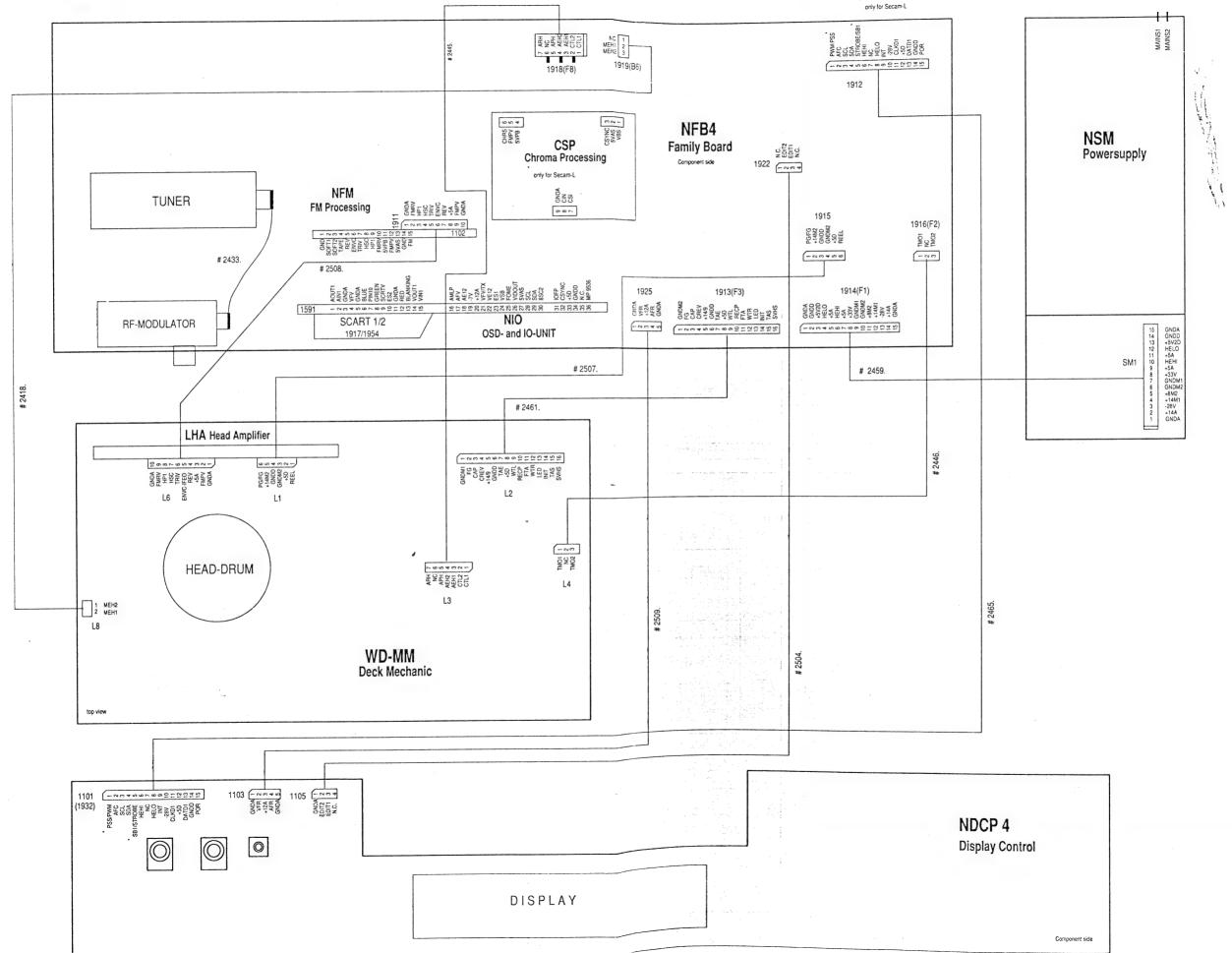
N2



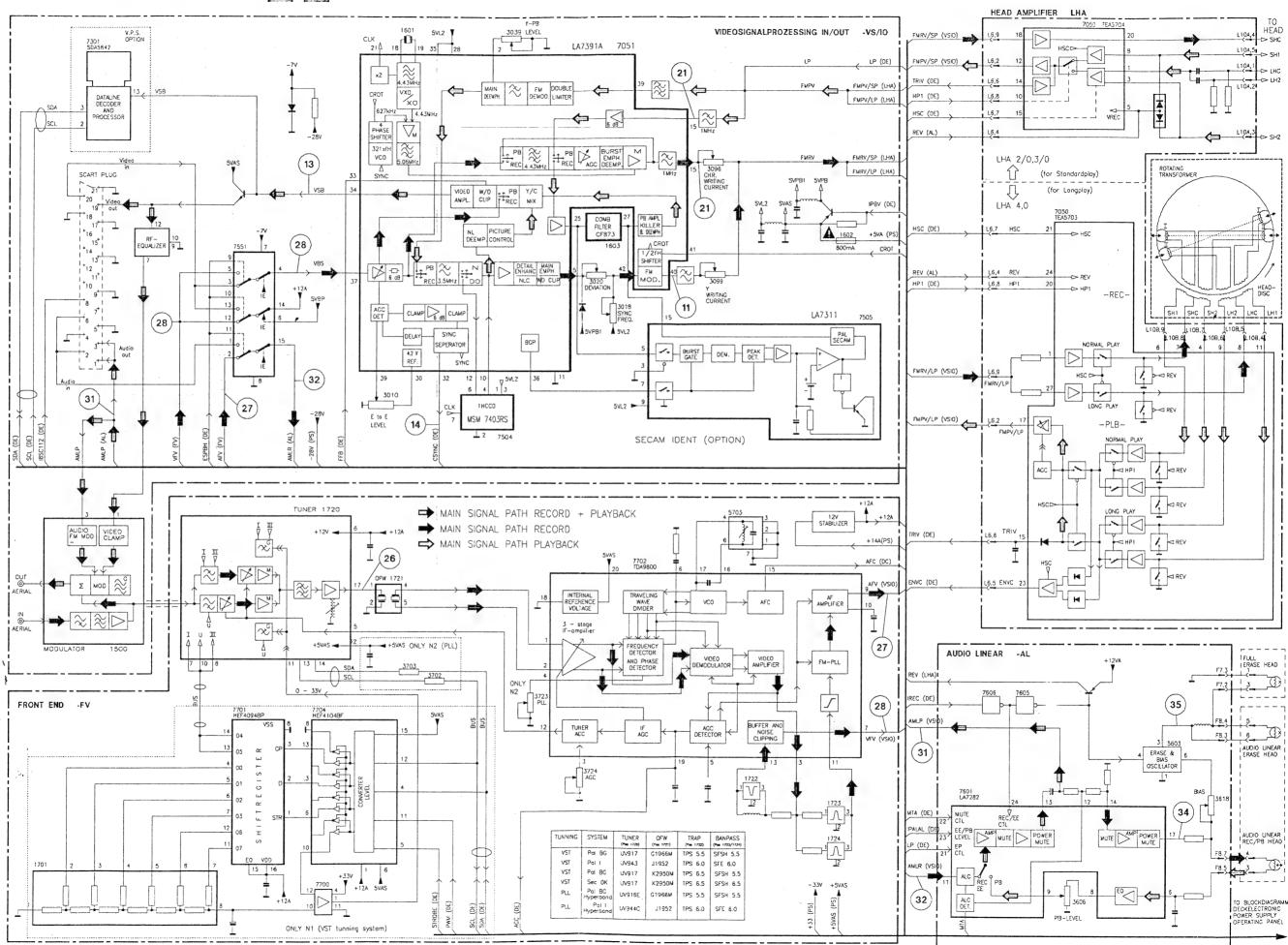


^{#....} ONLY FOR TXT

^{*....} ONLY FOR SECAM-L

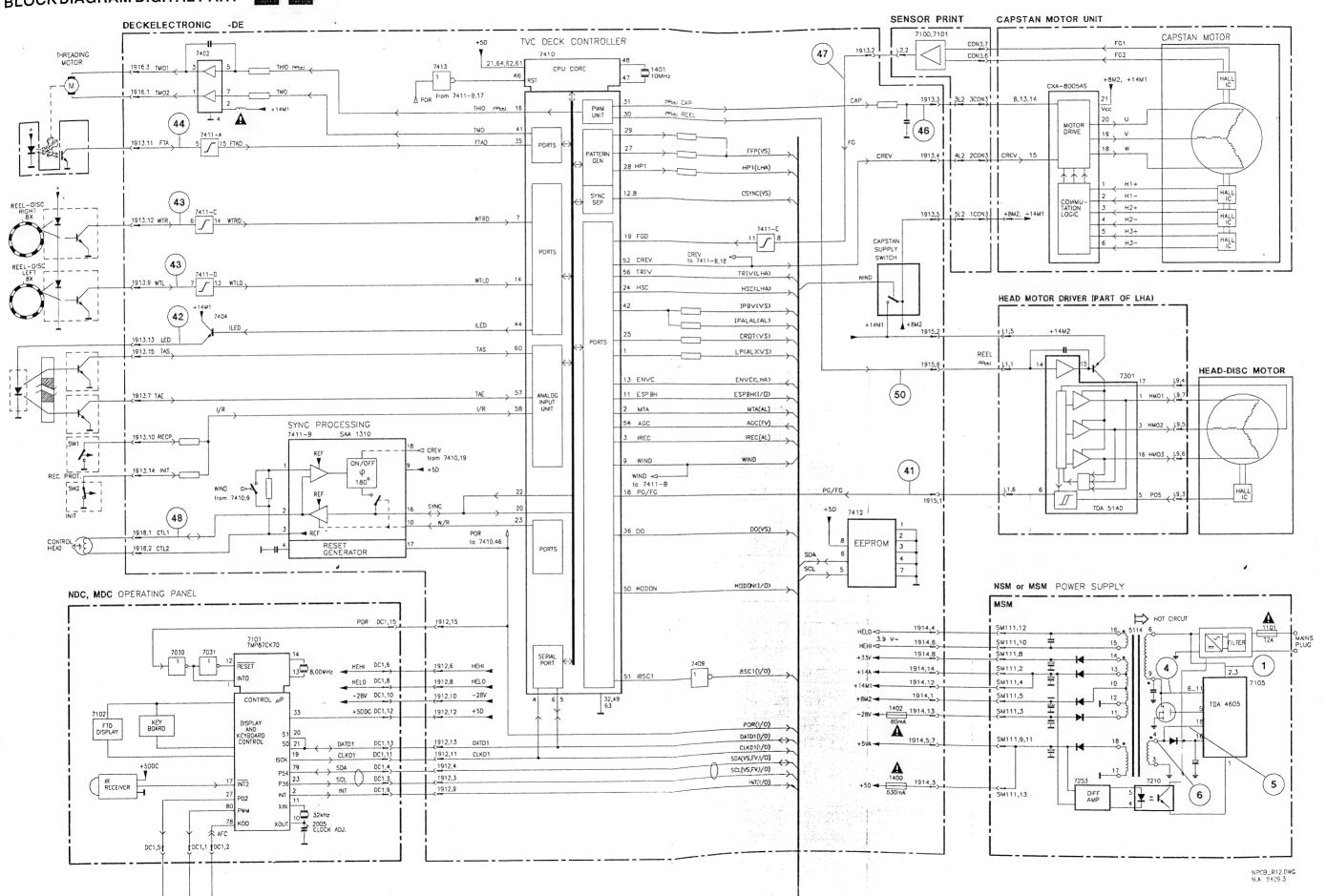


PCS 74501

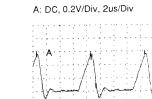


BLOCK DIAGRAM DIGITAL PART N1 N2

TO BLOCKDIAGRAM ANALOG PART

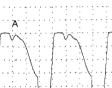


OSCILLOGRAM



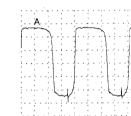
IC7105 Pin 2

A: DC, 0.1kV/Div, 2us/Div



IC7105 Pin 6 11

A: DC, 0.2V/Div, 2us/Div



IC7105 Pin 18

A: DC. 10V/Div. 2us/Div



Trafo 5114 Pin 4

A: AC, 0.2 V/Div , 2 us/Div



IC7051 Pin 40

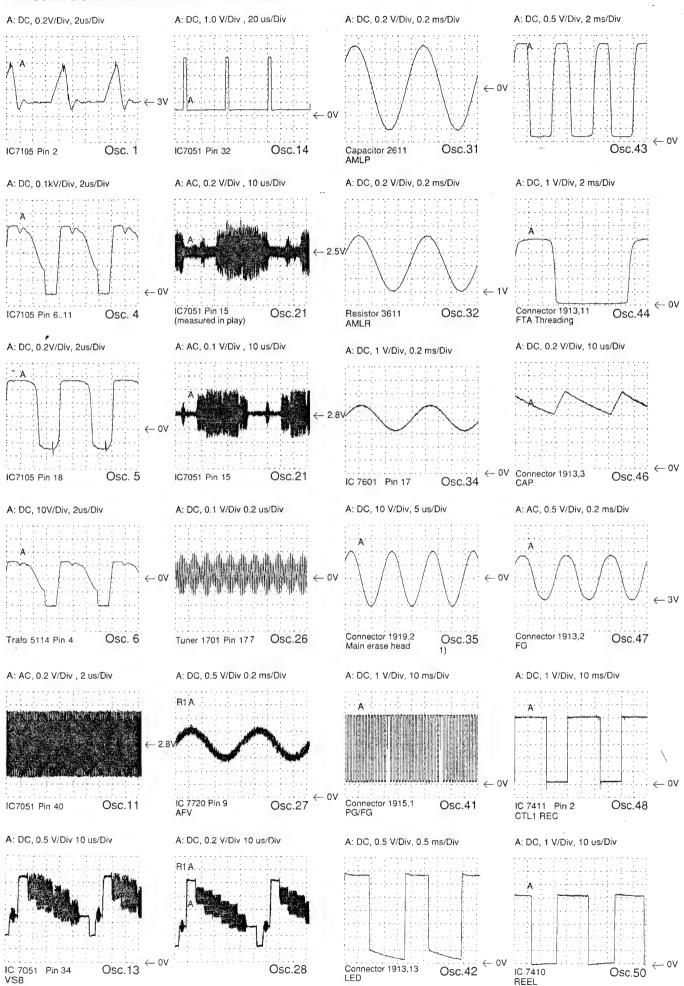
-0.051//8: ... /8:

A: DC, 0.5 V/Div 10 us/Div



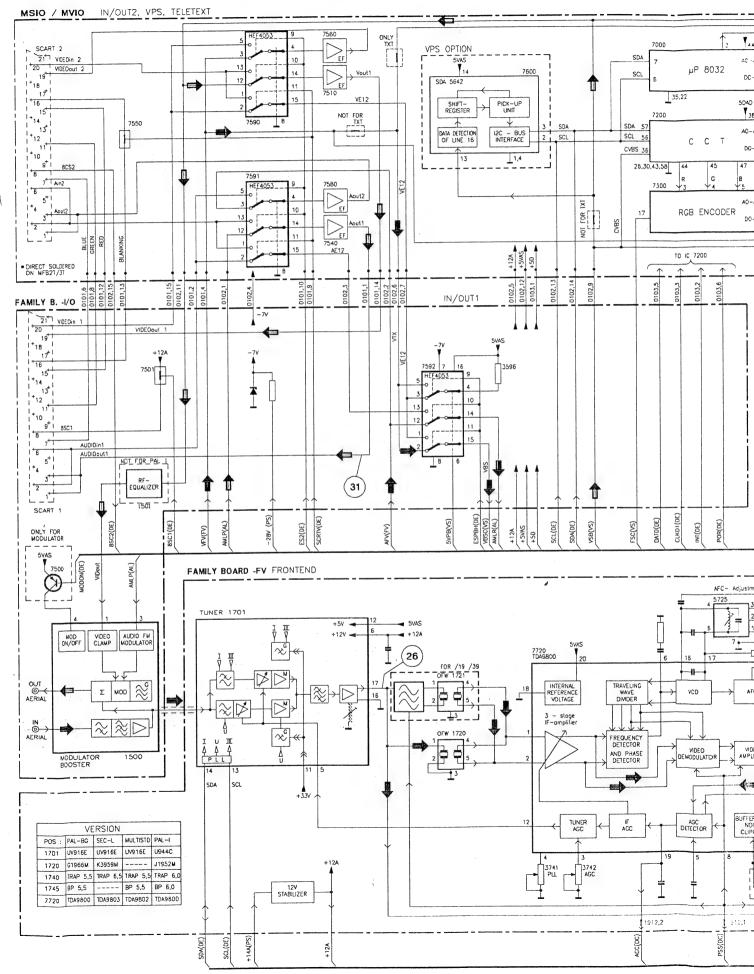
IC 7051 Pin 34

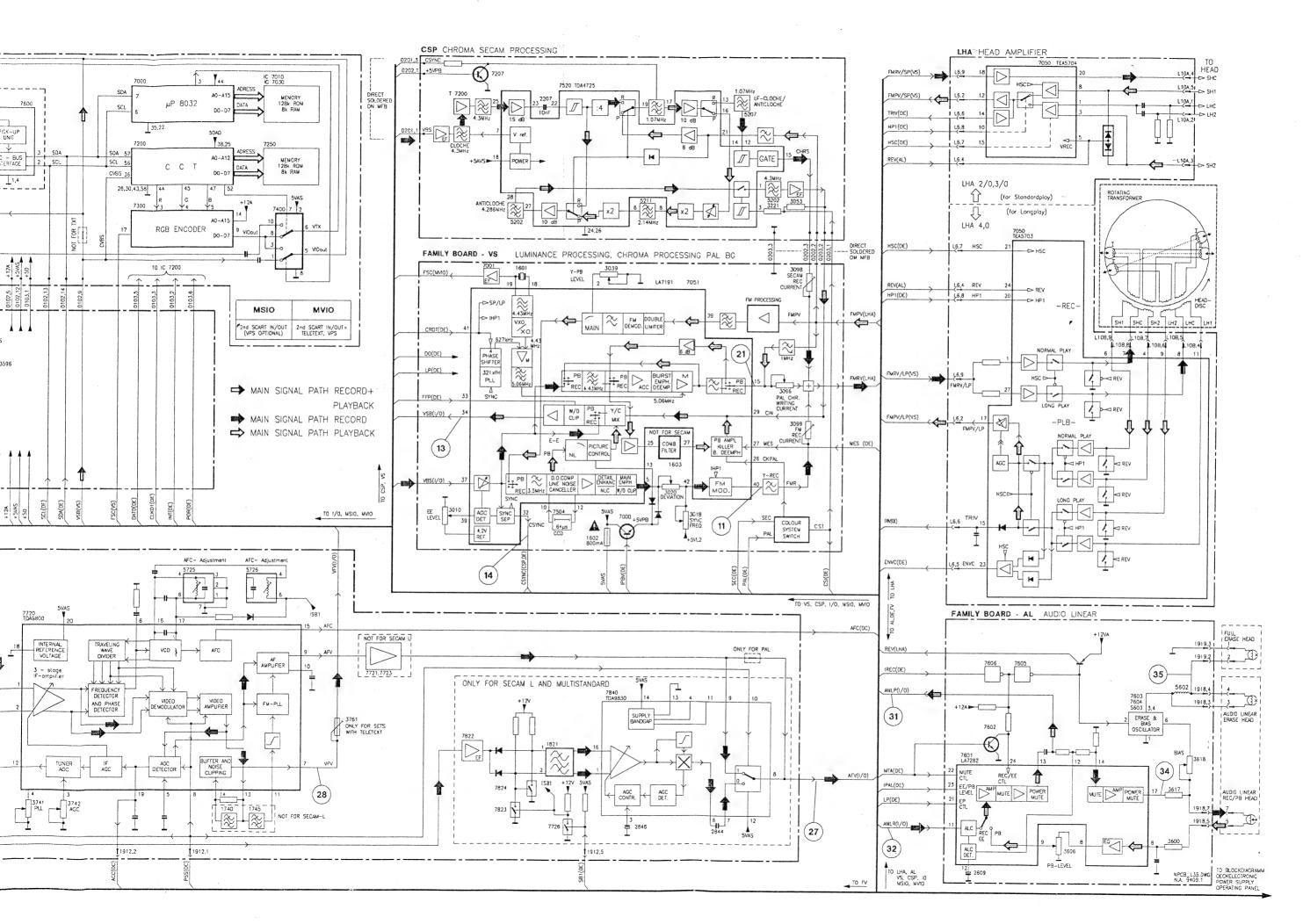
OSCILLOGRAMS BLOCK DIAGRAM



JG

BLOCK DIAGRAM ANALOG PART N3 N5

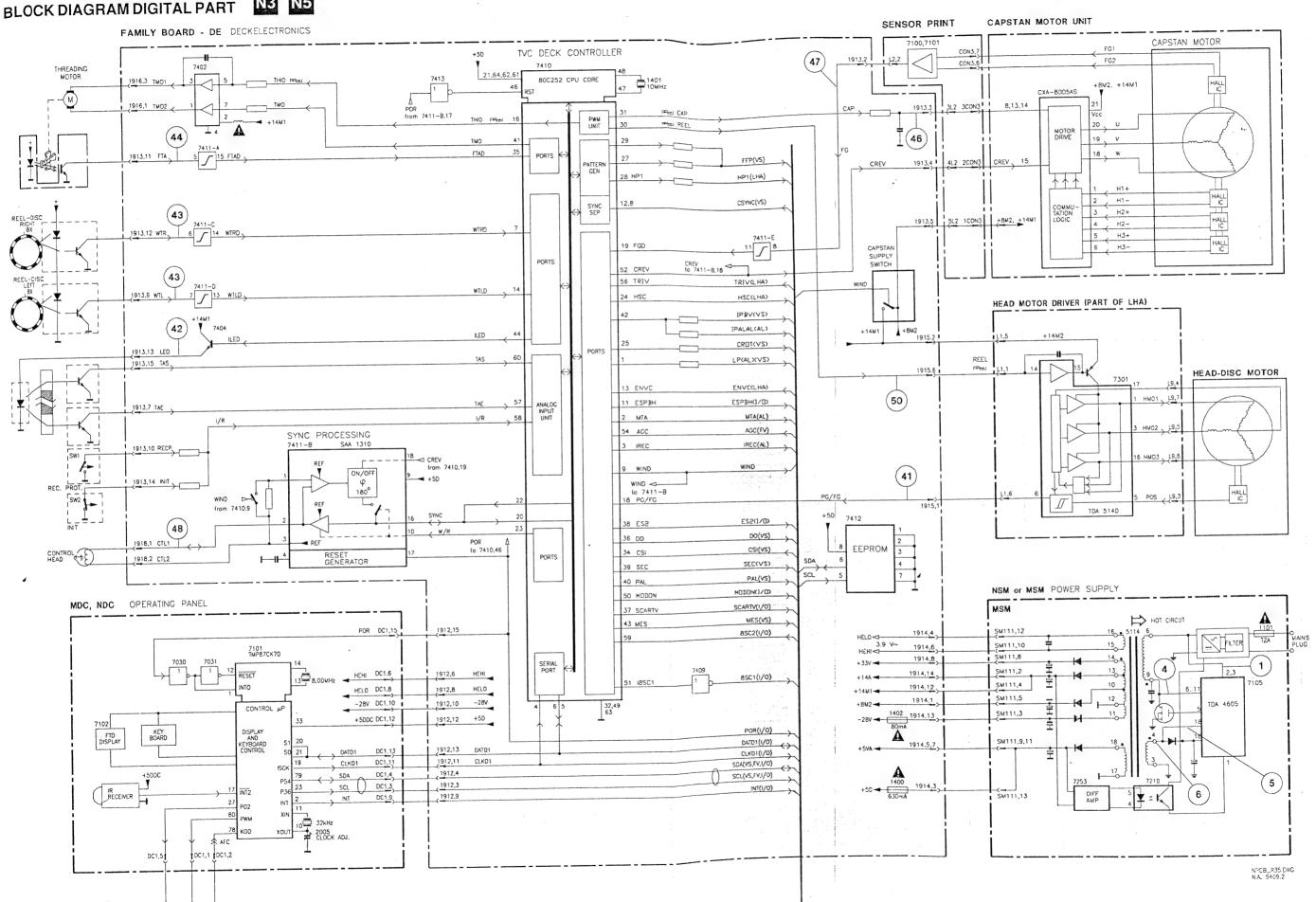






*PSS AFC(FV)

TO REDCKDIAGRAM ANALOG PART



OSCILLOGI

IC7105 Pin 2

A: DC, 0.1kV/Div, 2us

IC7105 Pin 6..11

A: DC, 0.2V/Div, 2us/



IC7105 Pin 18

A: DC, 10V/Div, 2us/[

· · · A · · · · · · · · · · · · · ·

Trafo 5114 Pin 4

A: AC, 0.2 V/Div, 2 u



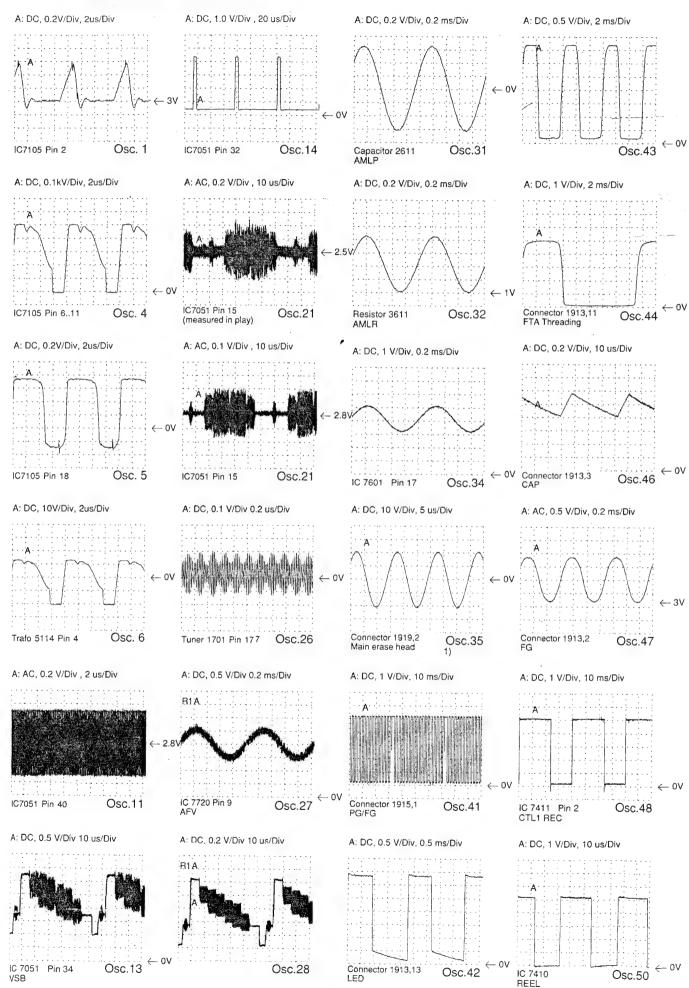
IC7051 Pin 40

A: DC, 0.5 V/Div 10 u

IC 7051 Pin 34

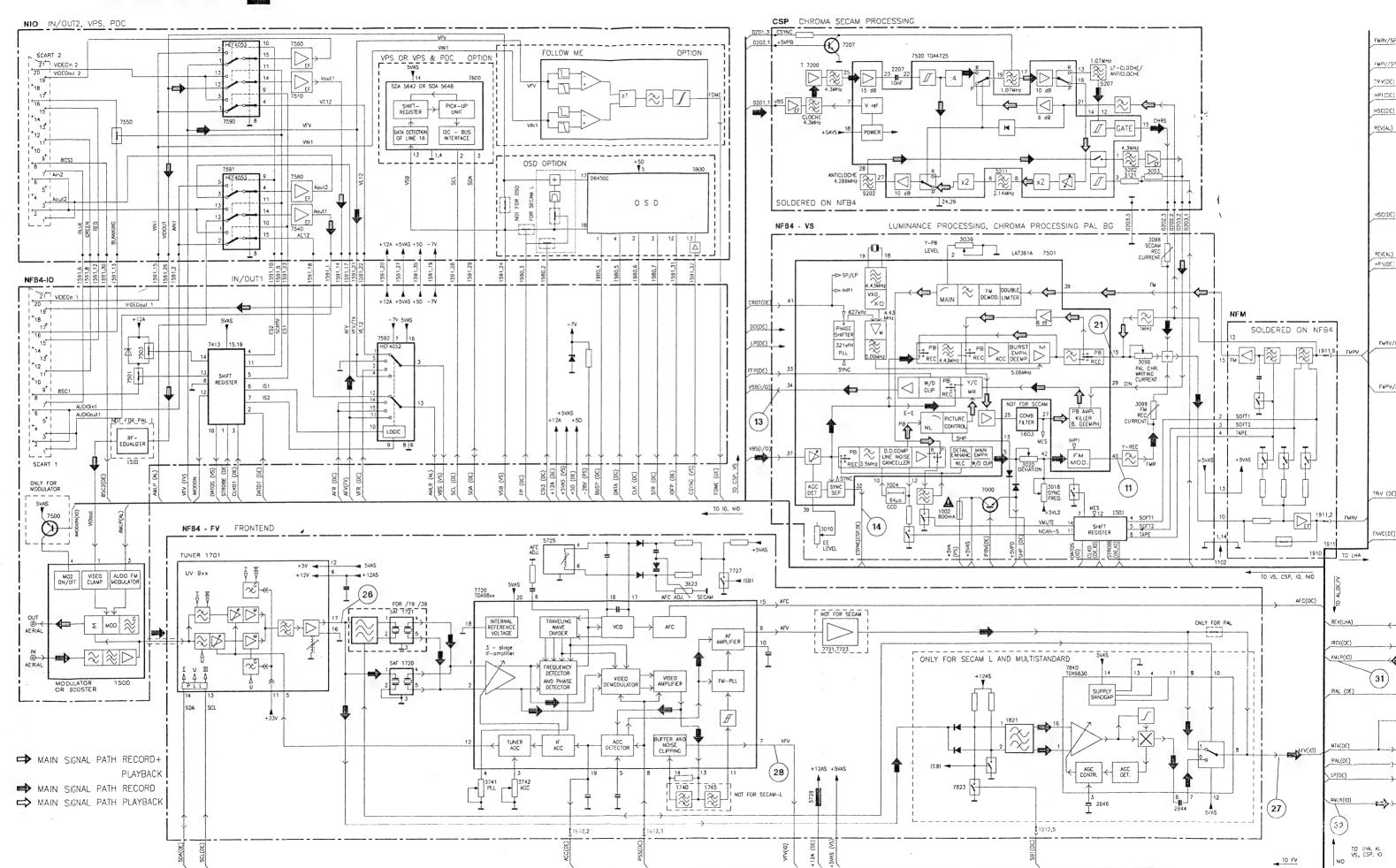
PCS 74505

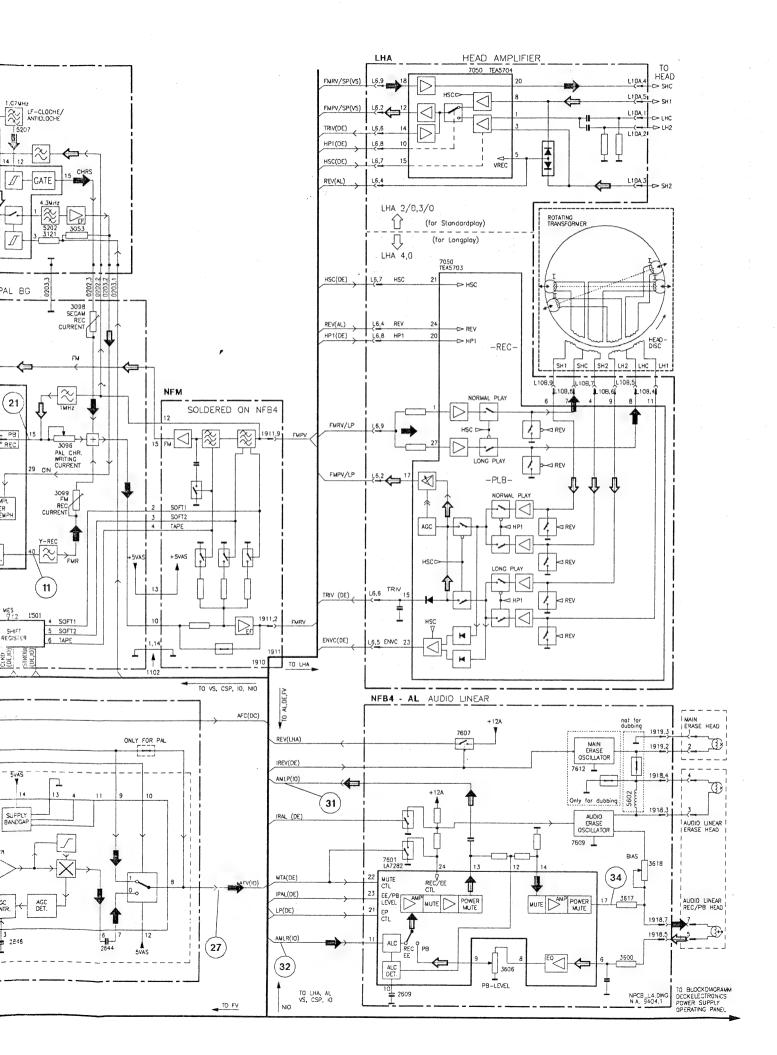
OSCILLOGRAMS BLOCK DIAGRAM



PCS 74506

BLOCK DIAGRAM ANALOG PART





1925,2 î

TO BLOCKDIAGRAM

ANALOG PART

1925,4 1922,3

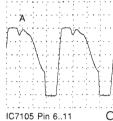
AFR (IO) EDITI (DE)

OSCILLOGRAM

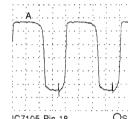


IC7105 Pin 2

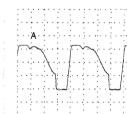
A: DC, 0.1kV/Div, 2us/Div



A: DC 0.2V/Div. 2us/Div



A: DC, 10V/Div, 2us/Div



A: AC, 0.2 V/Div , 2 us/Div



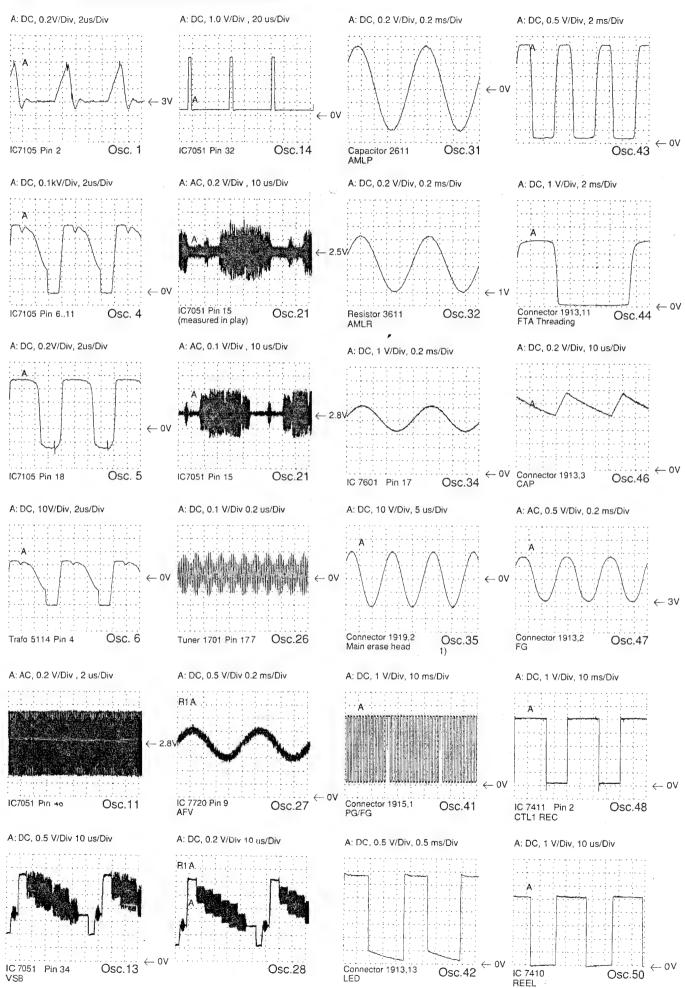
IC7051 Pin 40

A: DC, 0.5 V/Div 10 us/Div

IC 7051 Pin 34 OS

NPCB_R4.DWG N.A. 9404.1

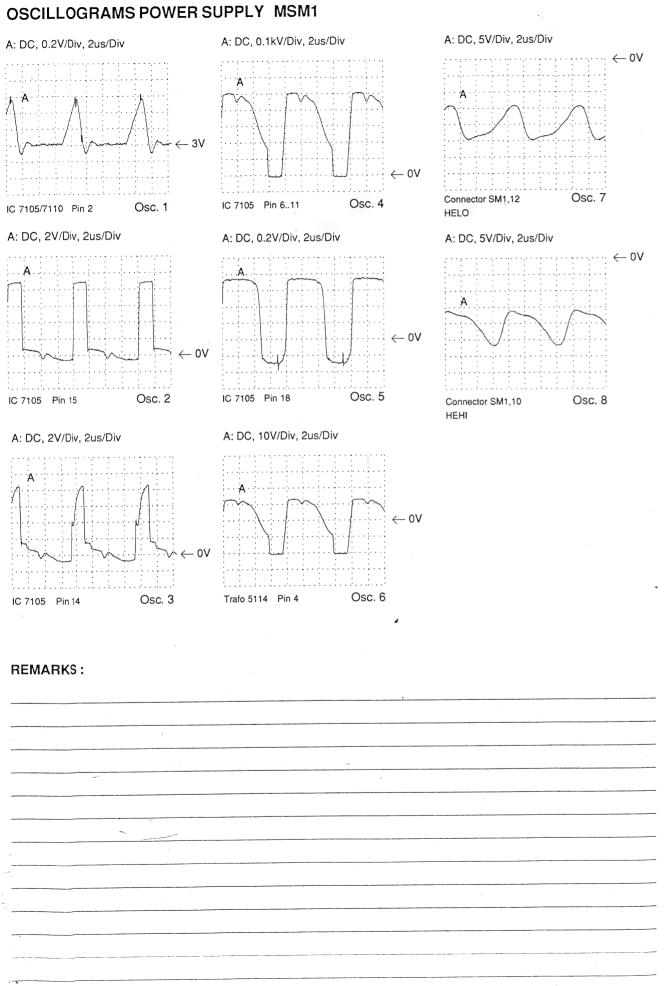
OSCILLOGRAMS BLOCK DIAGRAM



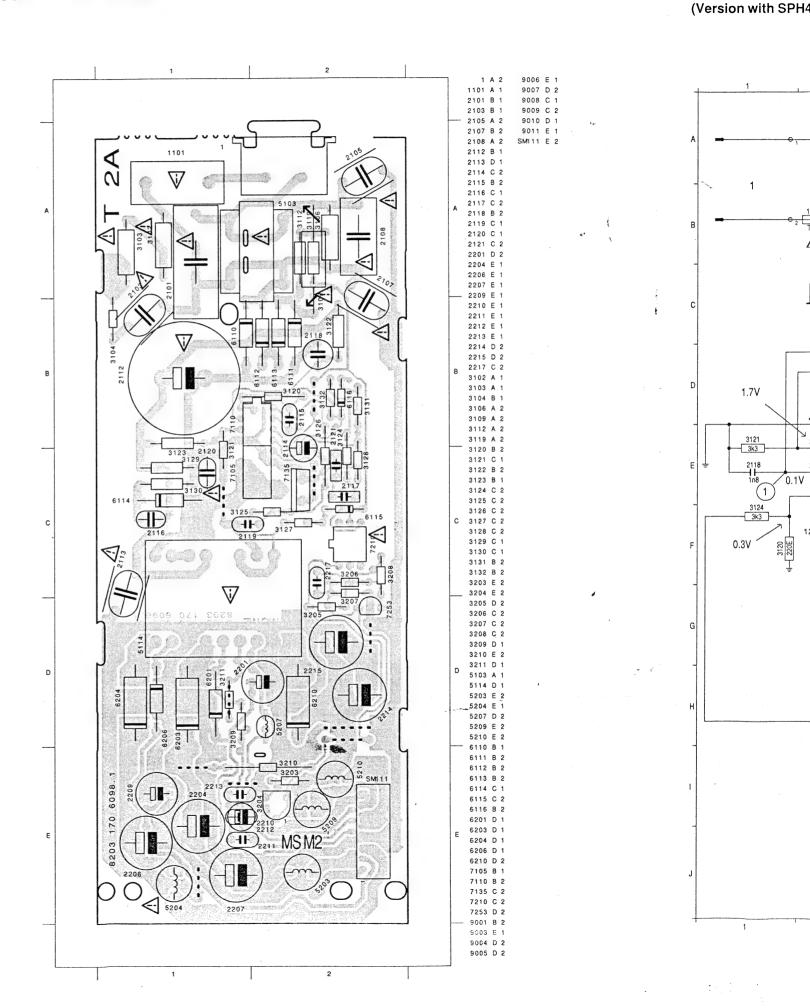
PCS 74507

POWER SUPPL

PCS 74508

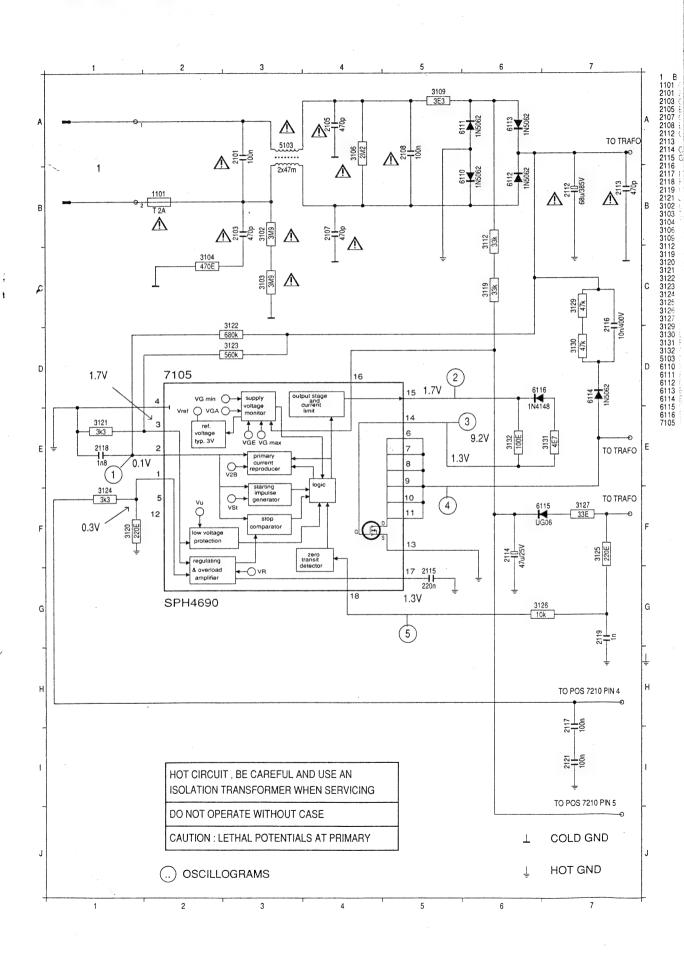


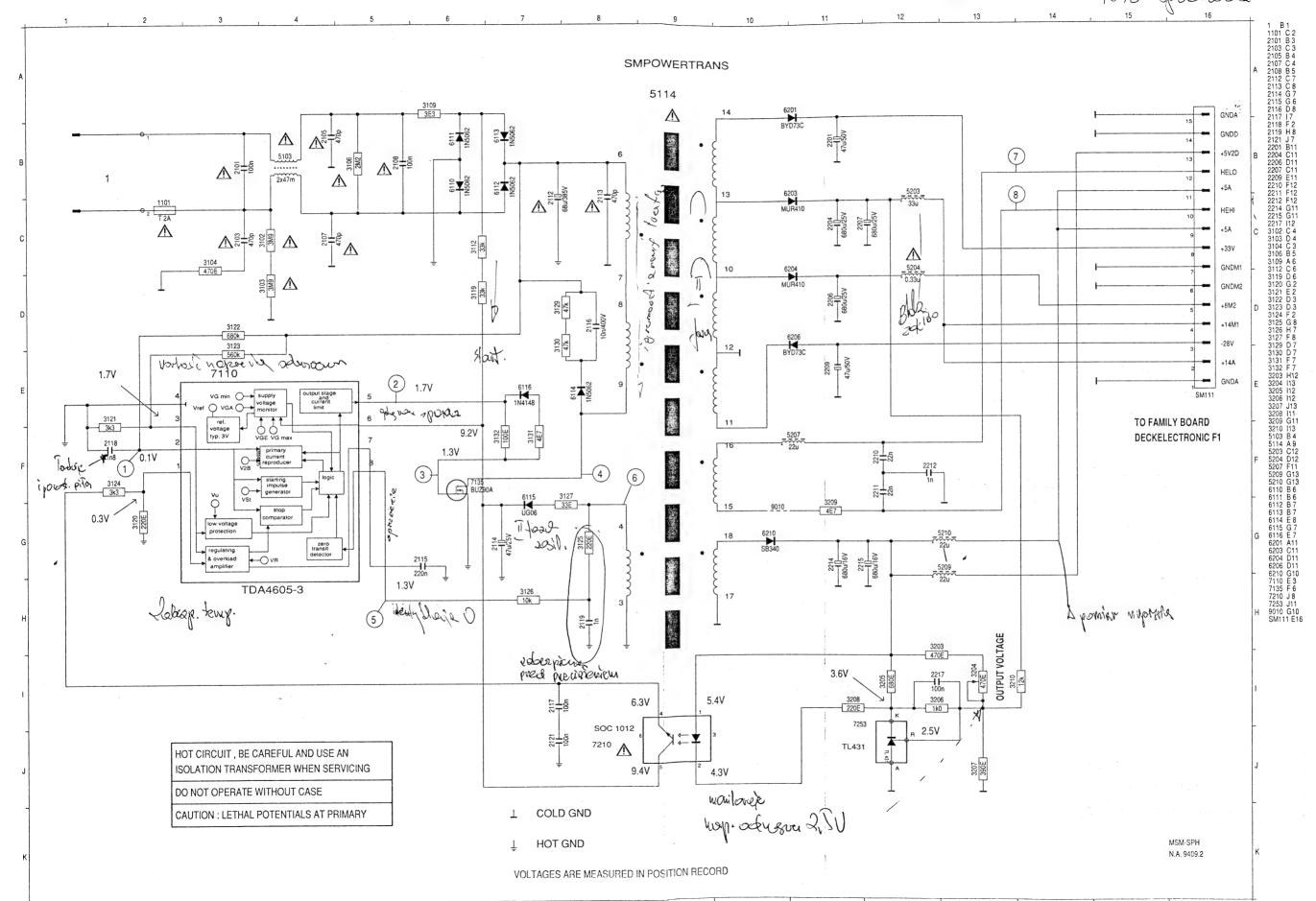
POWER SUPPLY MSM1



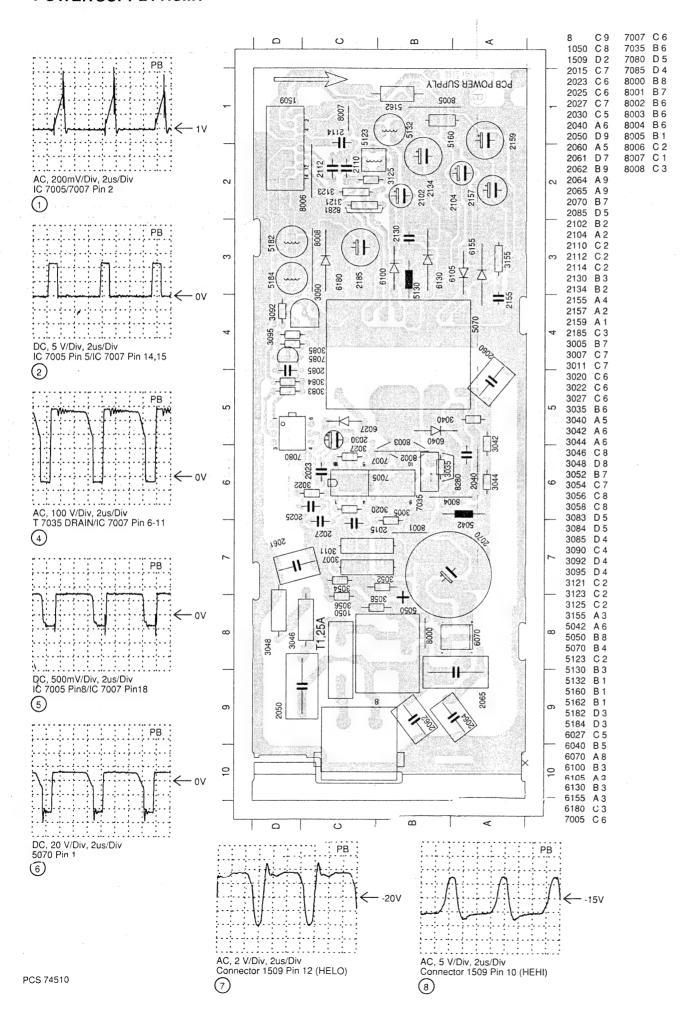
POWER SUPPLY MSM

(Version with SPH4690 IC7105, primary part)

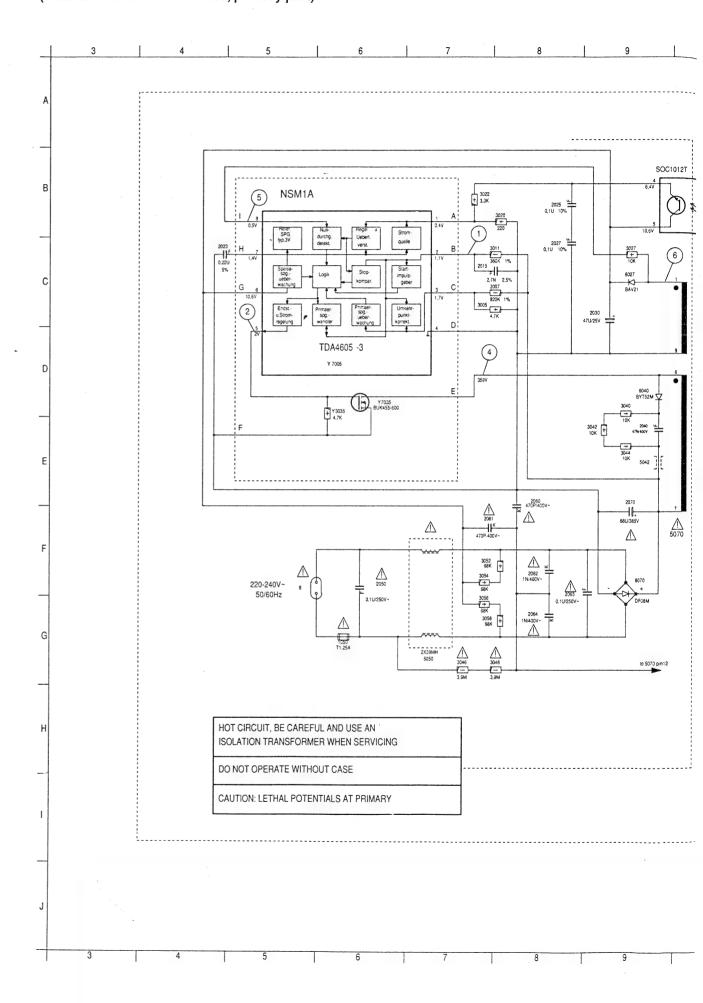




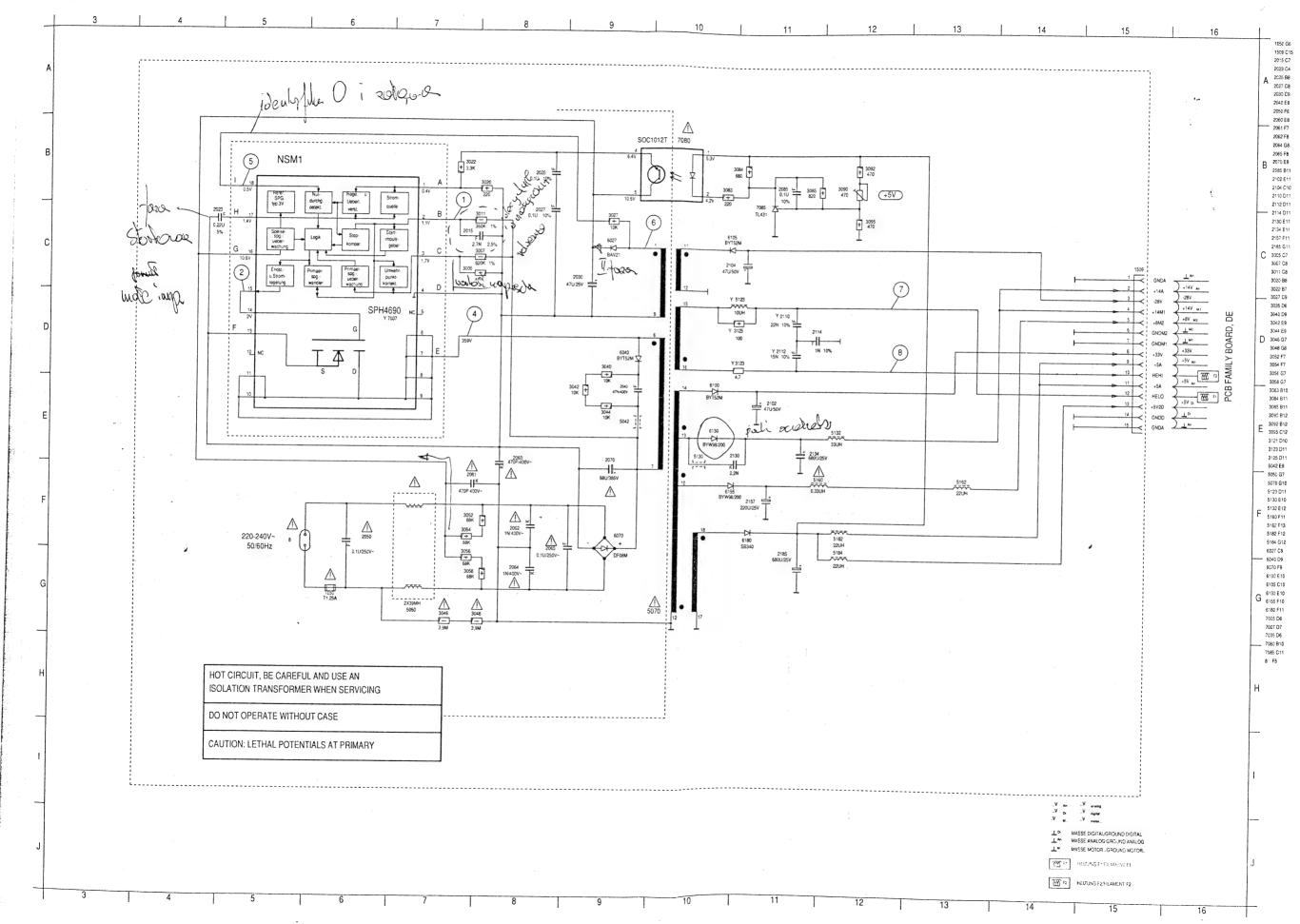
POWER SUPPLY NSM1



POWER SUPPLY NSM1 (Version with SPH4690 IC7005, primary part)

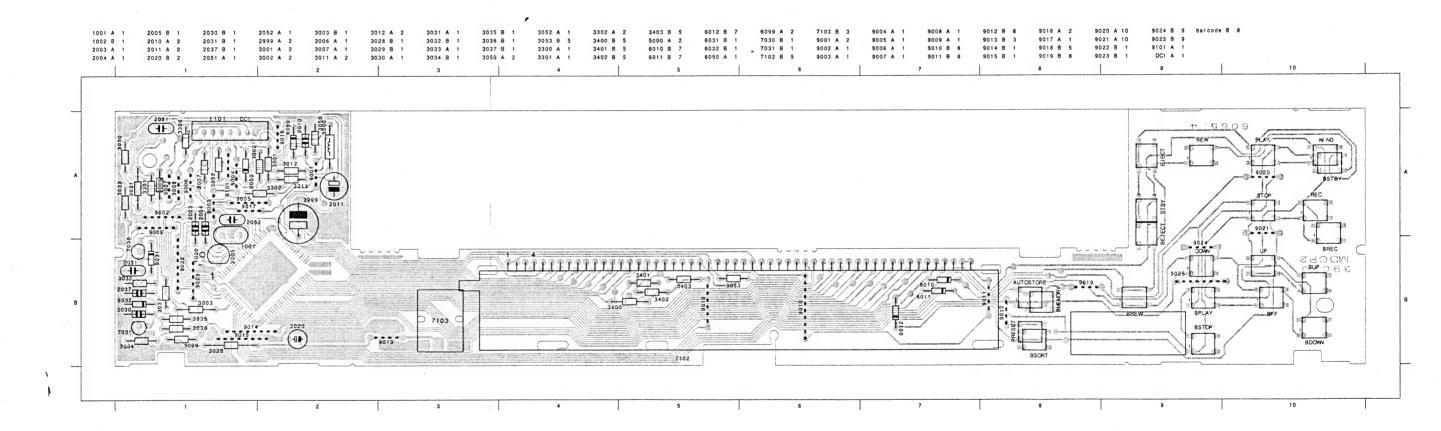


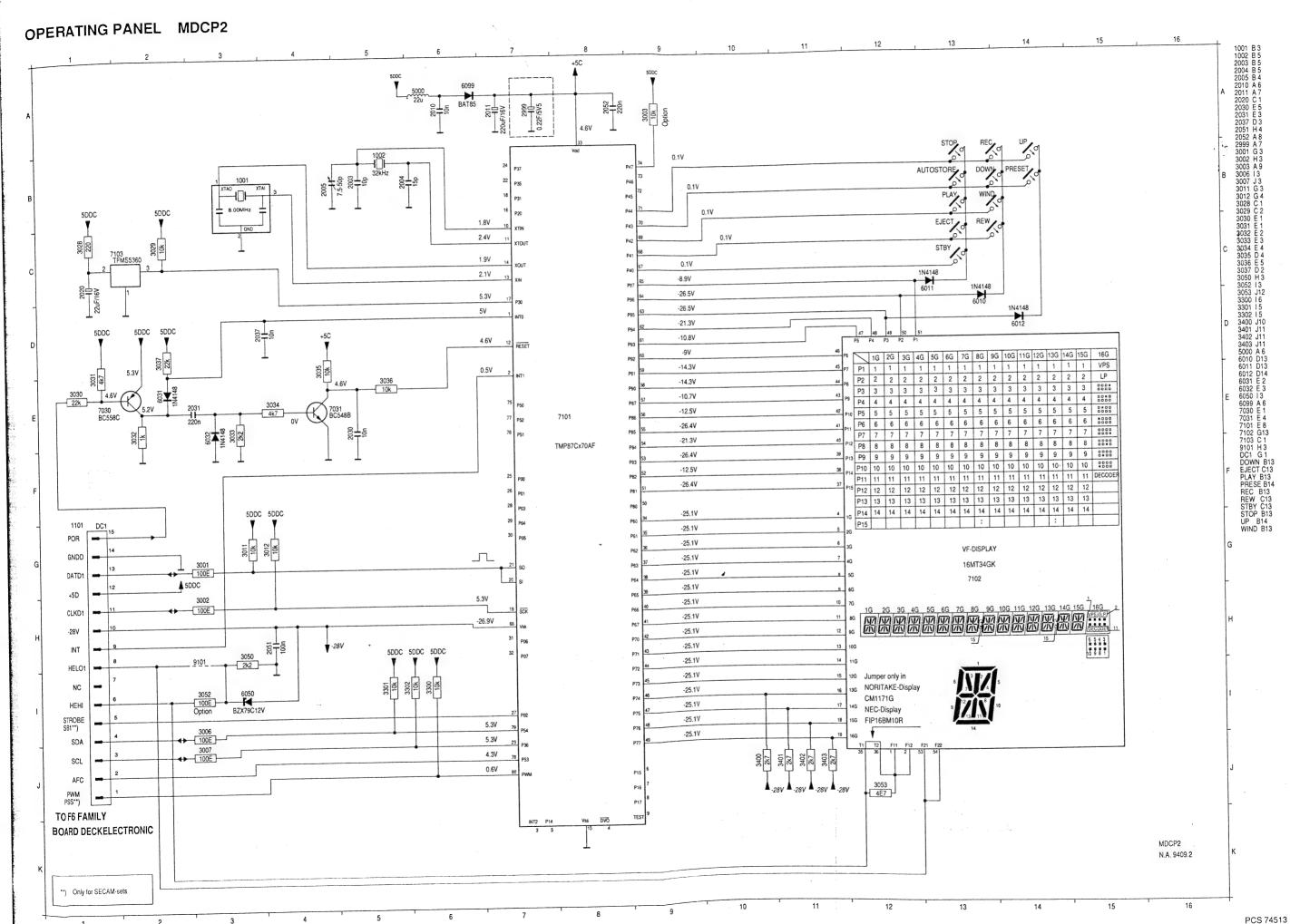
(Version with SPH4690 IC7007)



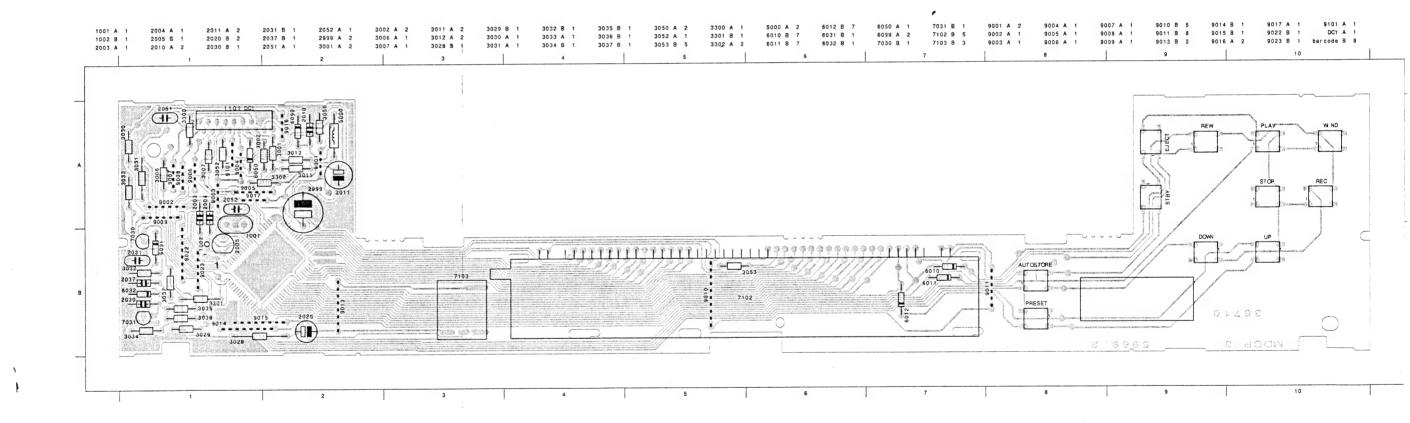
PCS 74509

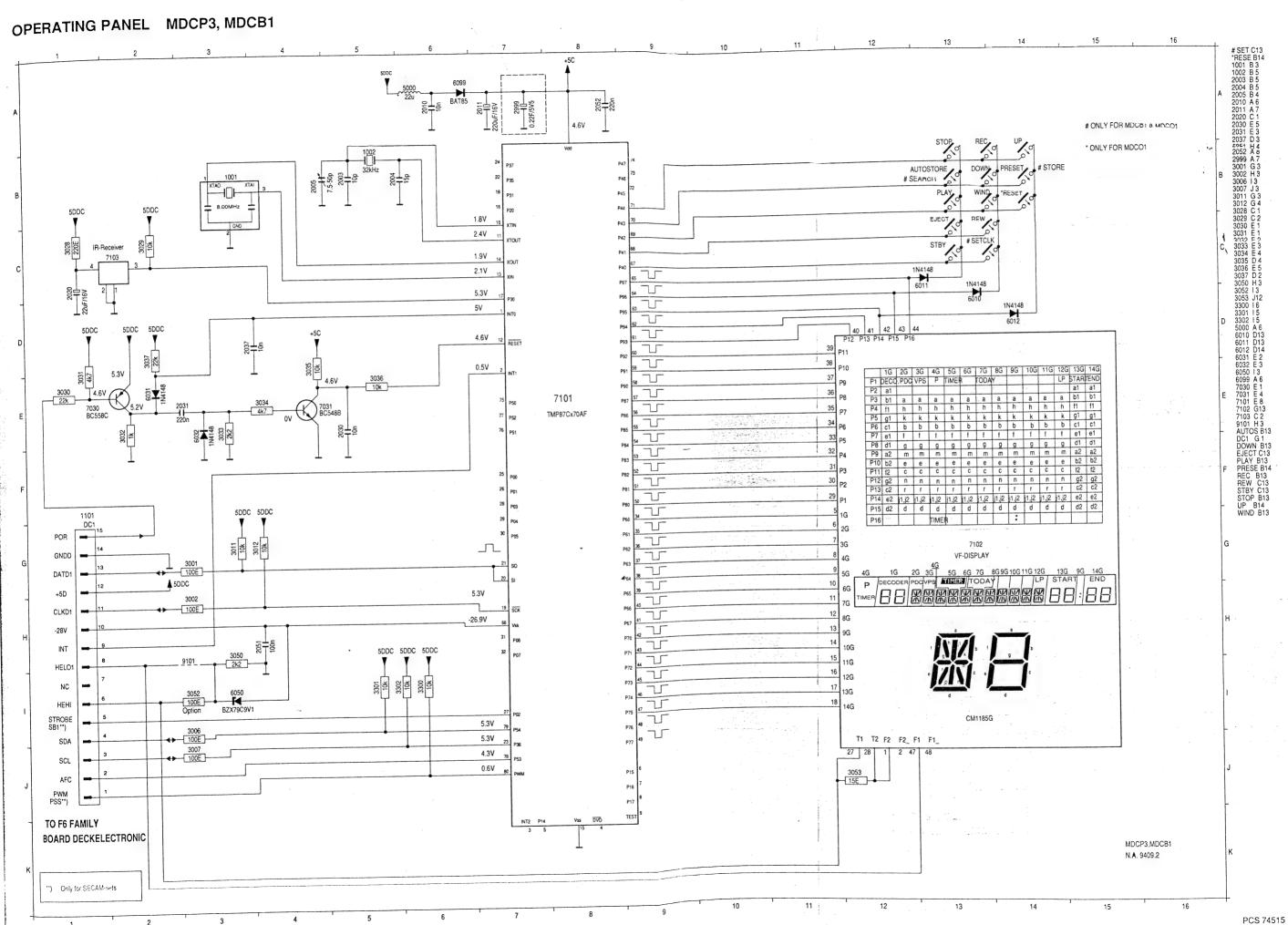
OSi





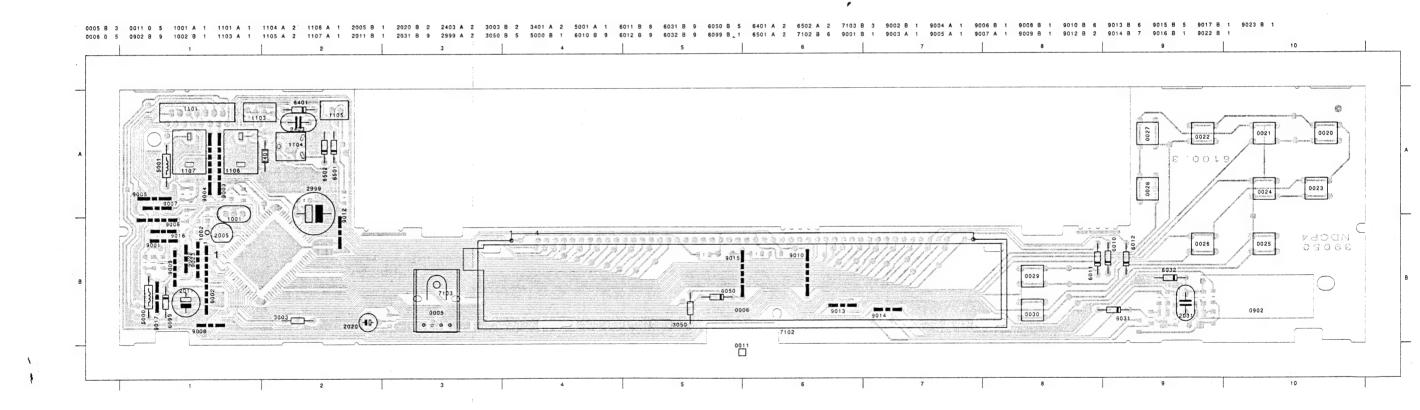
OPERATING PANEL MDCP3, MDCB1

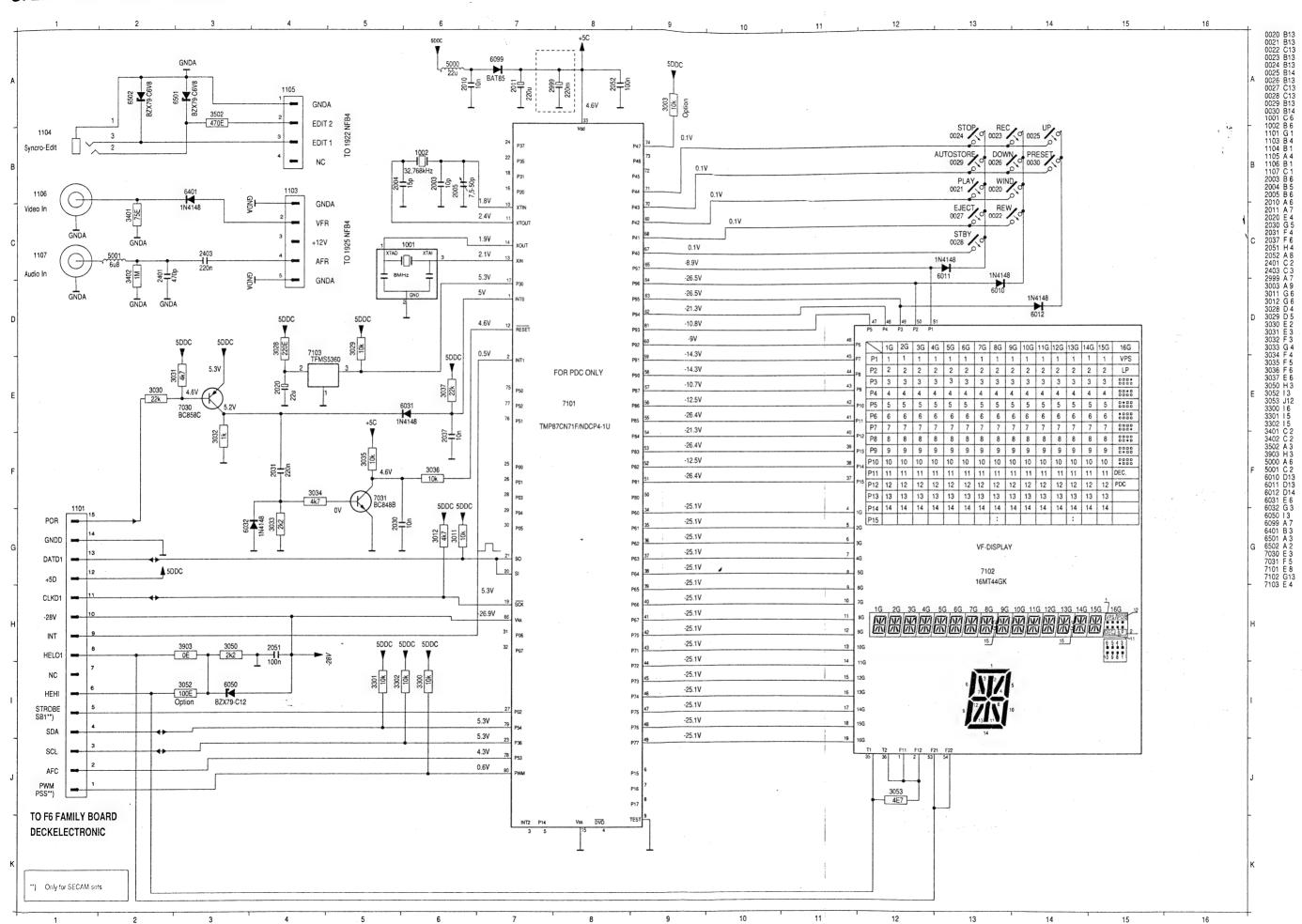




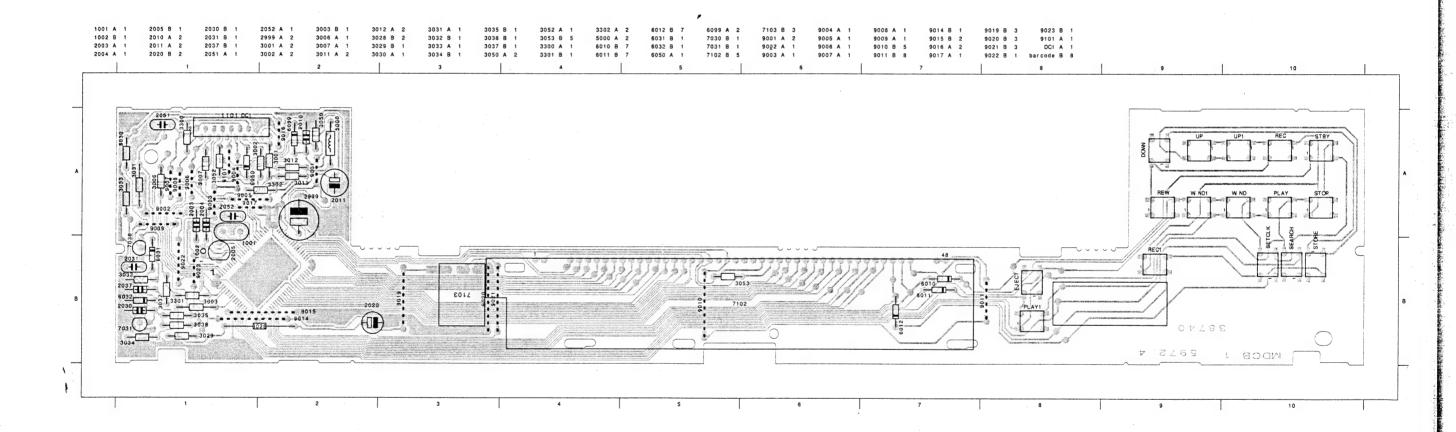
4513

OPERATING PANEL NDCP4

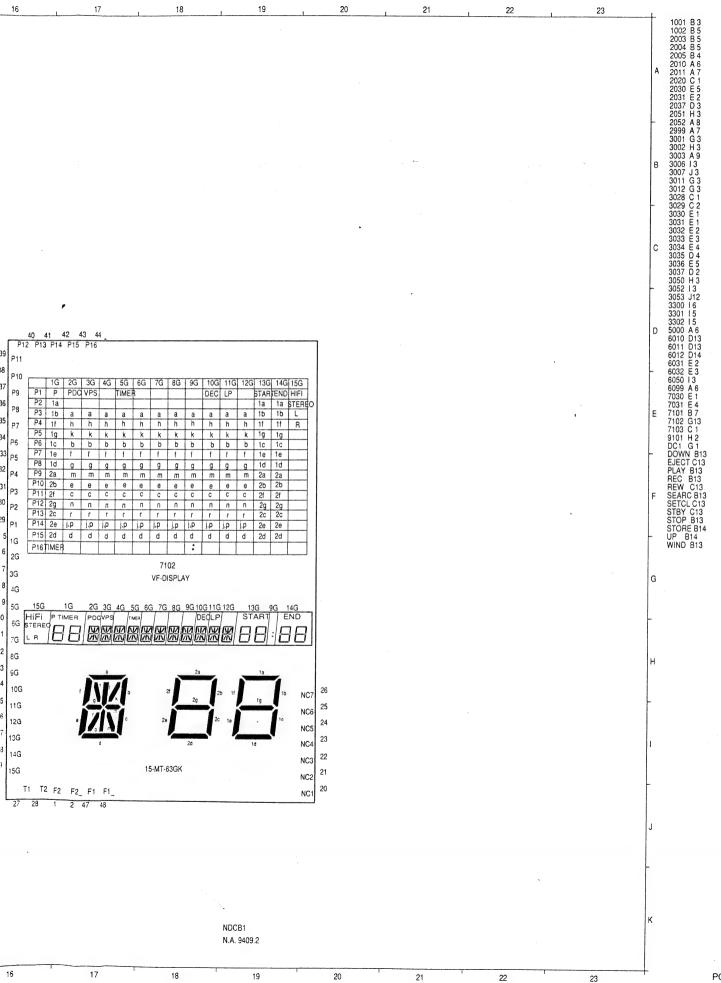




OPERATING PANEL NDCB1

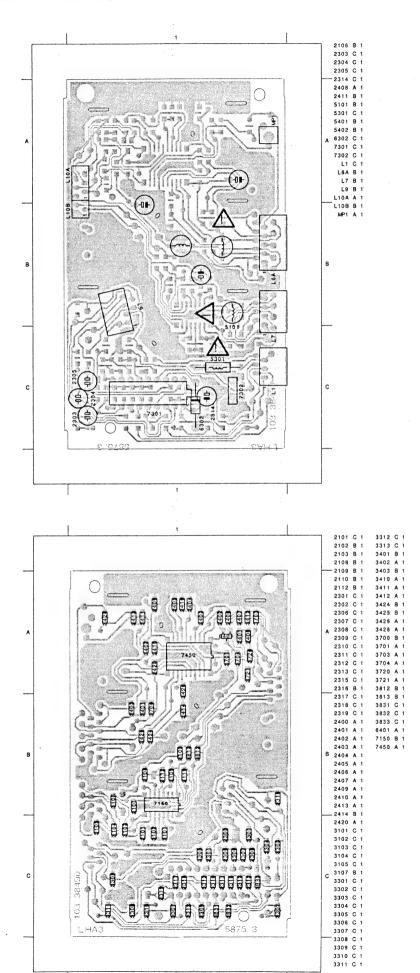


74517

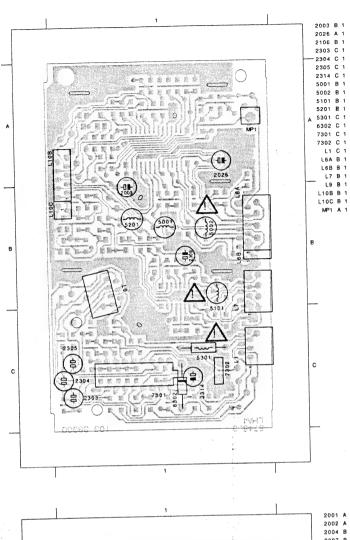


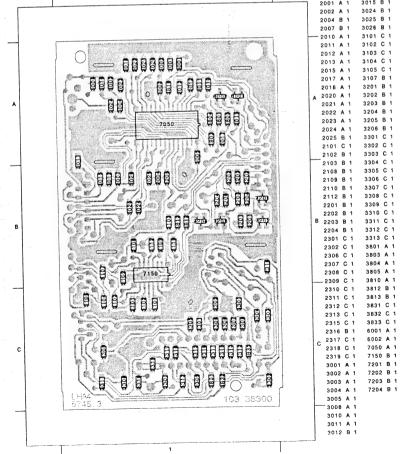
PCS 74519

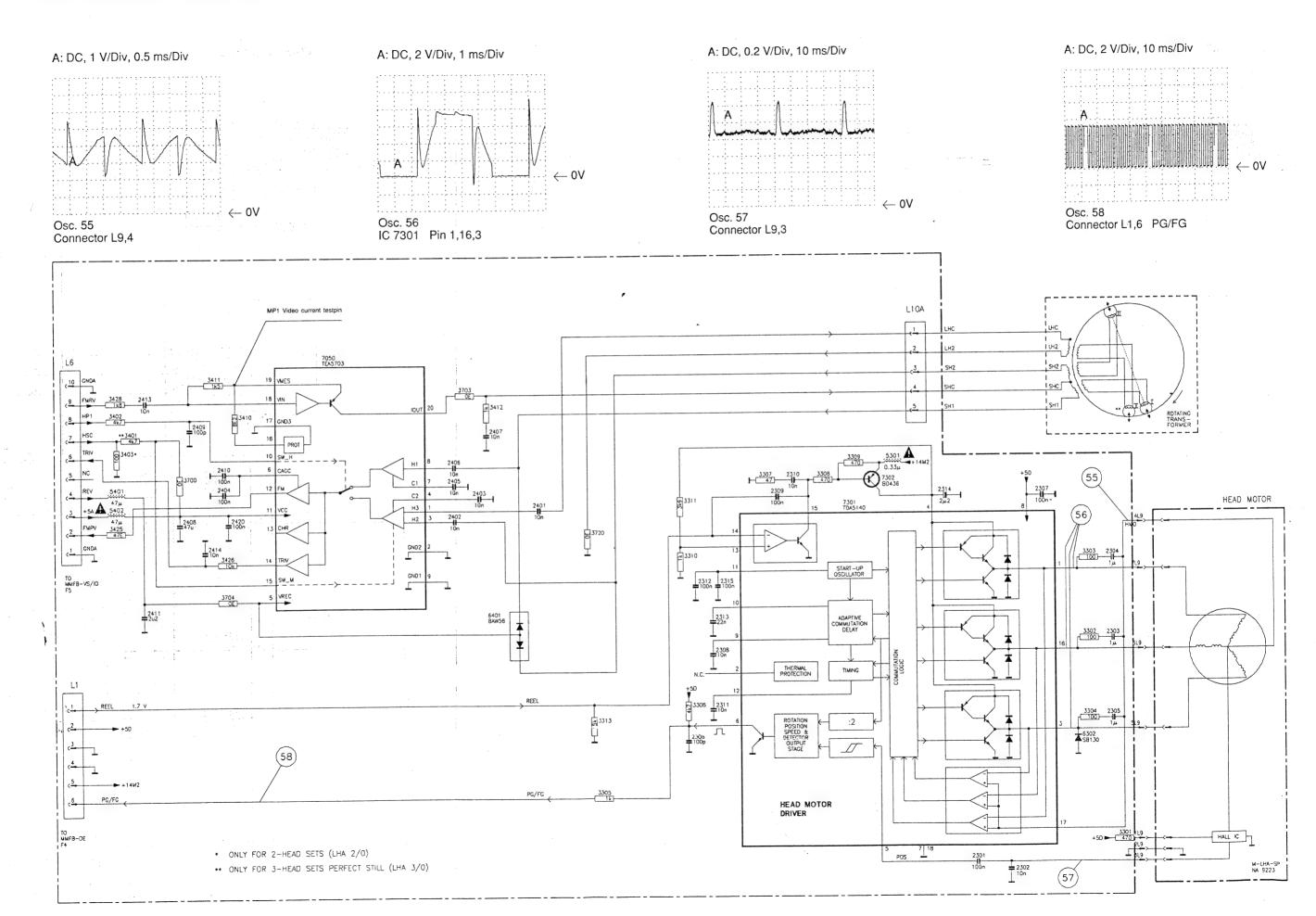
3-24 HEAD AMPLIFIER LHA4/0



HEAD AMPLIFIER LHA2/0, 3/0

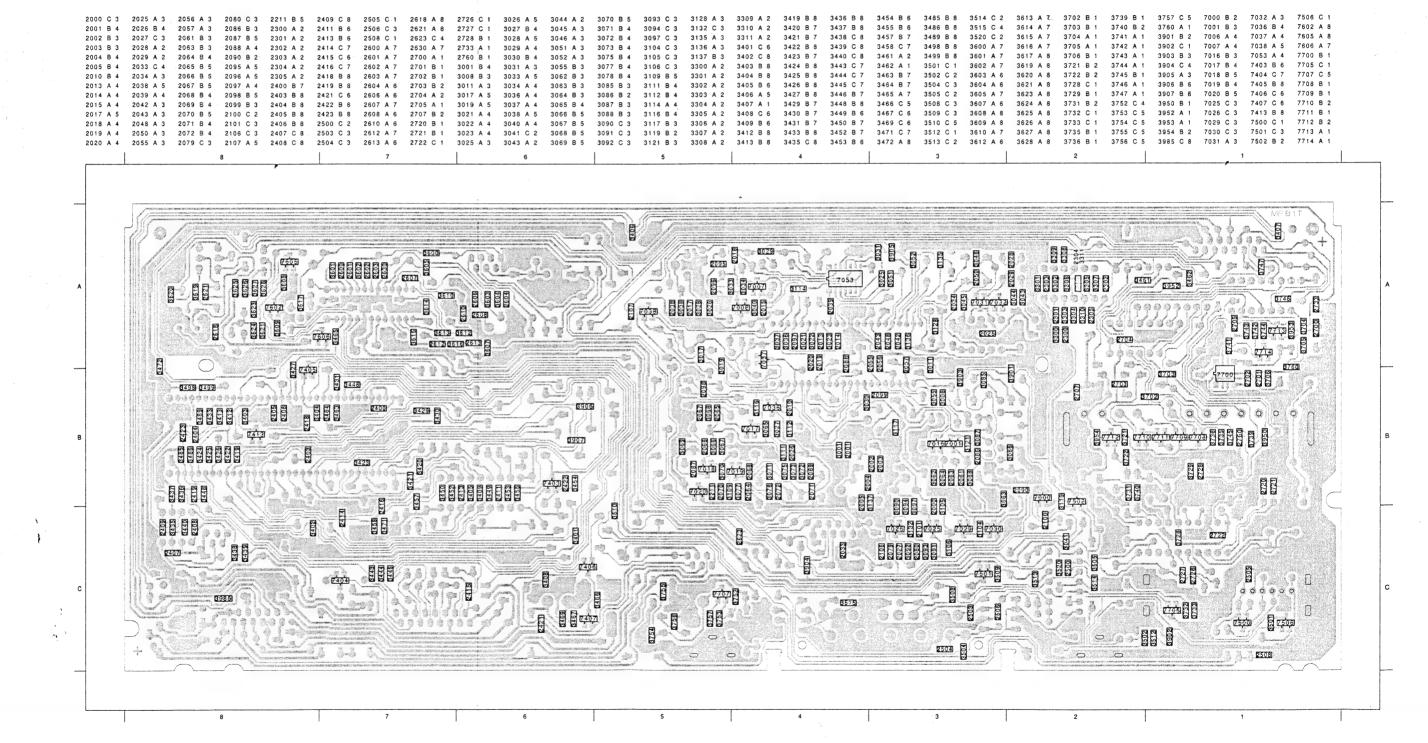


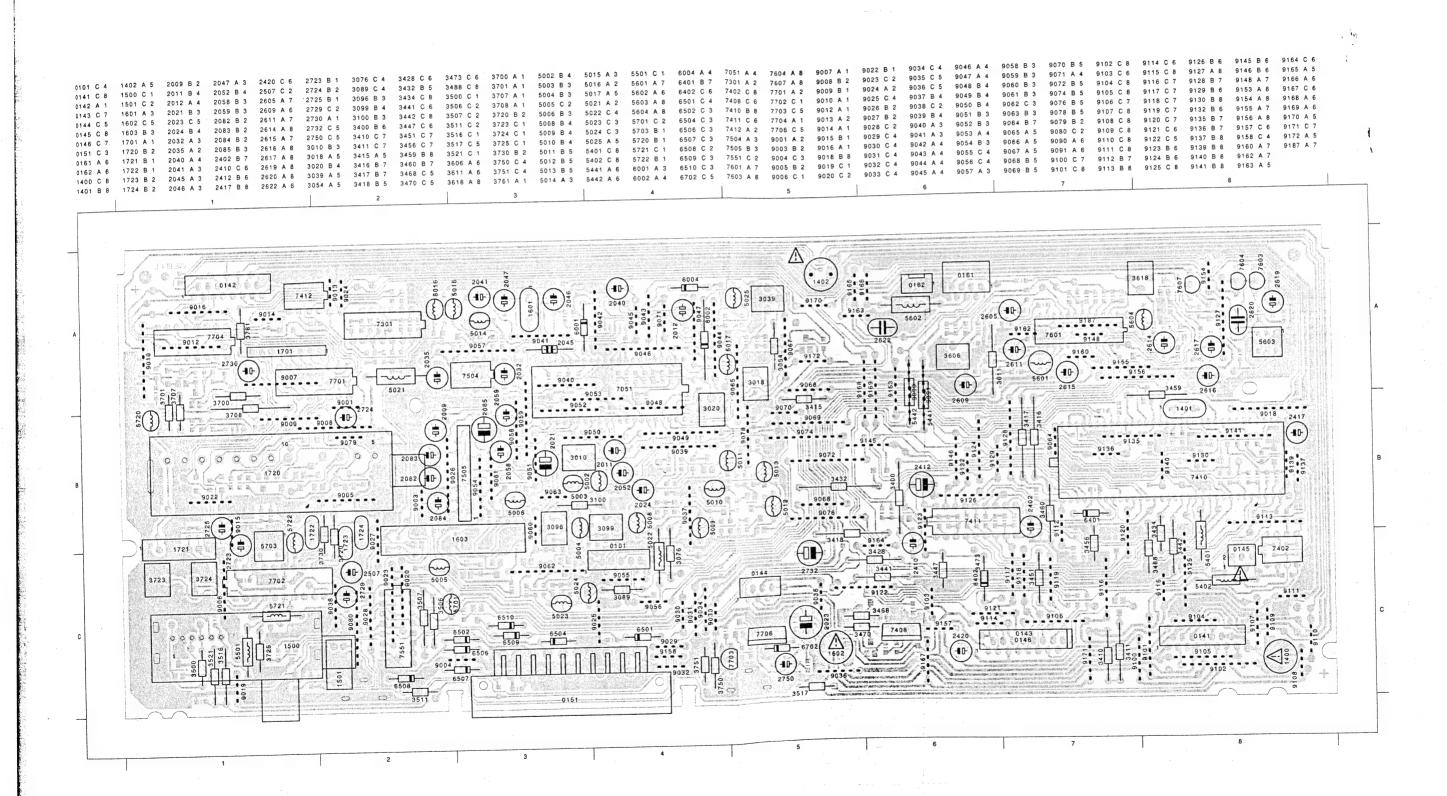




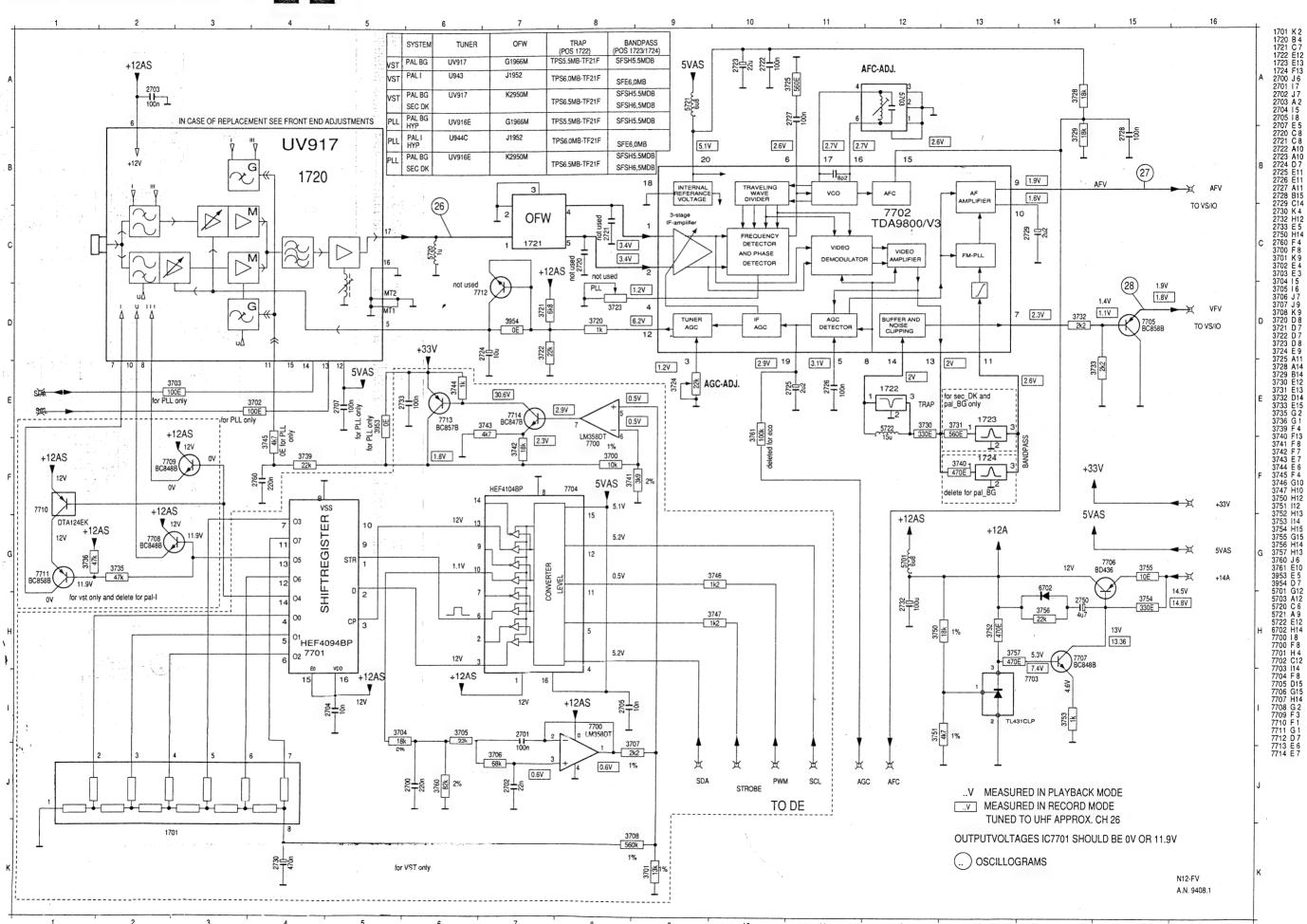
FAMILY BOARD N1 N2

"INSERTED COMPONENTS ARE DEPENDENT ON THE SET TYPE"





PCS 7452%



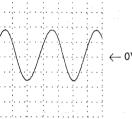
FAMILY BOARD AUDIO LINEAR - AL

/Div, 10 ms/Div

← 0√

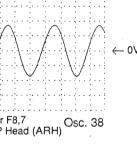
F8,1 Osc. 36

V/Div, 5 us/Div

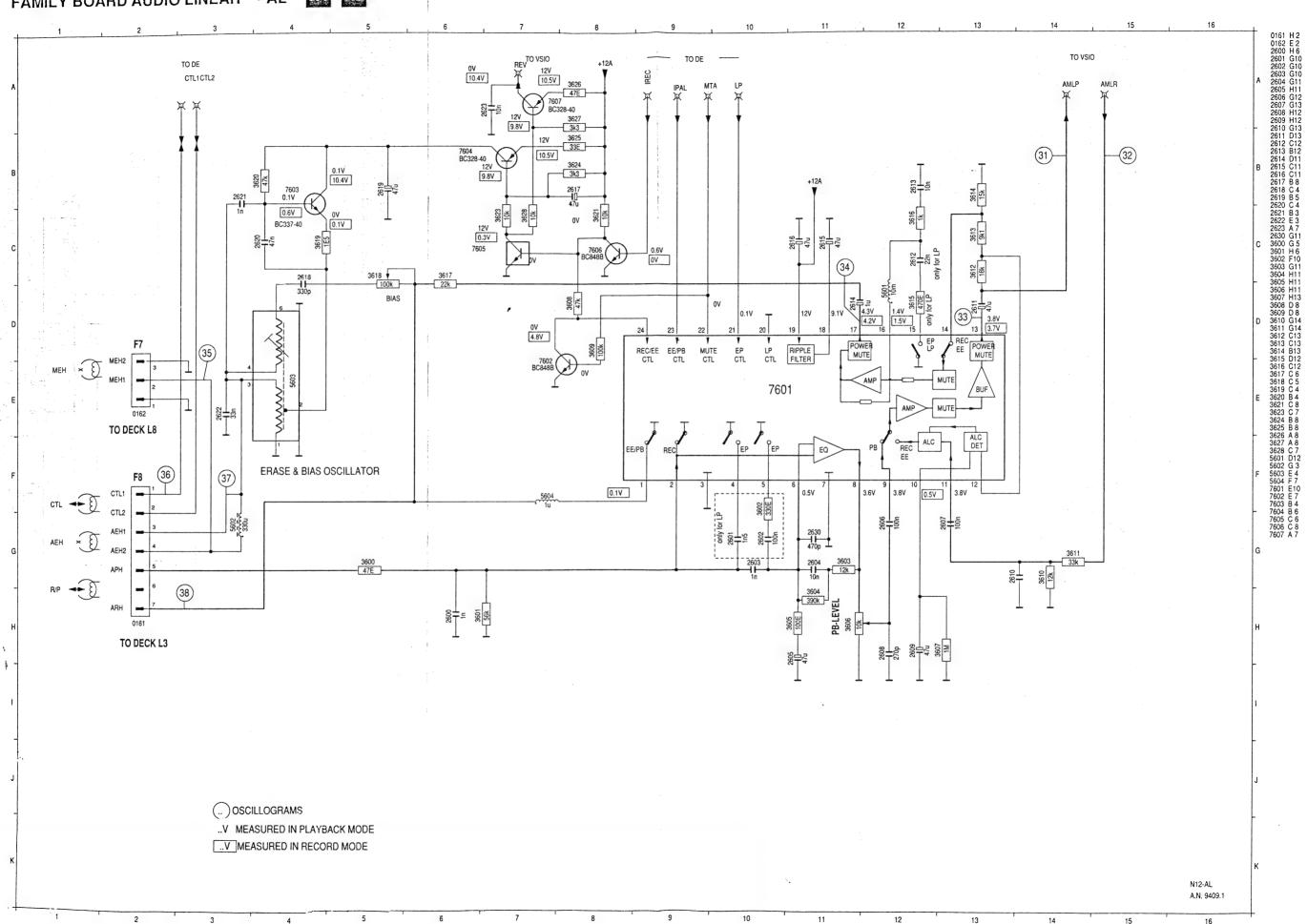


F8,3 Osc. 37 se head (AEH1)

V/Div, 5 us/Div

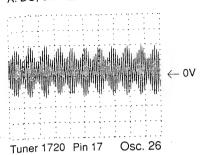


rams are measured in Record .

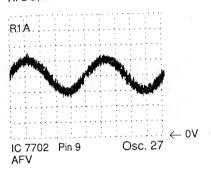


OSCILLOGRAMS FRONTEND - FV

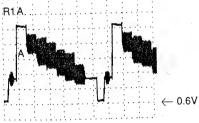
A: DC, 0.1 V/Div 0.2 us/Div



A: DC, 0.5 V/Div 0.2 ms/Div



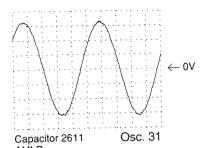
A: DC, 0.2 V/Div 10 us/Div



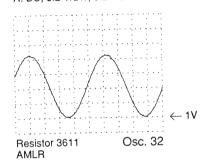
Transistor 7705-Emitter Osc. 28

OSCILLOGRAMS AUDIO LINEAR - AL

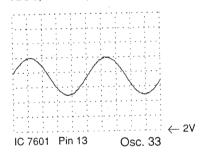
A: DC, 0.2 V/Div, 0.2 ms/Div



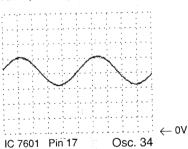
A: DC, 0.2 V/Div, 0.2 ms/Div



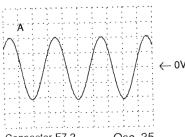
A: DC, 0.5 V/Div, 0.2 ms/Div



A: DC, 1 V/Div, 0.2 ms/Div

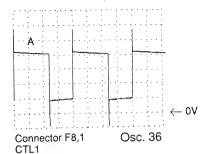


A: DC, 0.1 V/Div, 5 us/Div

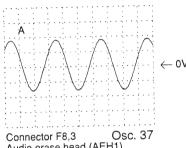


Connector F7,2 Osc. 35 Main erase head (MEH1)

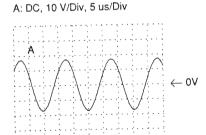
A: DC, 1 V/Div, 10 ms/Div



A: DC, 20 V/Div, 5 us/Div



Audio erase head (AEH1)

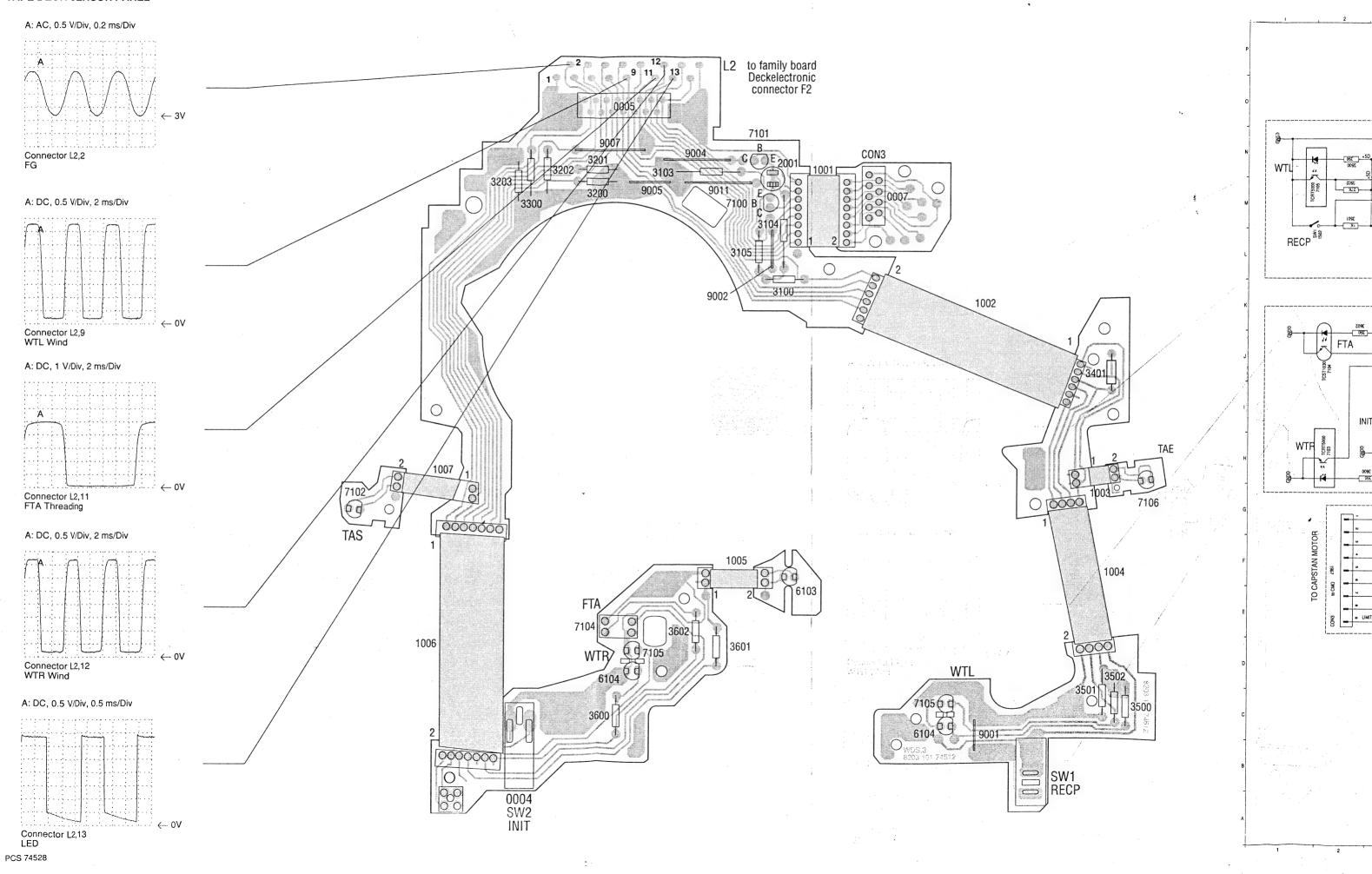


Connector F8,7 Audio R/P Head (ARH)

Oscillograms are measured in Position Record.

FAN

TAPE DECK SENSOR PANEL



0.6 OV

24 REC/EE CTL

OSCILLOGR

A: DC, 1 V/Div, 10 m Connector F4,1 PG/FG

Connector F3,13 LED

Connector F3,12

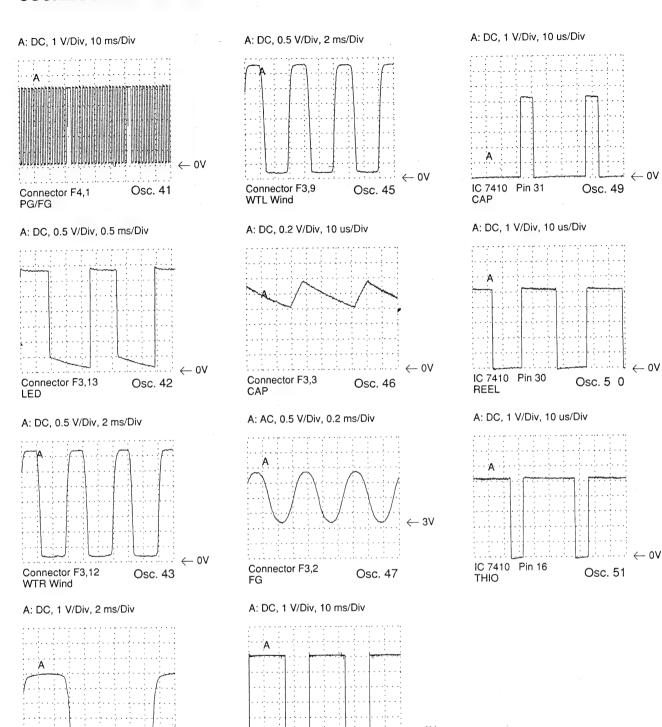
Connector F3,11 FTA Threading

OSCILLOGRAMS DECKELECTRONIC - DE

Connector F3,11

FTA Threading

Osc. 44

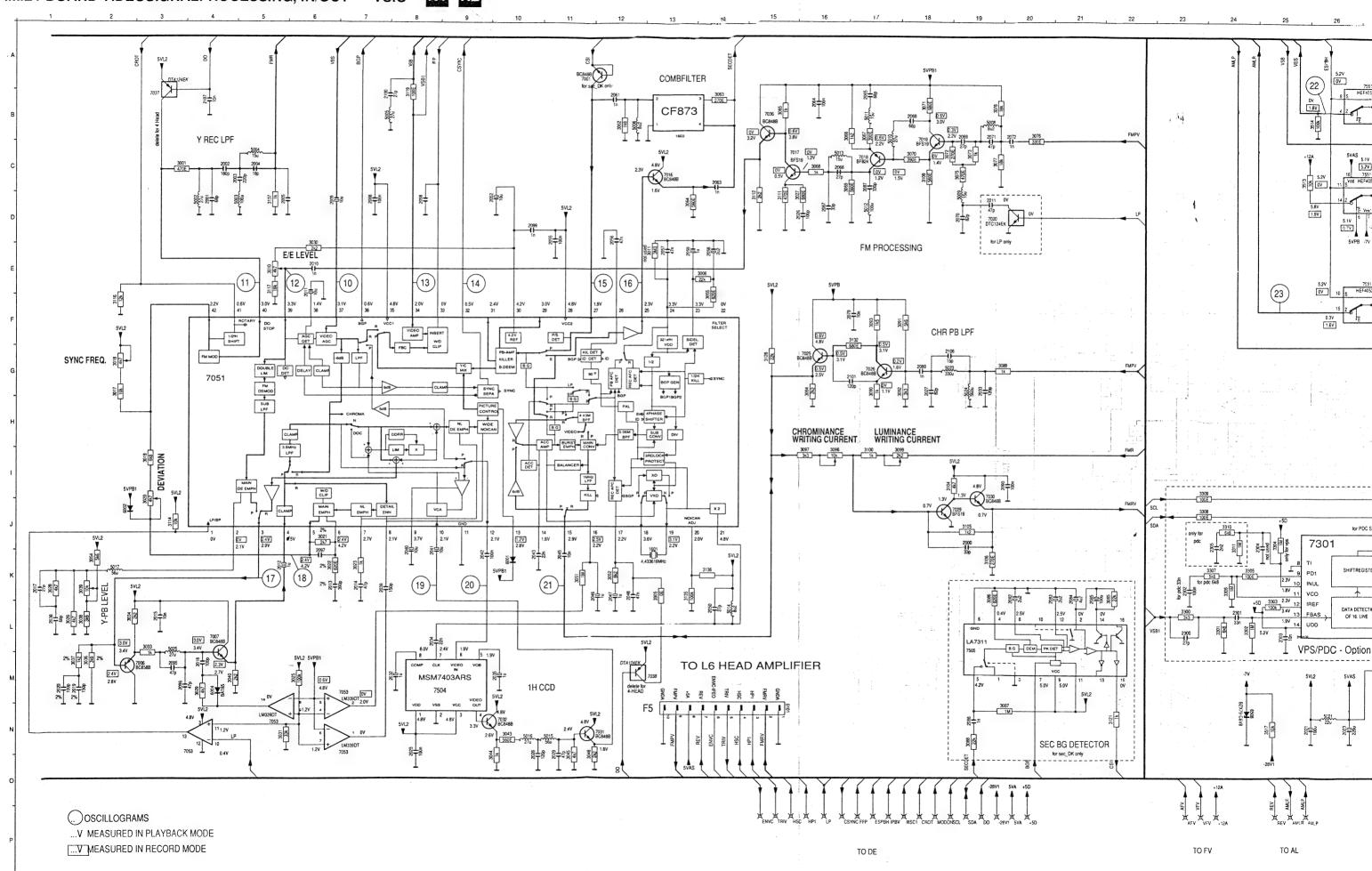


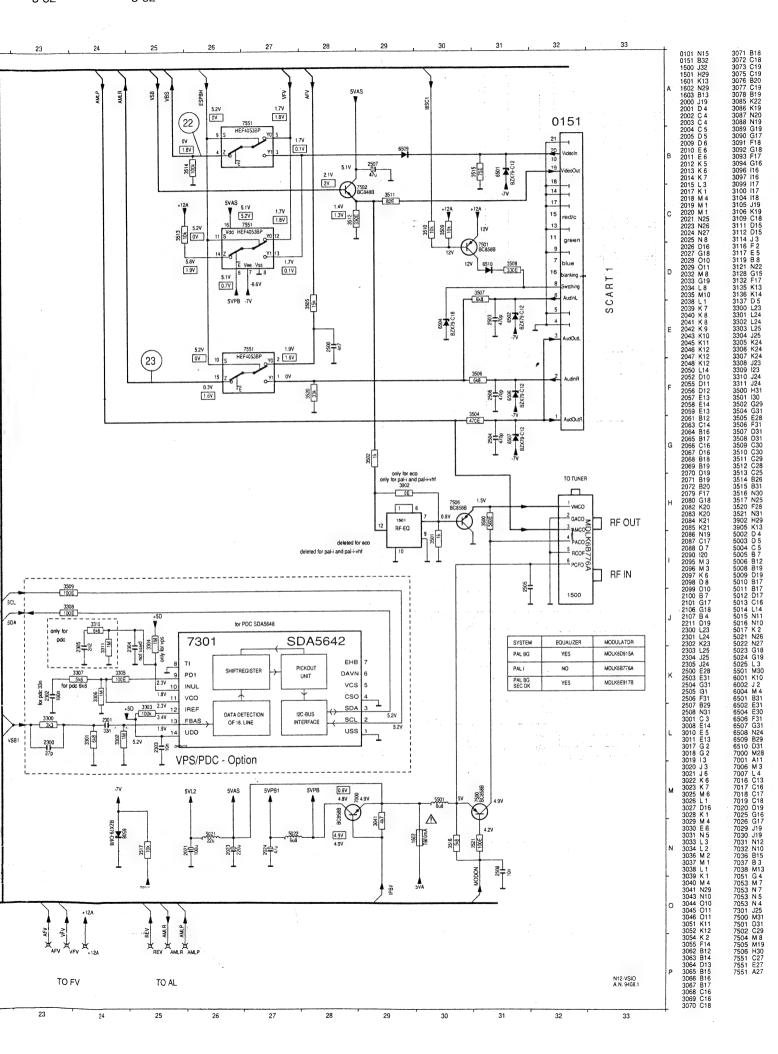
IC 7411 Pin 2

CTL1 REC

Osc. 48

3-32





A: AC, 0.2 V/Div , 2 us/Div

← 2.8V

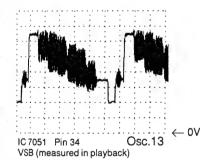
Osc. 11

A: AC, 50 mV/Div , 20 us/Div

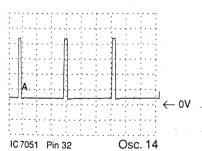
IC 7051 Pin 40

IC 7051 Pin 39 Osc. 12 (measured in playback)

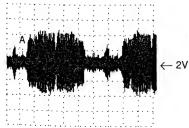
A: DC, 0.5 V/Div 10 us/Div



A: DC, 1.0 V/Div , 20 us/Div



A: AC, 50 mV/Div , 5 ms/Div



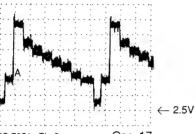
IC 7051 Pin 27 Osc. 15 (measured in playback)

A: DC, 0.5 V/Div , 10 us/Div



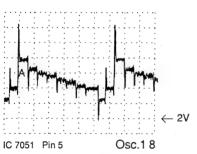
IC 7051 Pin 25 Osc. 16 (measured in playback)

A: AC, 0.1 V/Div , 10 us/Div

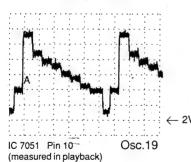


IC 7051 Pin 3 Osc. 17 (measured in playback)

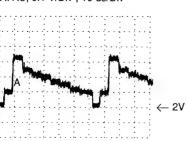
A: DC, 0.2 V/Div , 10 us/Div



A: AC, 0.1 V/Div , 10 us/Div

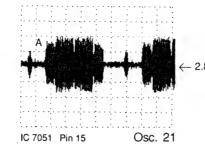


A: AC, 0.1 V/Div , 10 us/Div

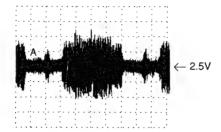


IC 7051 Pin 12 Osc. 20 (measured in playback)

A: AC, 0.1 V/Div , 10 us/Div

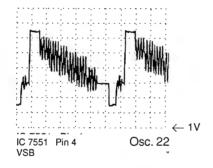


A: AC, 0.2 V/Div , 10 us/Div



IC 7051 Pin 15 Osc. 21 (measured in playback)

A: DC, 0.2 V/Div 10 us/Div



A: DC, 0.2 V/Div 0.2 ms/Div

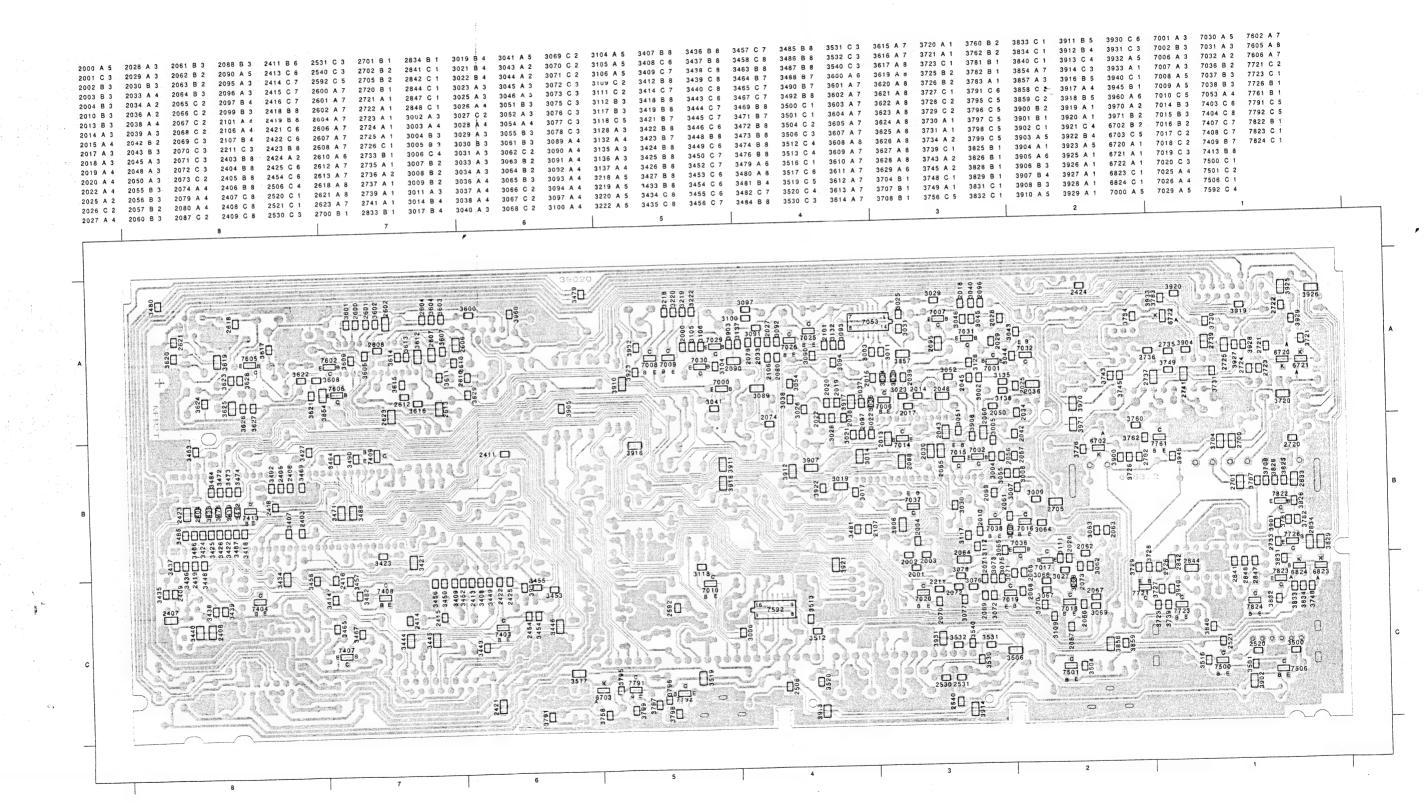
IC 7551	Pin 15	Osc. 23
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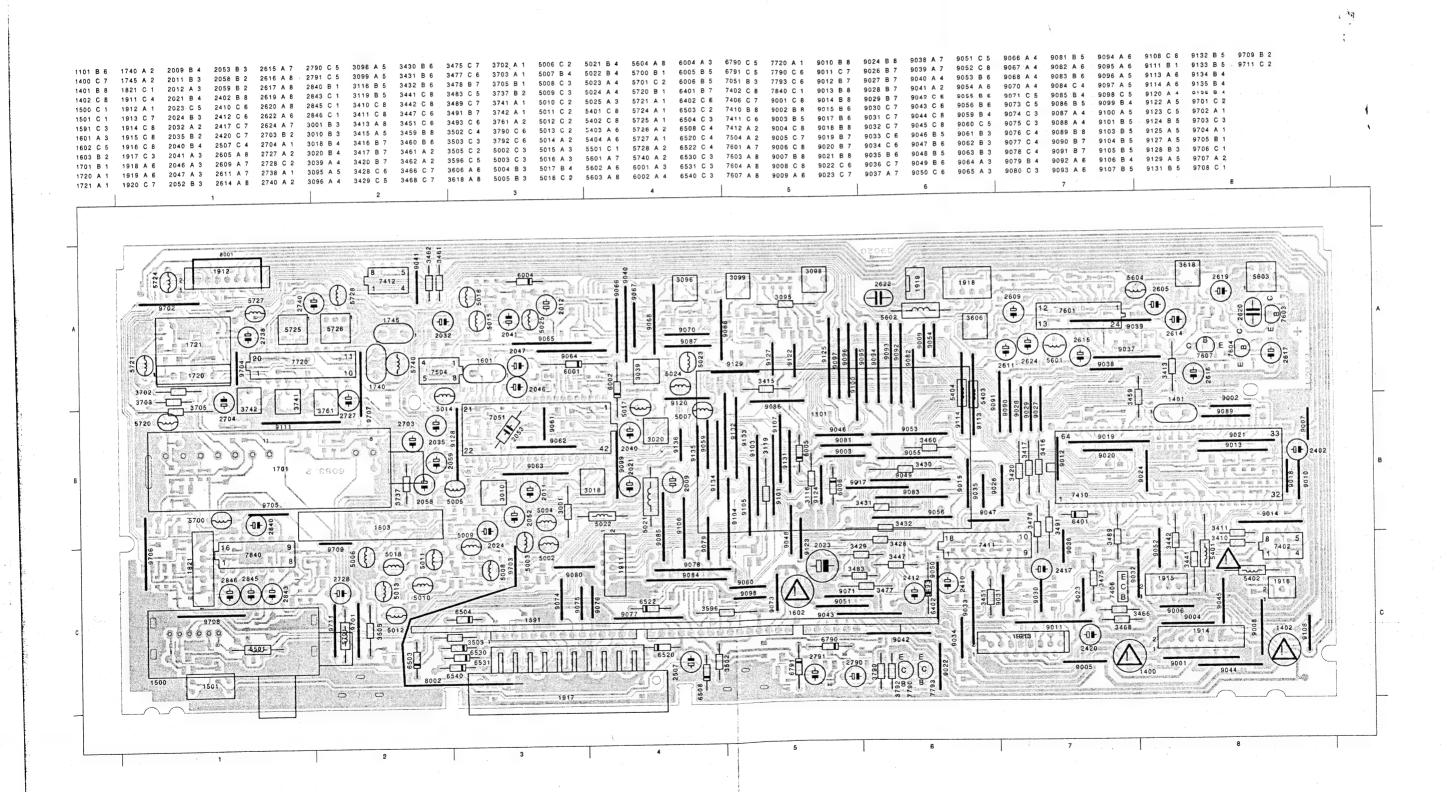
REMARKS:			

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PCS 74531

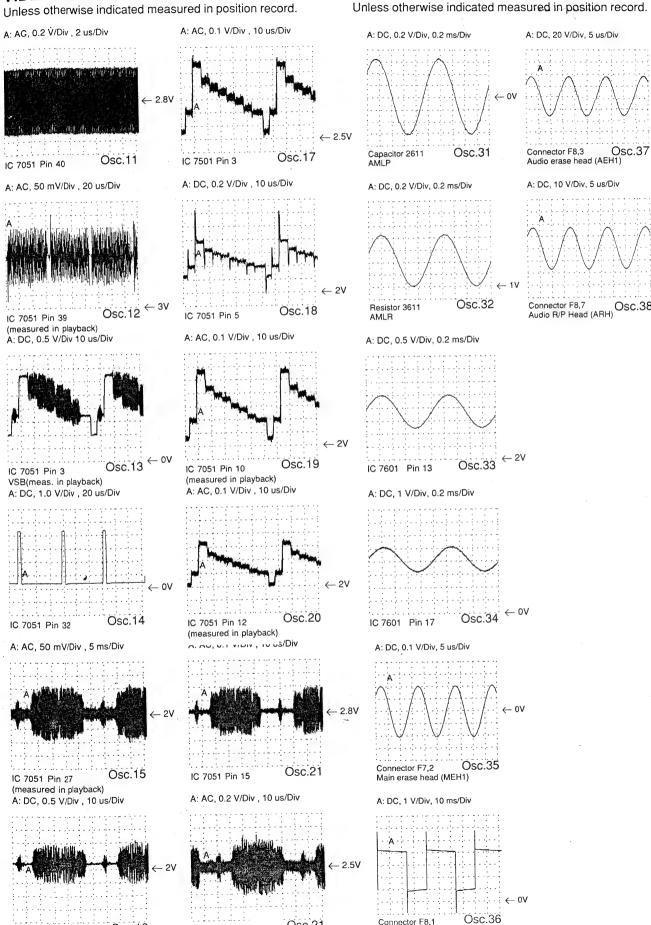
FAMILY BOARD N3 N5





OSCILLOGRAMS VIDEOSIGNALPROCESSING -VS

Unless otherwise indicated measured in position record.



Osc.37

OSCILLOGRAMS

Connector F8,1

Osc.21

IC 7051 Pin 15

(measured in playback)

Osc.16

IC 7051 Pin 25

(measured in playback)

AUDIO LINEAR -AL

PCS 74536

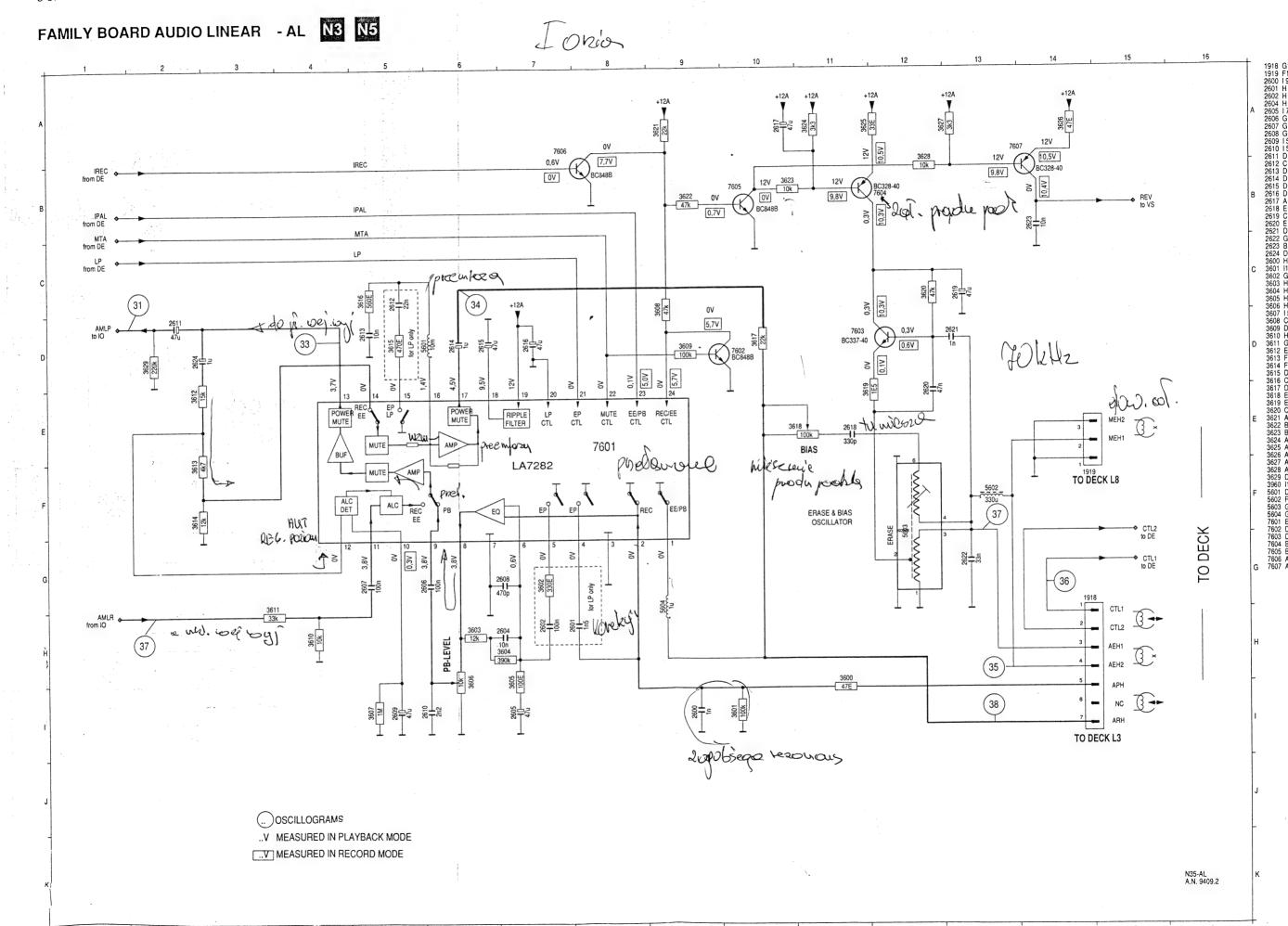
in position record.

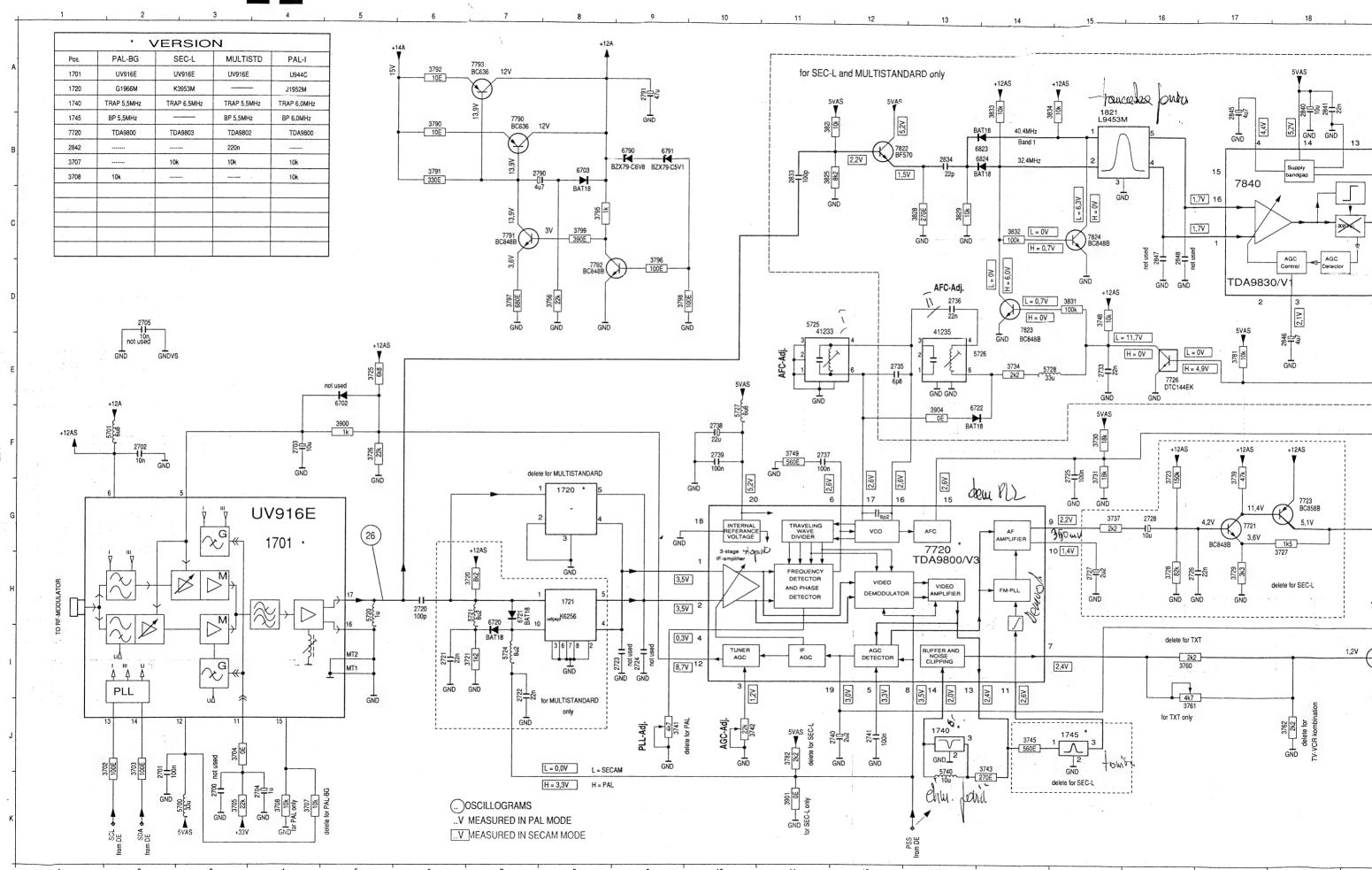
ctor F8,3 Osc.37 erase head (AEH1)

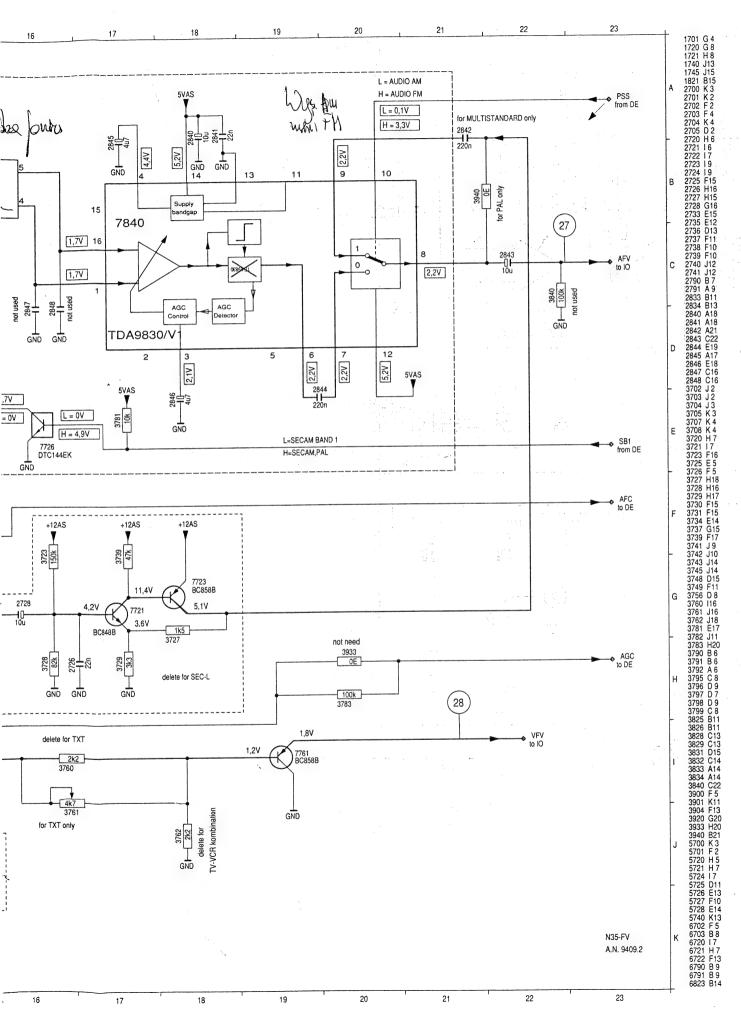
ector F8,7 Osc.38 R/P Head (ARH)

10 V/Div, 5 us/Div

20 V/Div, 5 us/Div



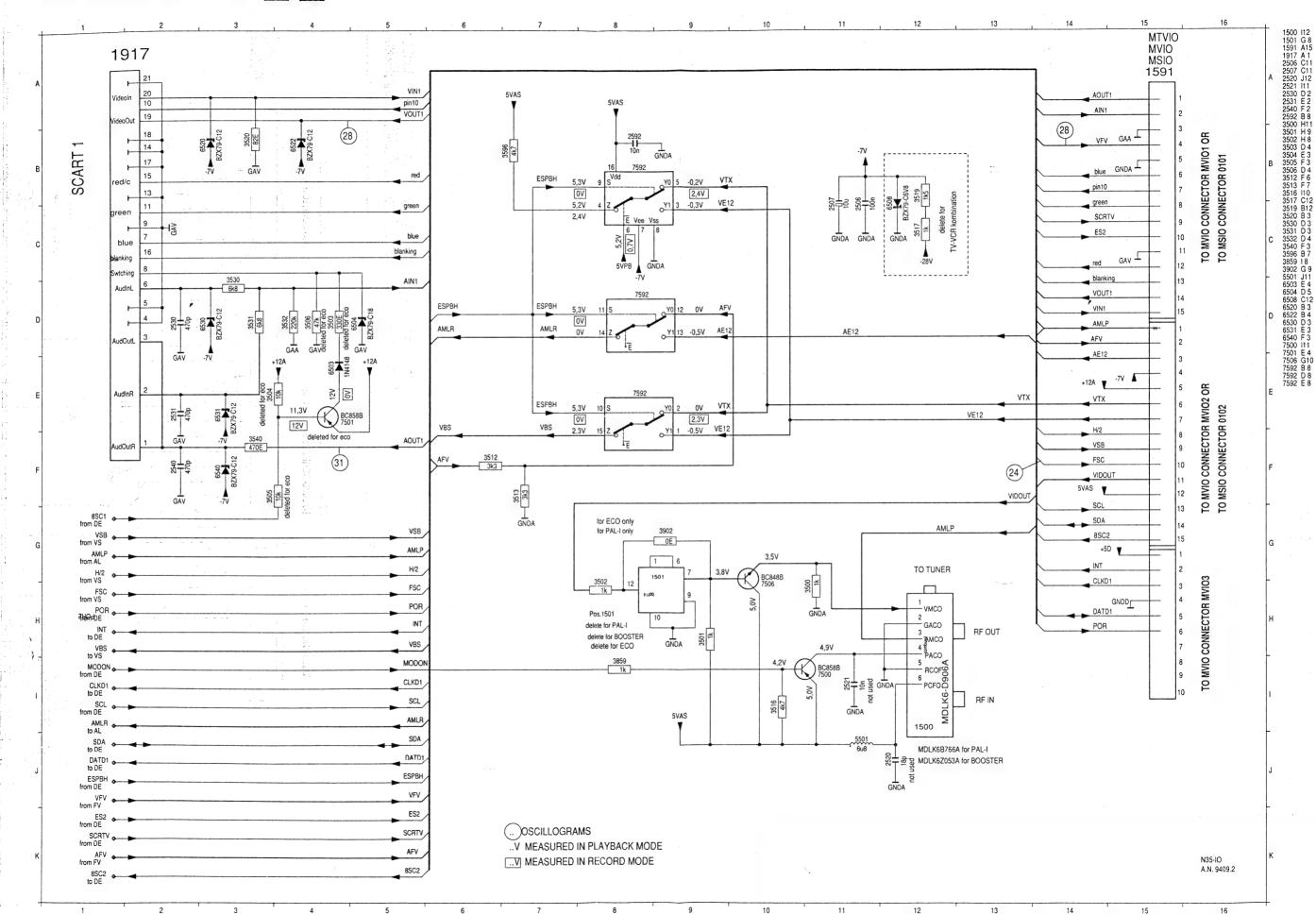




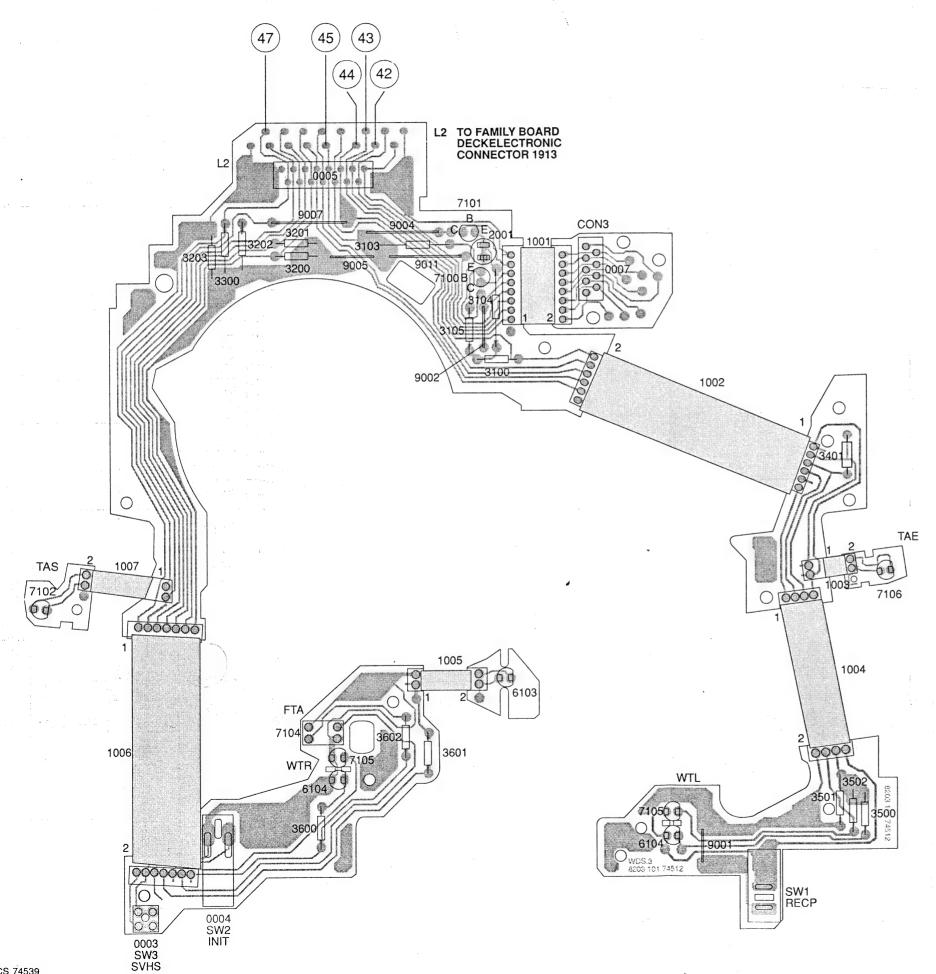
6824 B14 7720 H13 7721 G17 7723 G18 7726 E16 7761 I19 7790 B 7 7791 C 7 7792 D 8 7793 A 7 7822 B12 7823 E14 7824 C15 7840 C17

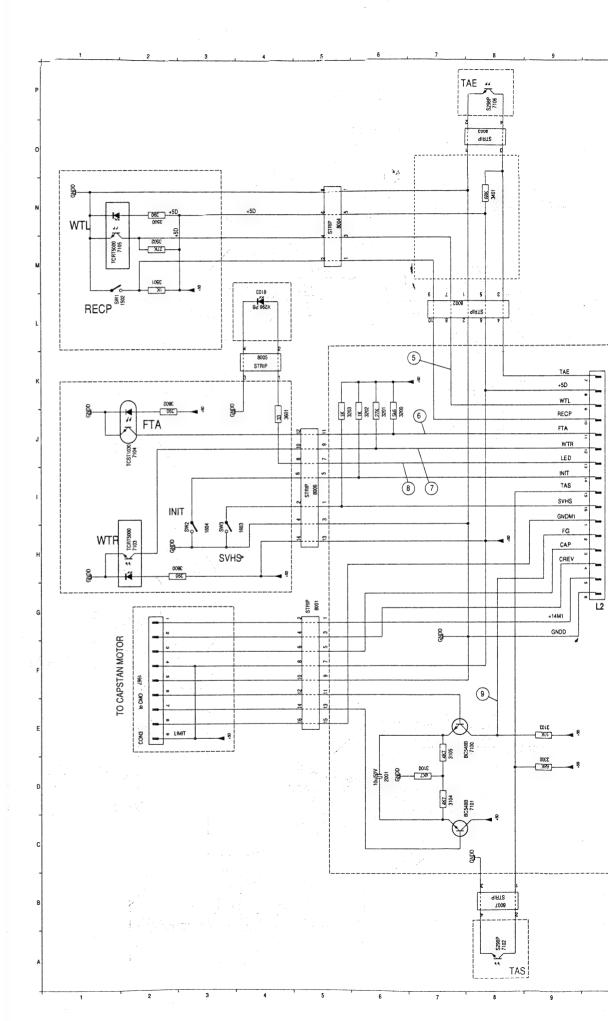
OSCILLOGRAMS FRONT END IN/OUT	DEMARKS -
A: AC, 20mV/Div, 100ns/Div	REMARKS:
$\bigwedge \bigwedge \bigwedge \bigwedge \bigvee \longleftrightarrow ov$	
Connector 1591 OSC.24 FSC	
A: DC, 0.1 V/Div 0.2 us/Div	
← ov	
alle alle alle alle alle alle alle alle	
Tuner 1720 Pin 17 OSC.26	
A: DC, 0.5 V/Div 0.2 ms/Div	
R1A.	
IC 7702 Pin 9 OSC.27 AFV	
A: DC, 0.2 V/Div 10 us/Div	
R1A.	
← 0.6V	
Transistor 7705-Emitt@SC.28	
VFV	
A: DC, 0.2 V/Div, 0.2 ms/Div	
Capacitor 2611 OSC.31 AMLP	
AMLP	

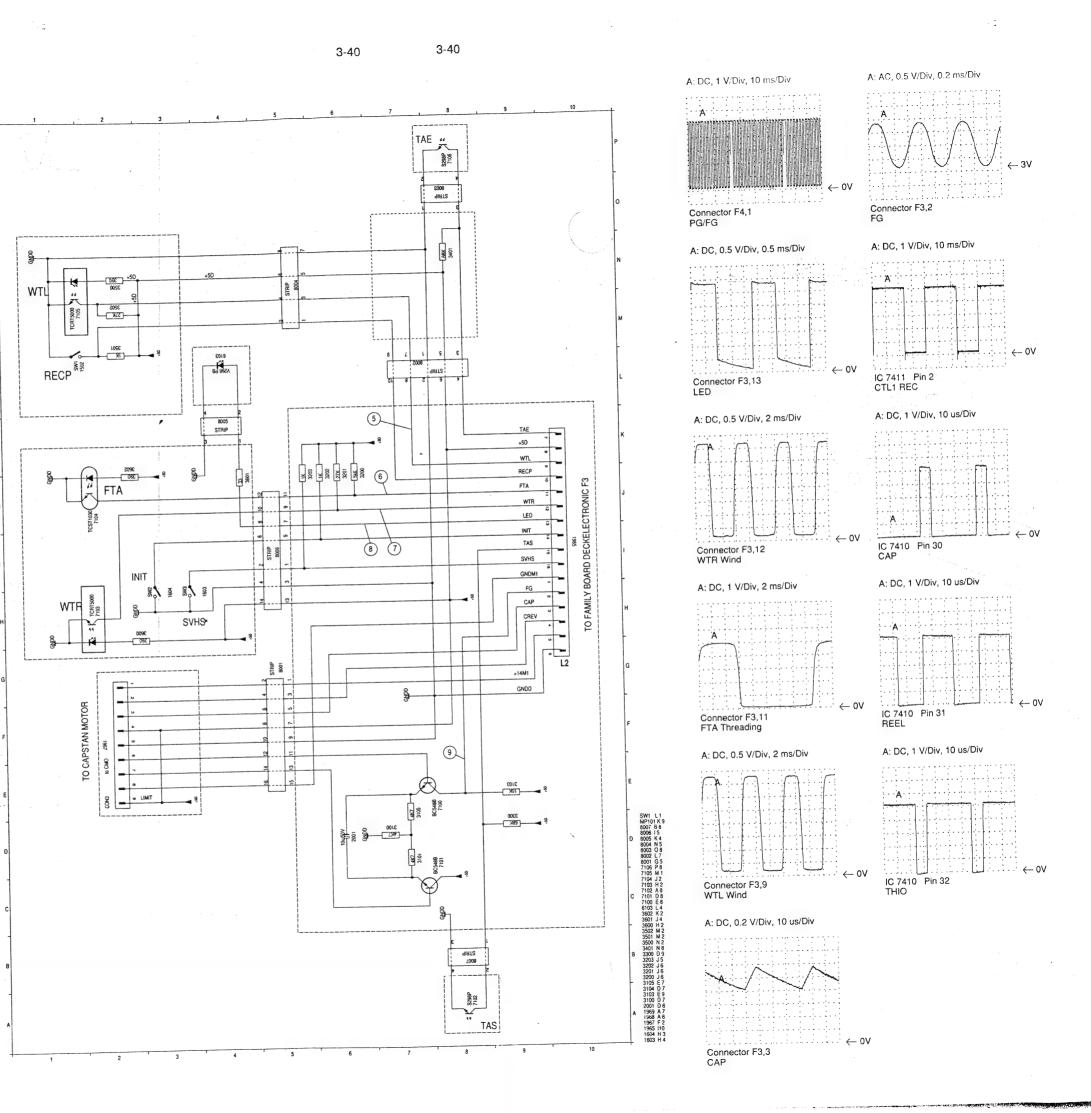
FAMILY BOARD IN/OUT - I/O 18 16



TAPE DECK SENSOR BOARD







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CON3

1002

WTL

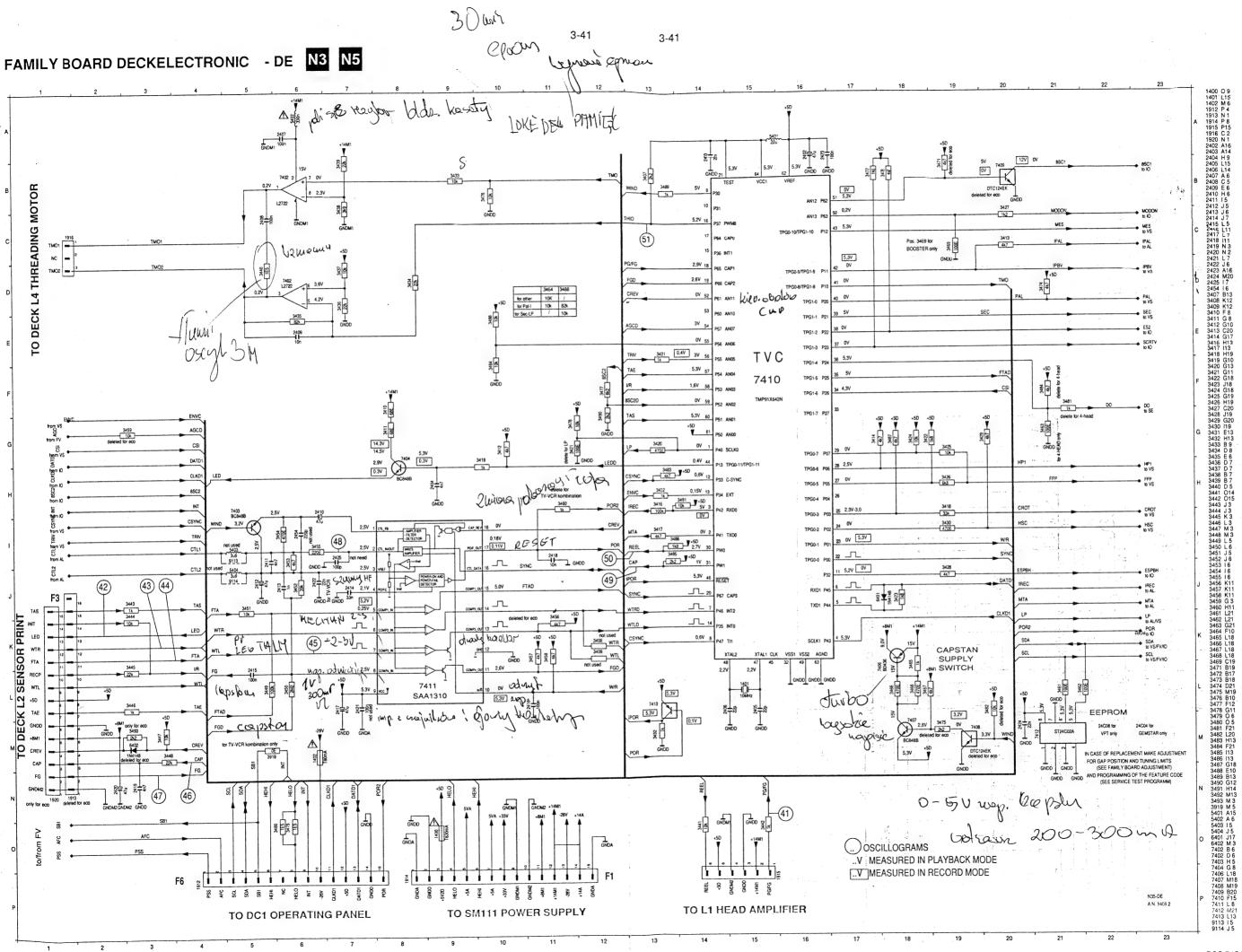
TAE

1004

3502

0000

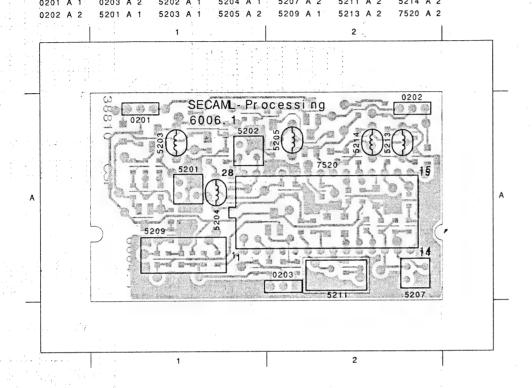
SW1 RECP



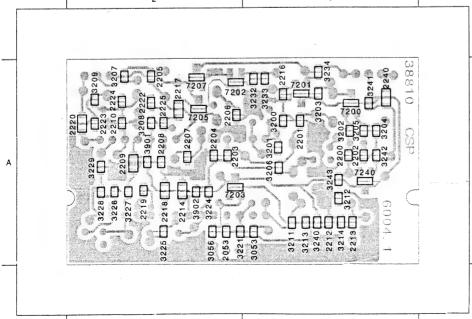
CHROMA SIGNAL SECAM PROCESSING BOARD CSP



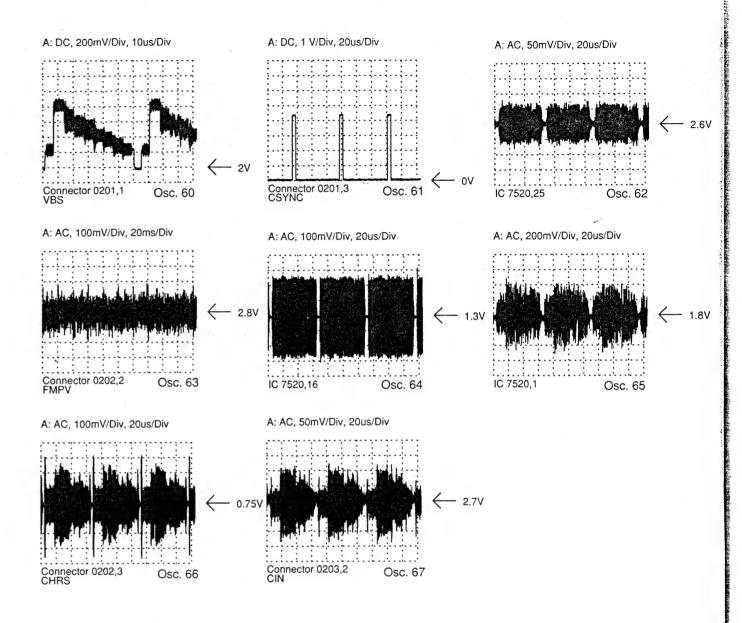




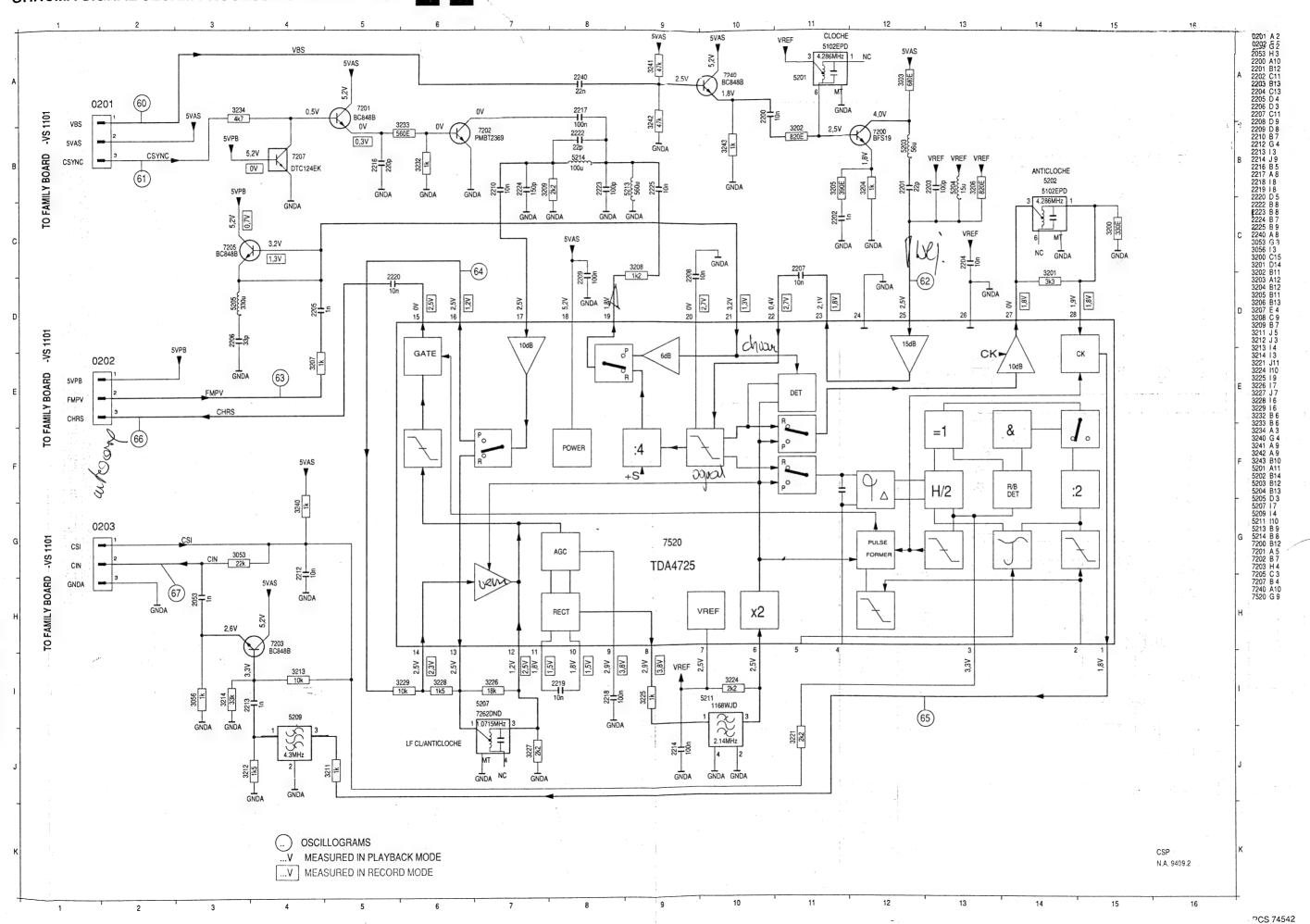
2210 A 2 2223 A 2 3204 A 1 3221 A 2 3240 A 1 2224 A 2 3205 A 1 3224 A 2 3241 A 1 3206 A 1 3225 A 2 3242 A 1 7240 A 1 3209 A 2 3228 A 2 3902 A 2 2205 A 2 2217 A 2 3056 A 2 2206 A 2 2218 A 2 3200 A 1 3211 A 1 3229 A 2 7200 A 1 2207 A 2 2219 A 2 3201 A 1 3212 A 1 3232 A 1 7201 A 1 2208 A 2 2220 A 2 3202 A 1 3213 A 1 3233 A 1 7202 A 2



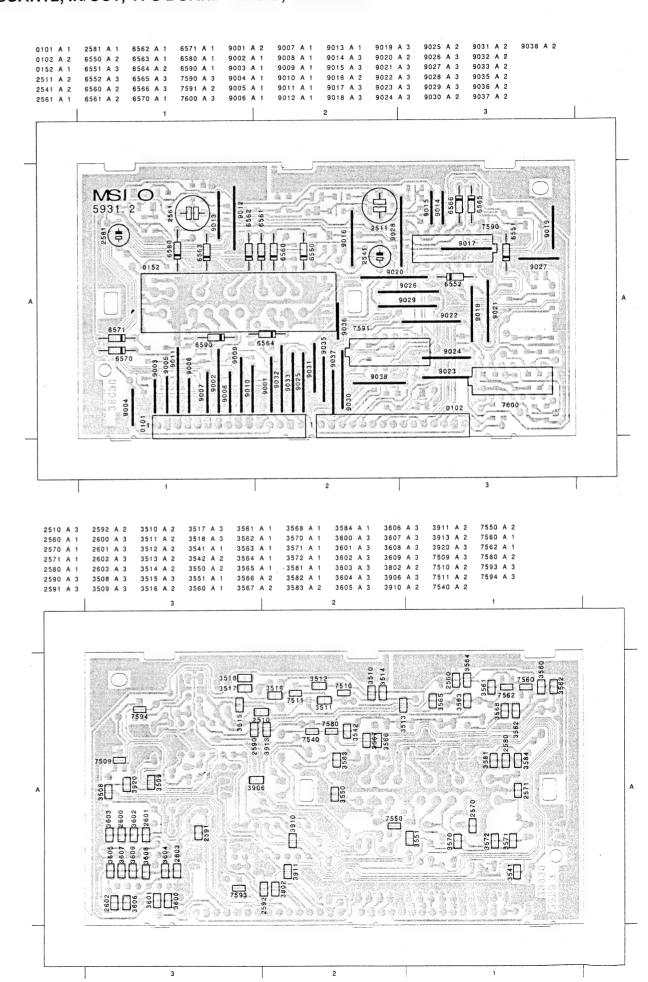
OSCILLOGRAMS CHROMA SECAM PRINT CSP



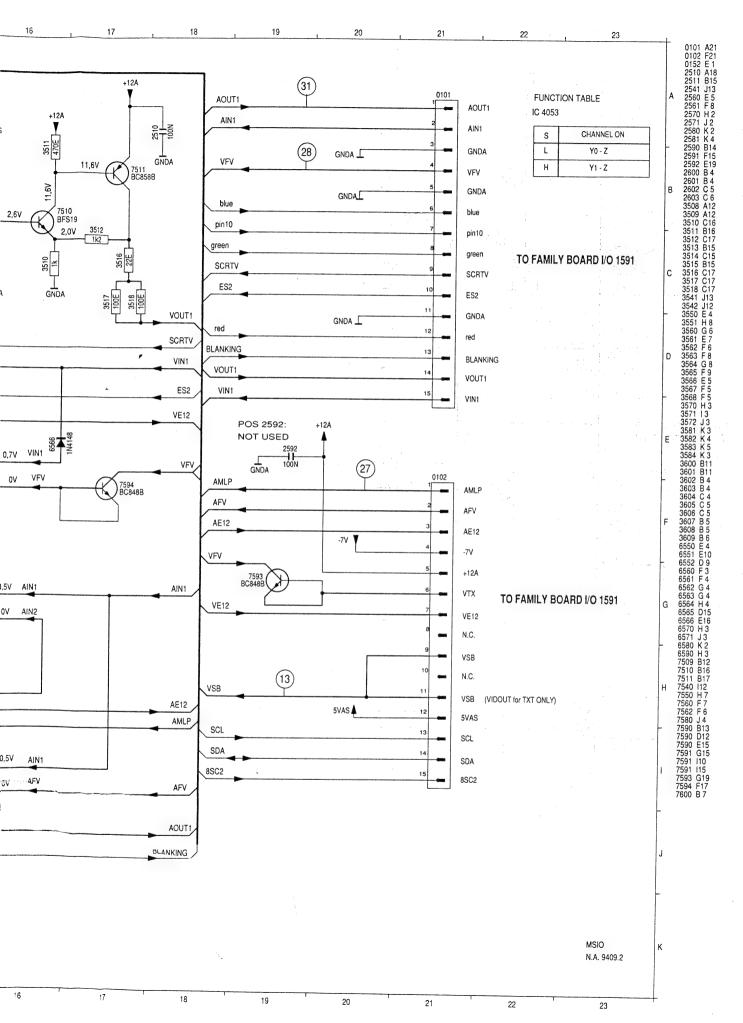
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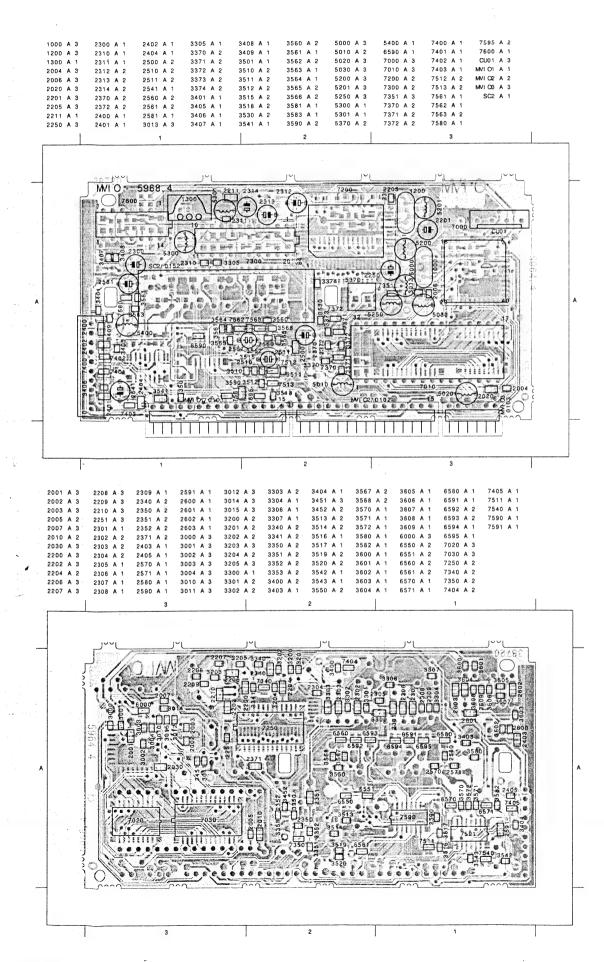


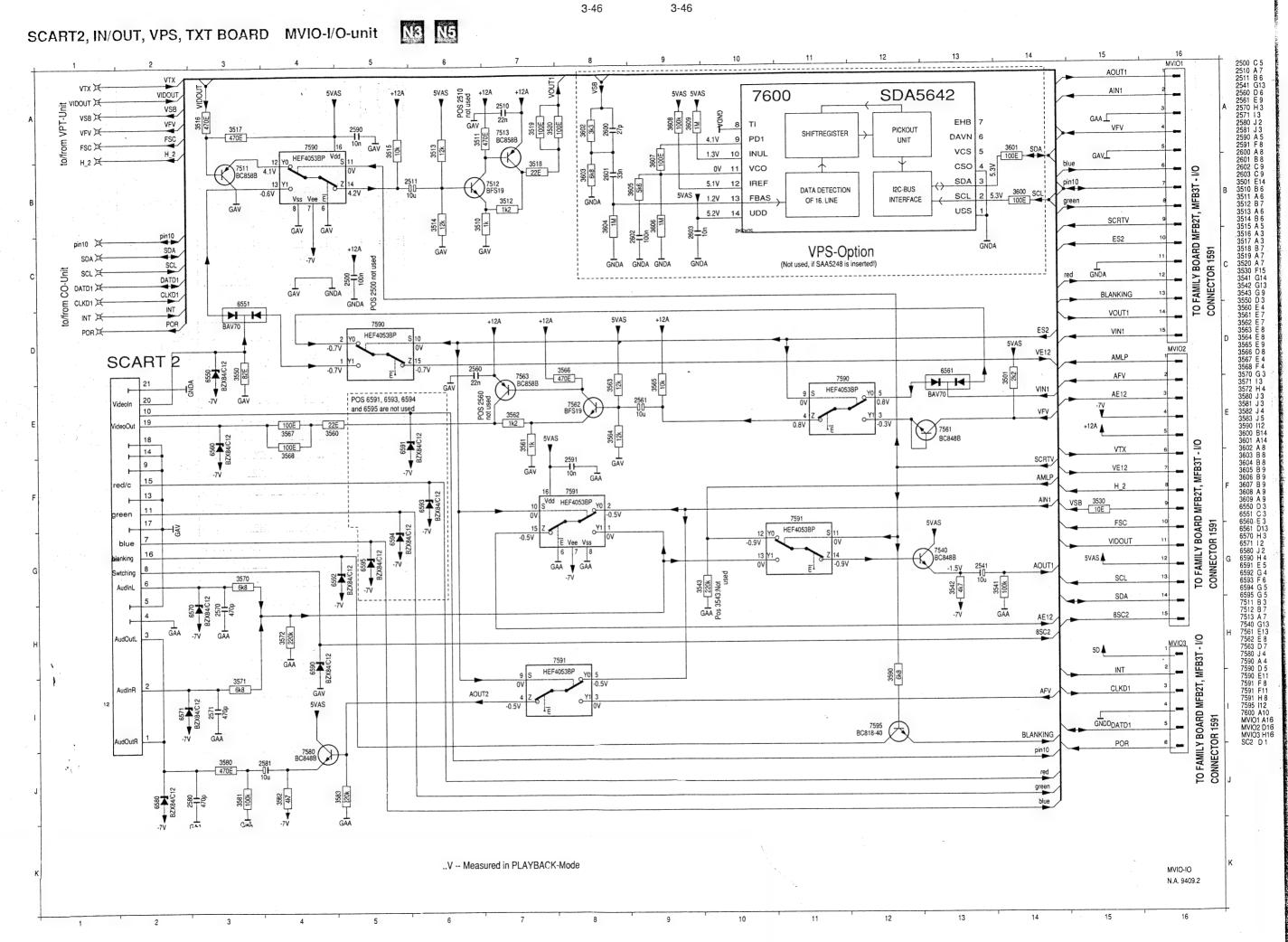
SCART2, IN/OUT, VPS BOARD MSIO, MSIO/VPS



3-45







PCS 74546

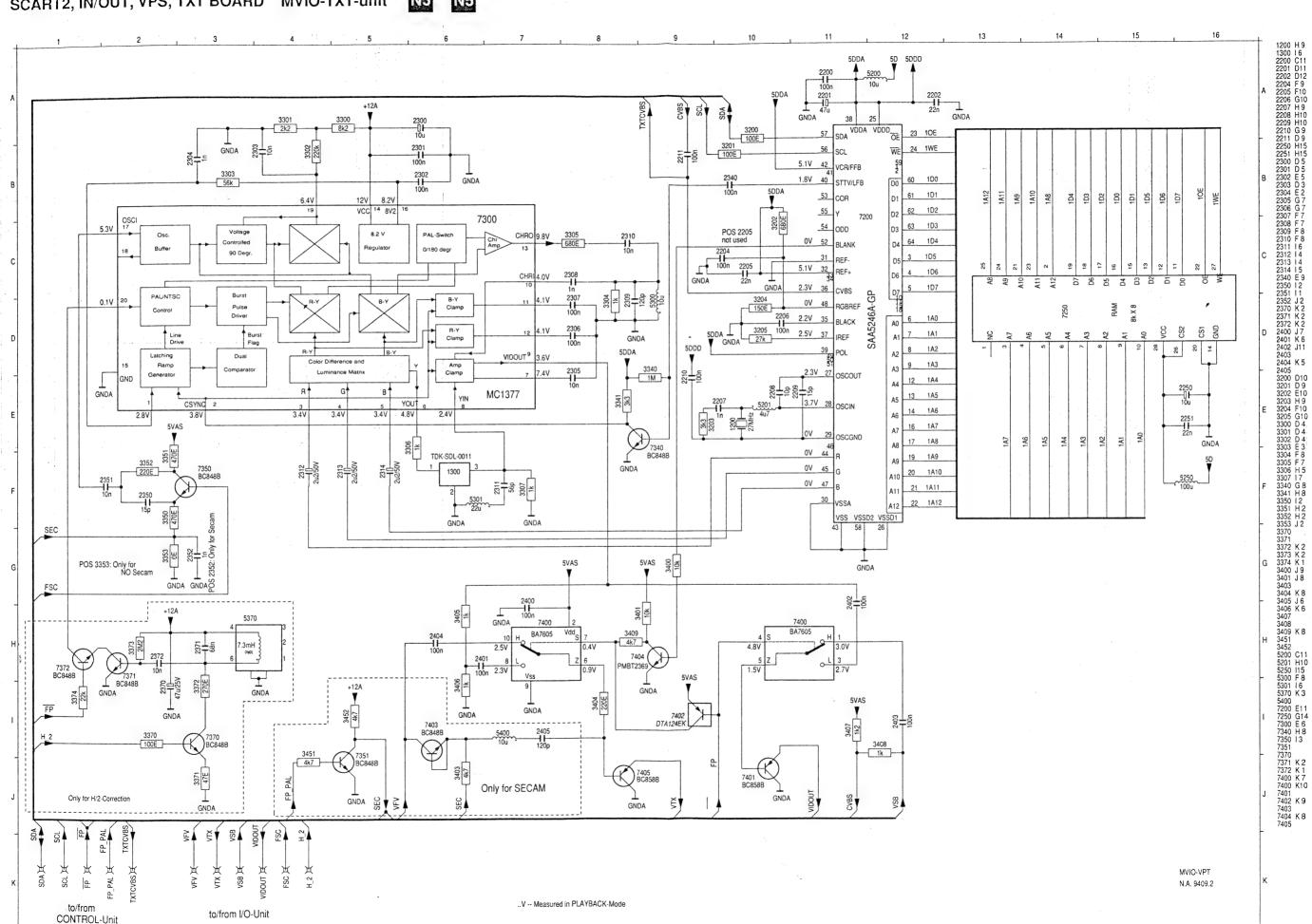
▼5DTX

CLKD1

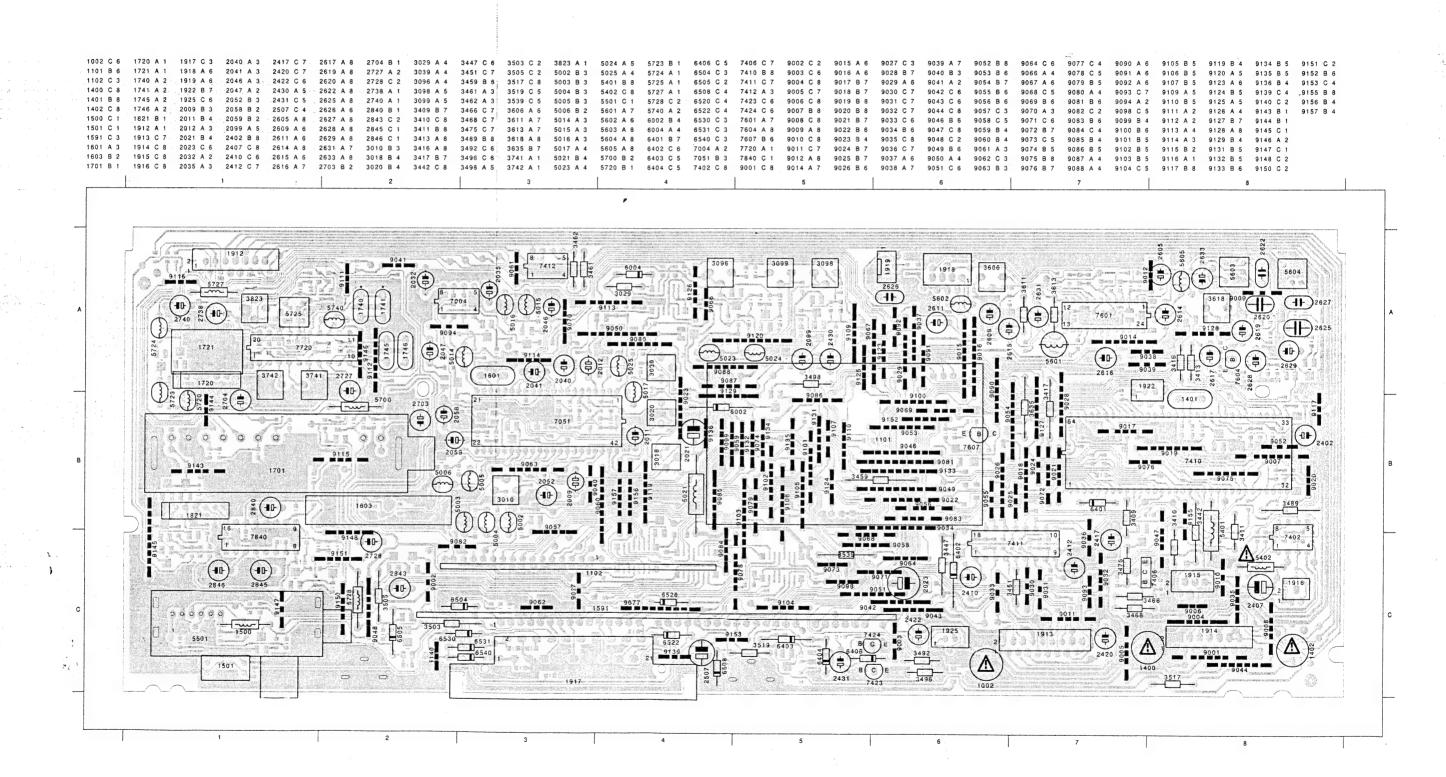
MVIO-CONTROL N.A. 9409.2

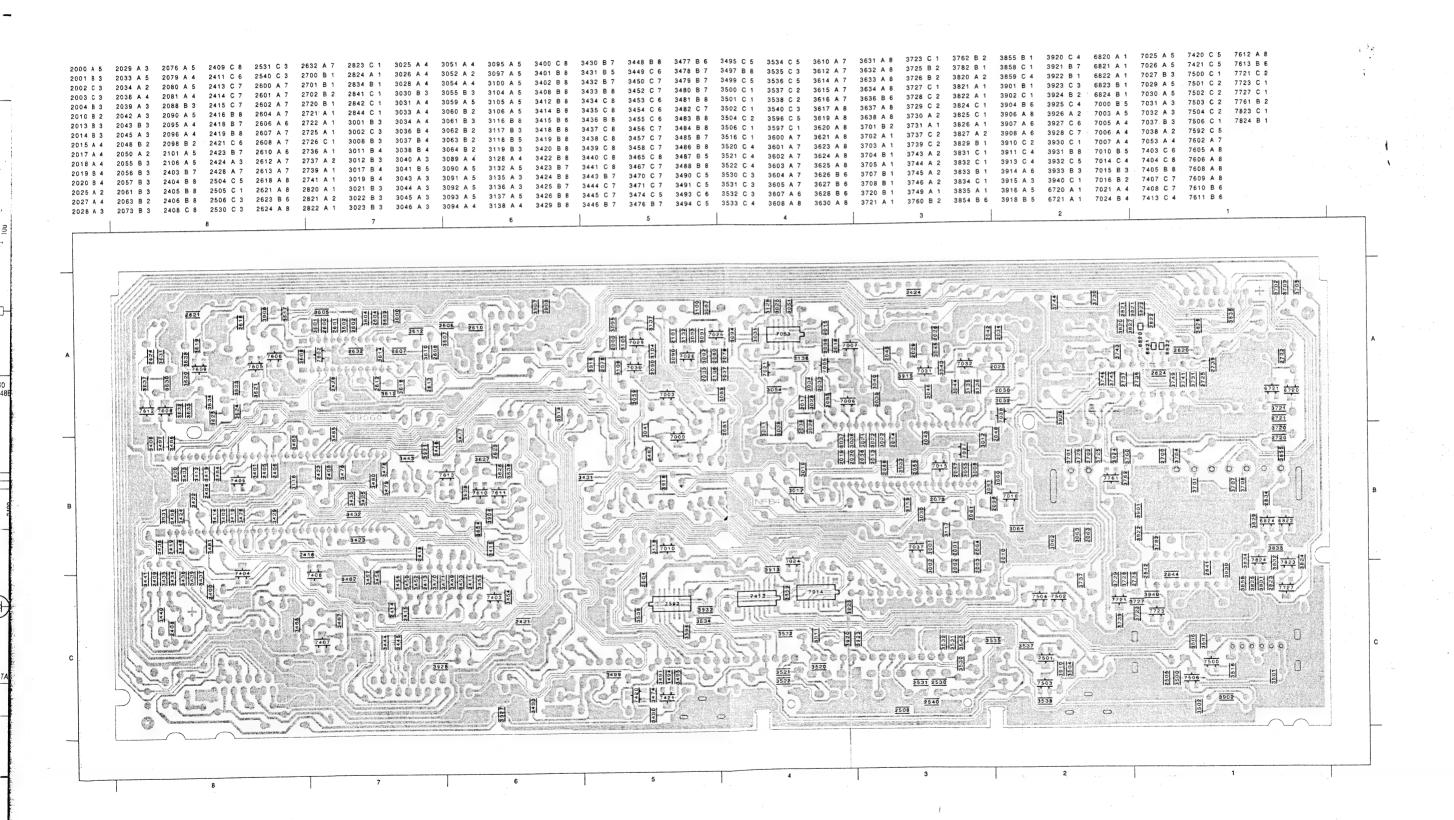
pin10-in

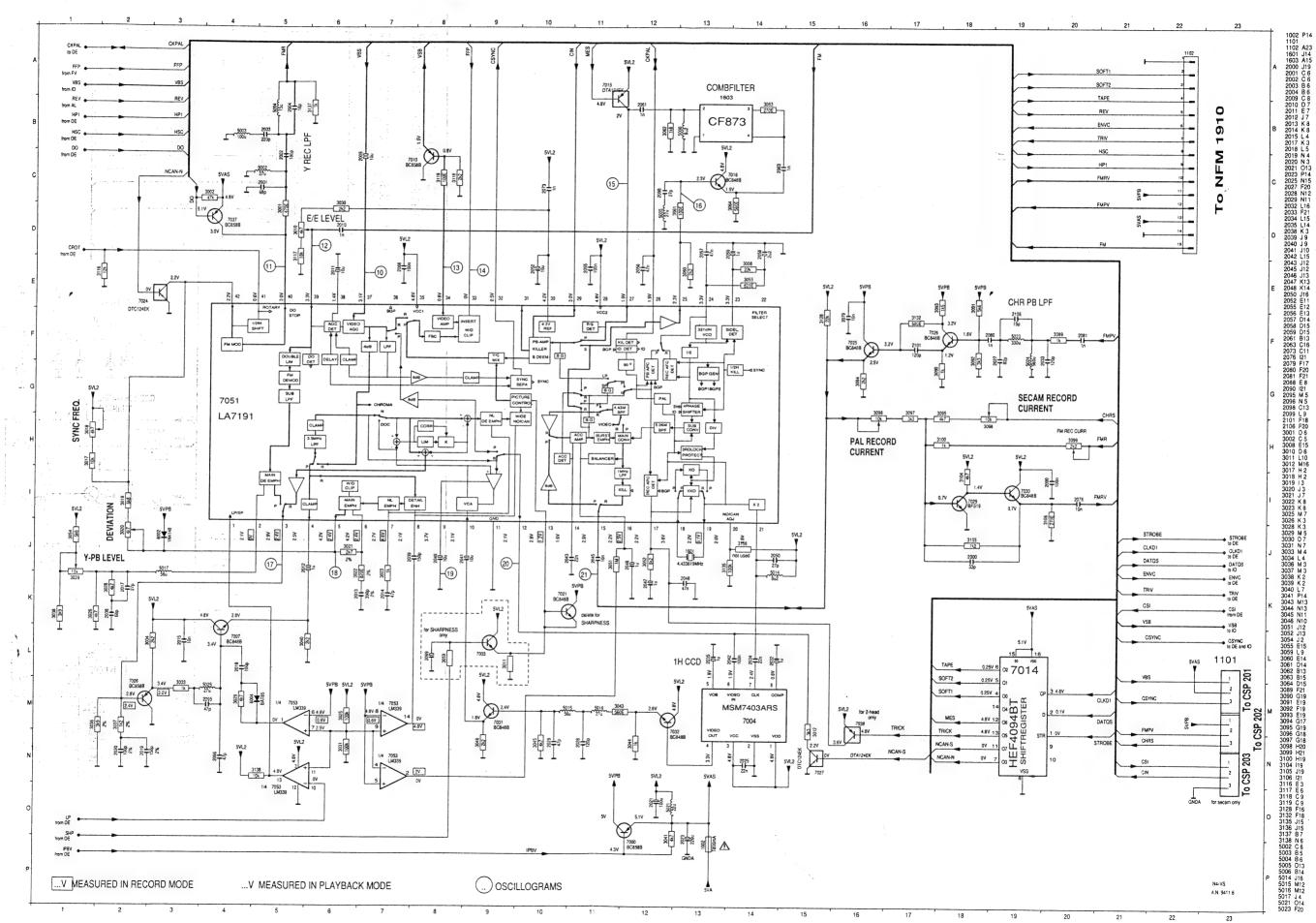
3-47



FAMILY BOARD







Osc.38

OSCILLOGRAMS VIDEOSIGNAL PROCESSING

AUDIO LINEAR -AL Unless otherwise indicated measured in position record. Unless otherwise indicated measured in position record. A: AC, 0.1 V/Div, 10 us/Div A: AC, 0.2 V/Div, 2 us/Div A: DC, 0.2 V/Div, 0.2 ms/Div A: DC, 20 V/Div, 5 us/Div ← 2.5V Connector F8,3 Osc.37 Audio erase head (AEH1) Osc.31 Capacitor 2611 Osc.17 Osc.11 IC 7501 Pin 3 IC 7051 Pin 40 A: DC, 0.2 V/Div, 10 us/Div A: AC, 50 mV/Div , 20 us/Div A: DC, 10 V/Div, 5 us/Div A: DC, 0.2 V/Div, 0.2 ms/Div Osc.32 Connector F8,7 Audio R/P Head (ARH) Resistor 3611 Osc.18 Osc.12 IC 7051 Pin 5 IC 7051 Pin 39 AMLR (measured in playback) A: DC, 0.5 V/Div 10 us/Div A: AC, 0.1 V/Div , 10 us/Div A: DC, 0.5 V/Div, 0.2 ms/Div ∠ 2V Osc.13 ^{← 0V} Osc.19 Osc.33 IC 7051 Pin 10 IC 7601 Pin 13 IC 7051 Pin 3 VSB(meas. in playback) (measured in playback) A: AC, 0.1 V/Div, 10 us/Div A: DC, 1.0 V/Div , 20 us/Div A: DC, 1 V/Div, 0.2 ms/Div Osc.34 ← ov Osc.20 Osc.14 IC 7051 Pin 12 IC 7601 Pin 17 IC 7051 Pin 32 (measured in playback) 10, 0.1 V/DIV, 10 03/DIV A: AC, 50 mV/Div, 5 ms/Div A: DC, 0.1 V/Div, 5 us/Div Osc.35 Connector F7.2 Osc.21 Osc.15 IC 7051 Pin 15 IC 7051 Pin 27 Main erase head (MEH1) (measured in playback) A: AC, 0.2 V/Div, 10 us/Div A: DC, 0.5 V/Div, 10 us/Div A: DC, 1 V/Div, 10 ms/Div

OSCILLOGRAMS

Osc.36

Connector F8,1

Osc.21

IC 7051 Pin 15

(measured in playback)

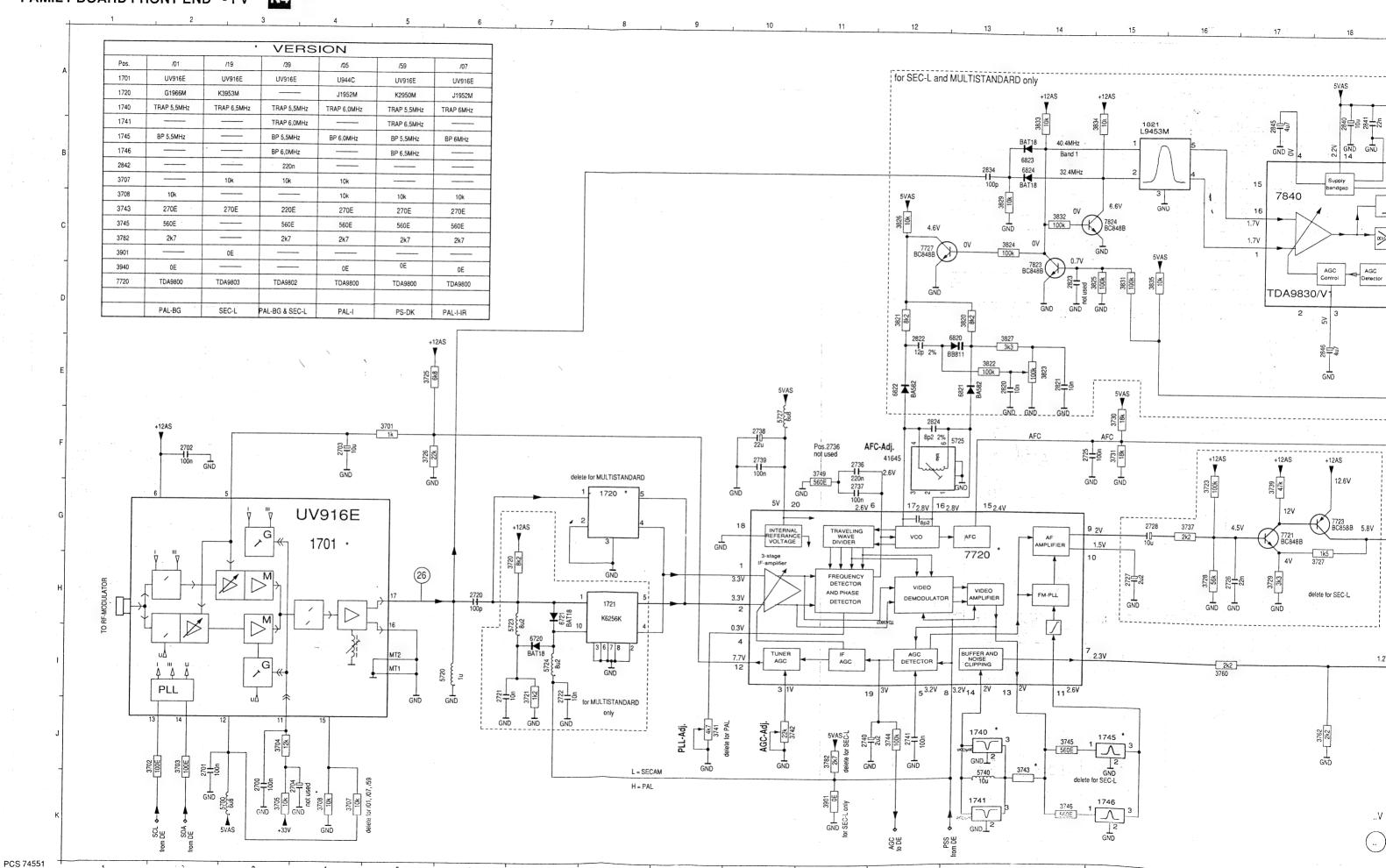
Osc.16

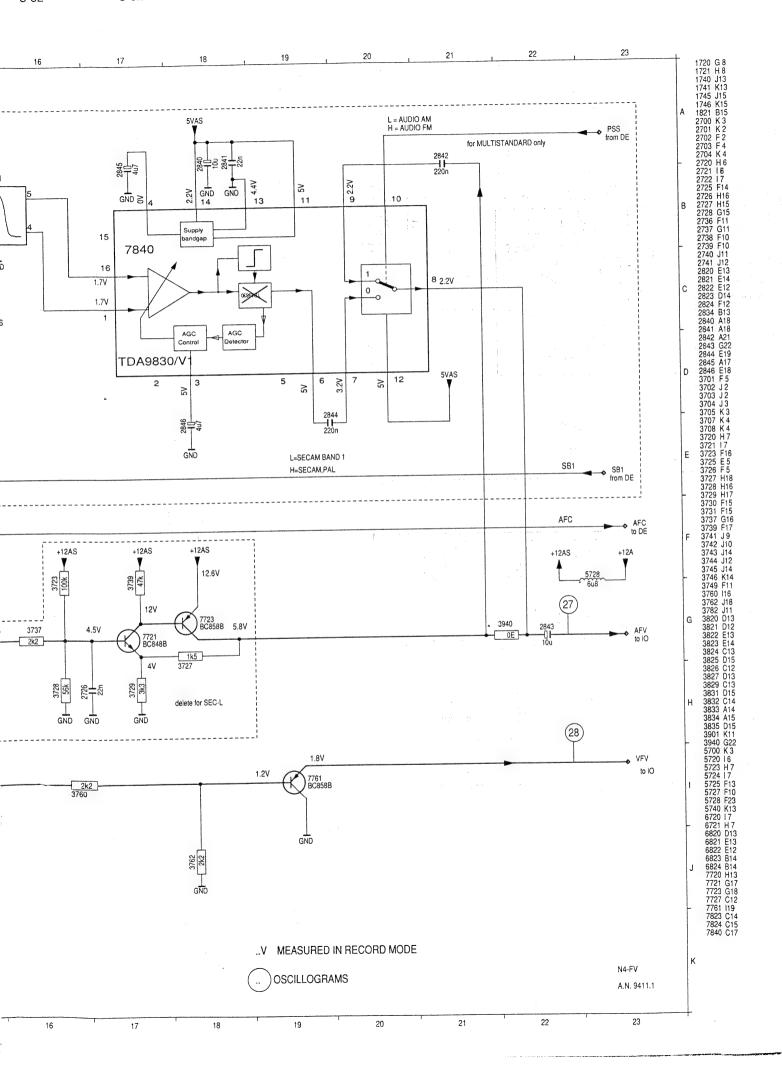
IC 7051 Pin 25

(measured in playback)

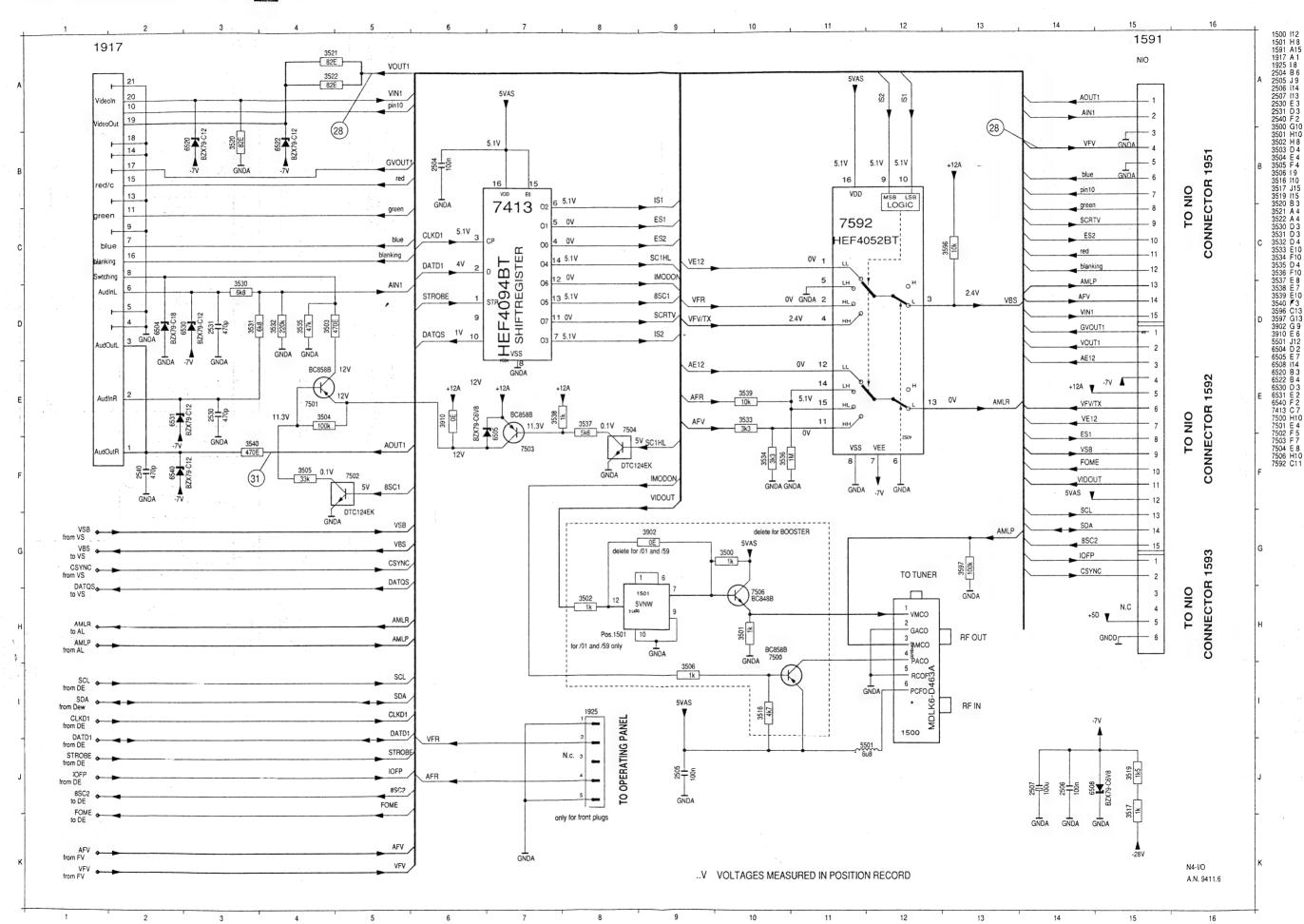
3-51

ed in position record. 1918 E15
1919 H19
1919 H19
26001 H17
26002 H17
26003 H17
26004 H17
26005 H77
26005 H77
26006 H15
26008 H15
26010 H27
26006 H26
26010 H27
26007
26010 H27
260 nnector F8,3 Osc.37 dio erase head (AEH1) DTC124EK 3608 47k DC, 10 V/Div, 5 us/Div -(34) 7602 -(33) nnector F8,7 dio R/P Head (ARH) delete for dub DTC124EK (36) EE/PB REC/EE CTL CTL RIPPLE FILTER MUTE CTL LP CTL (37)-3613 CTL2 7601 3614 10k LA7282 ALC DET (38) AEH2 TO DECK only for dub TO DECK L8 0.50 3908 0E for dub 7608 MEH2 X BC858B 12.7V BC817-40 1919 TO DECK L3 7607 (..)OSCILLOGRAMS .. V MEASURED IN PLAYBACK MODE ...V MEASURED IN RECORD MODE N4-AL A.N. 9411.1

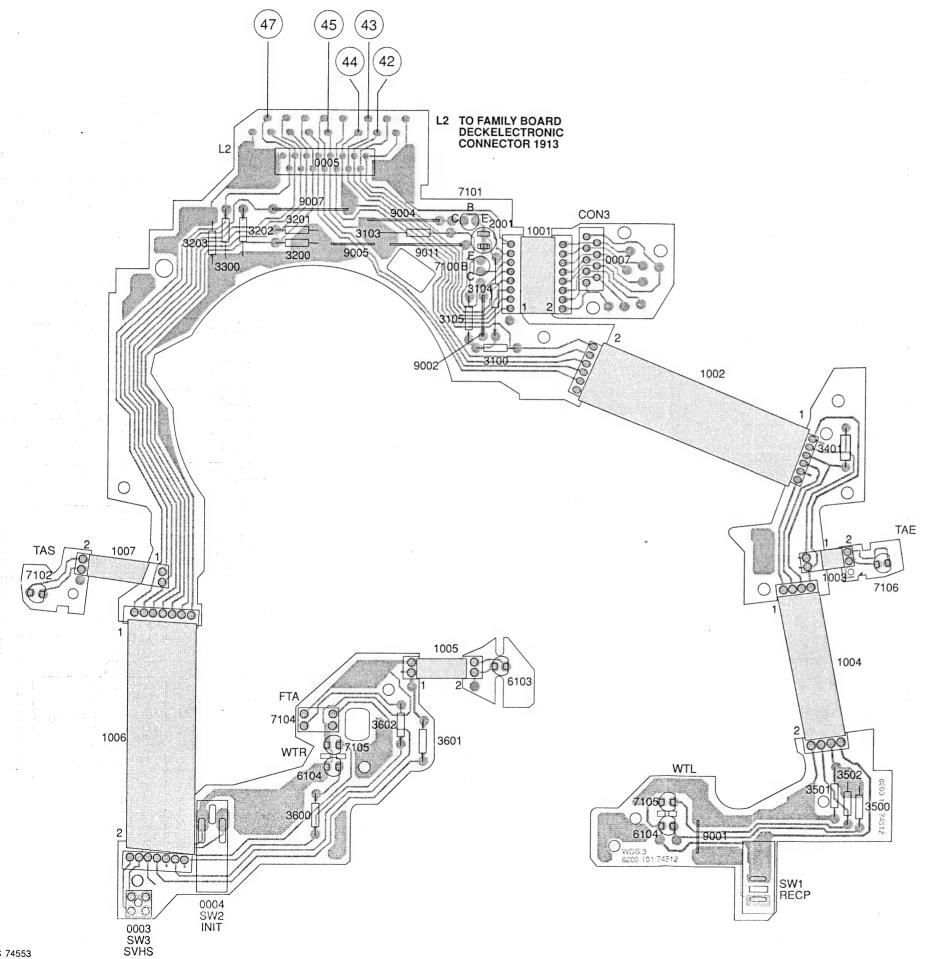


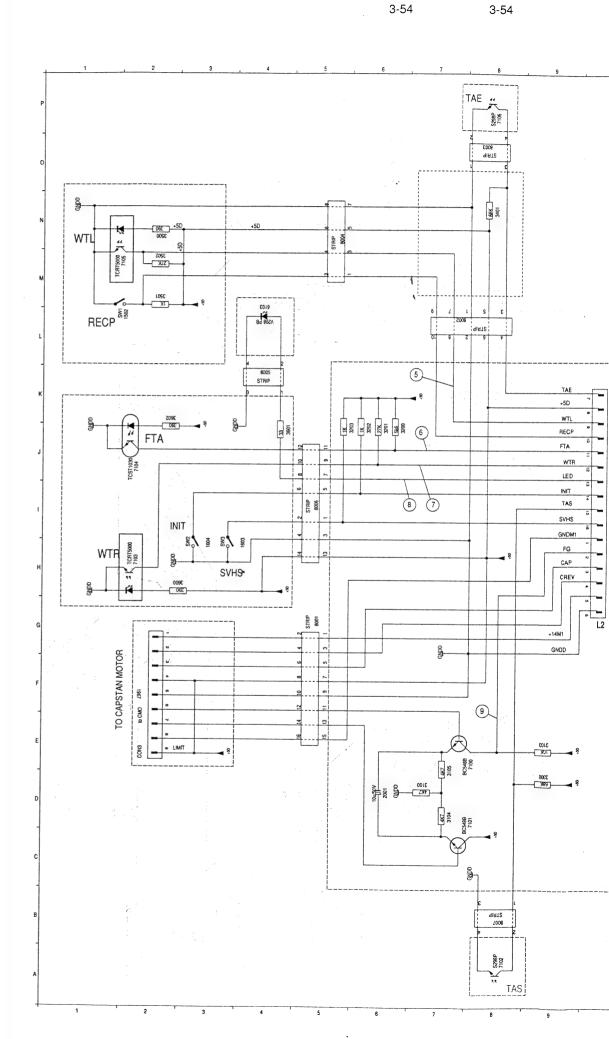


3-53

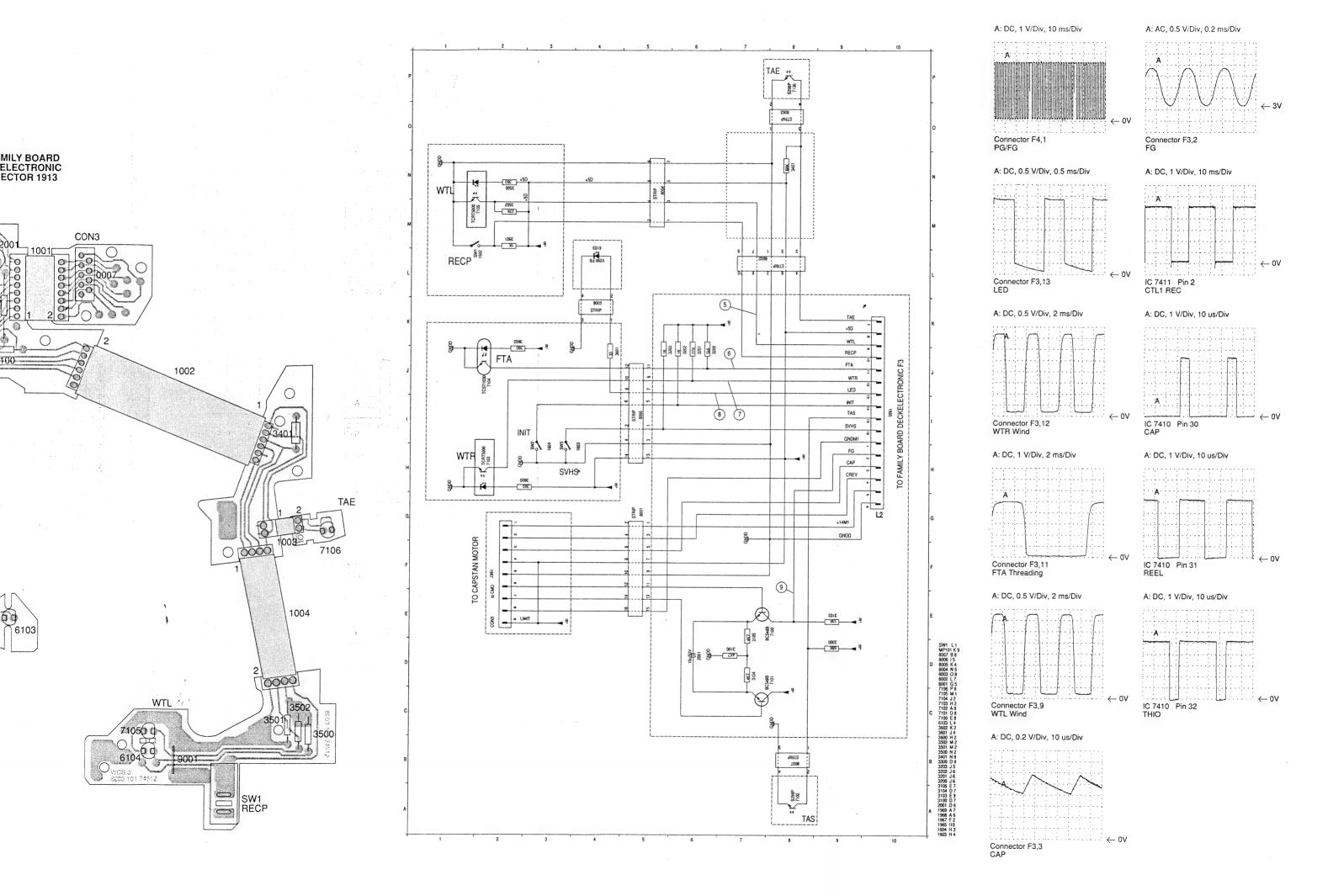


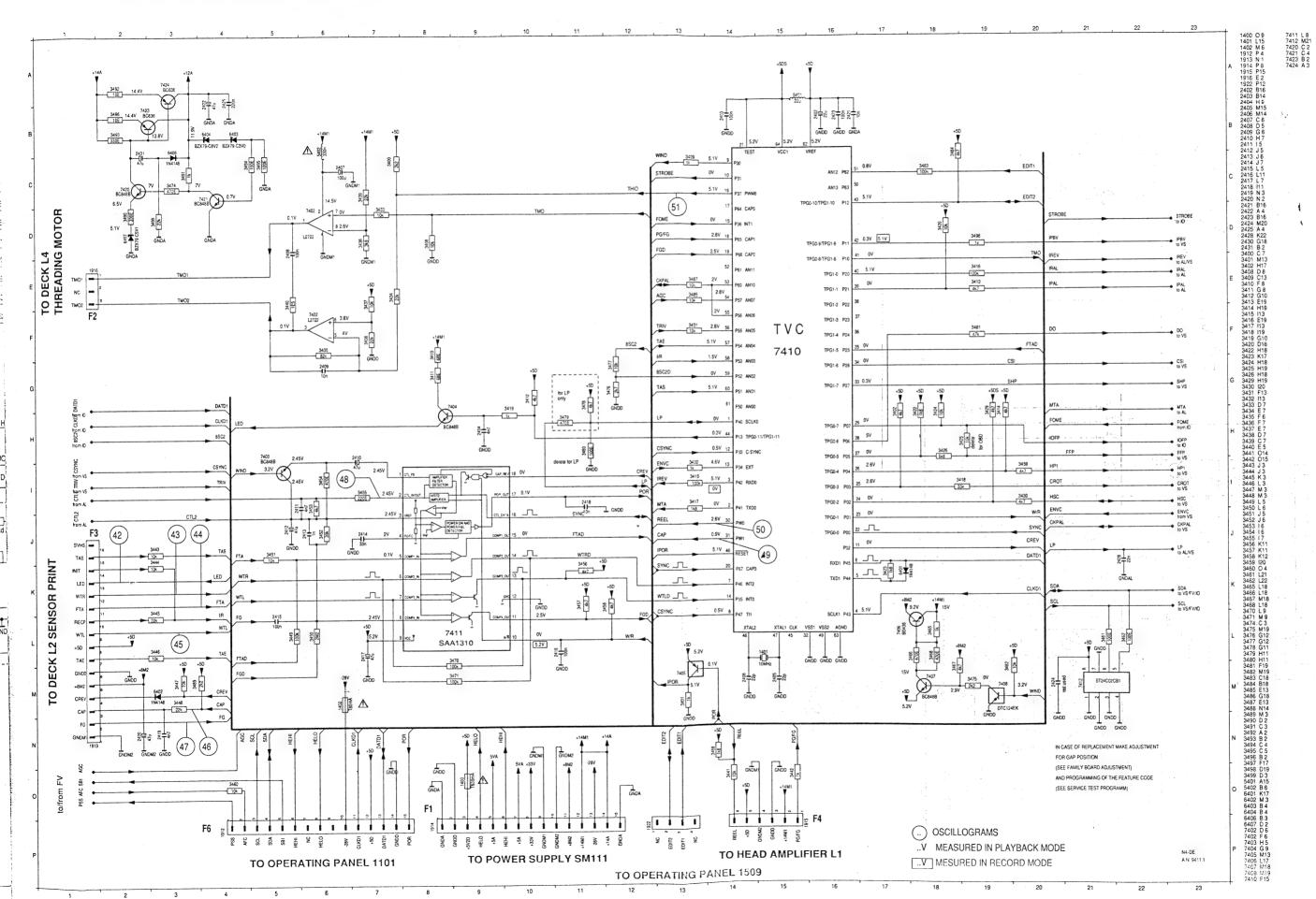
TAPE DECK SENSOR BOARD



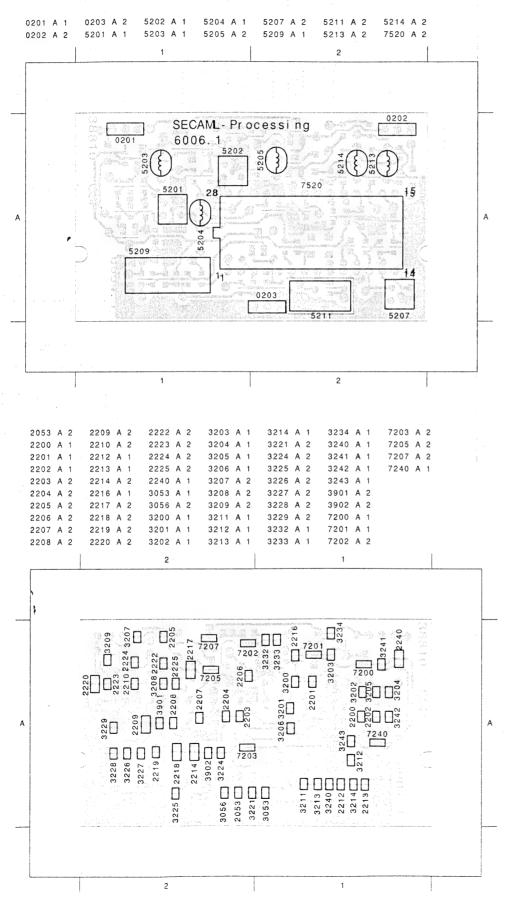


PCS 74553

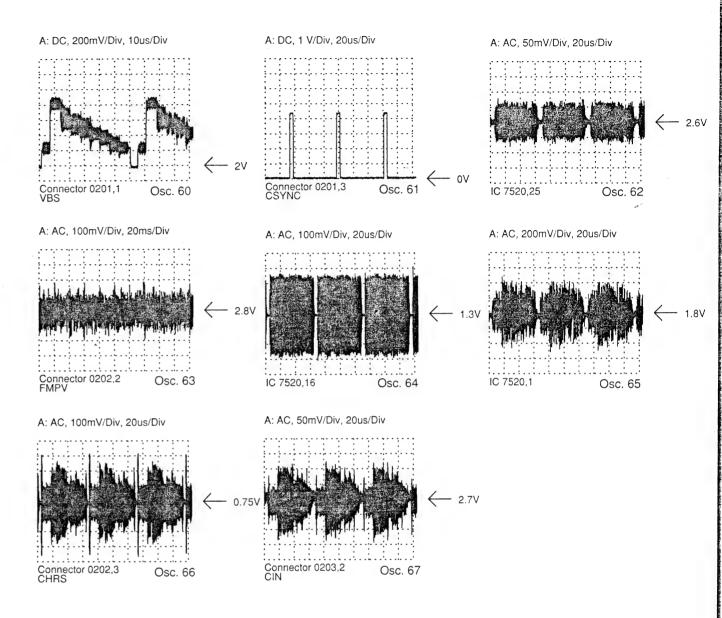




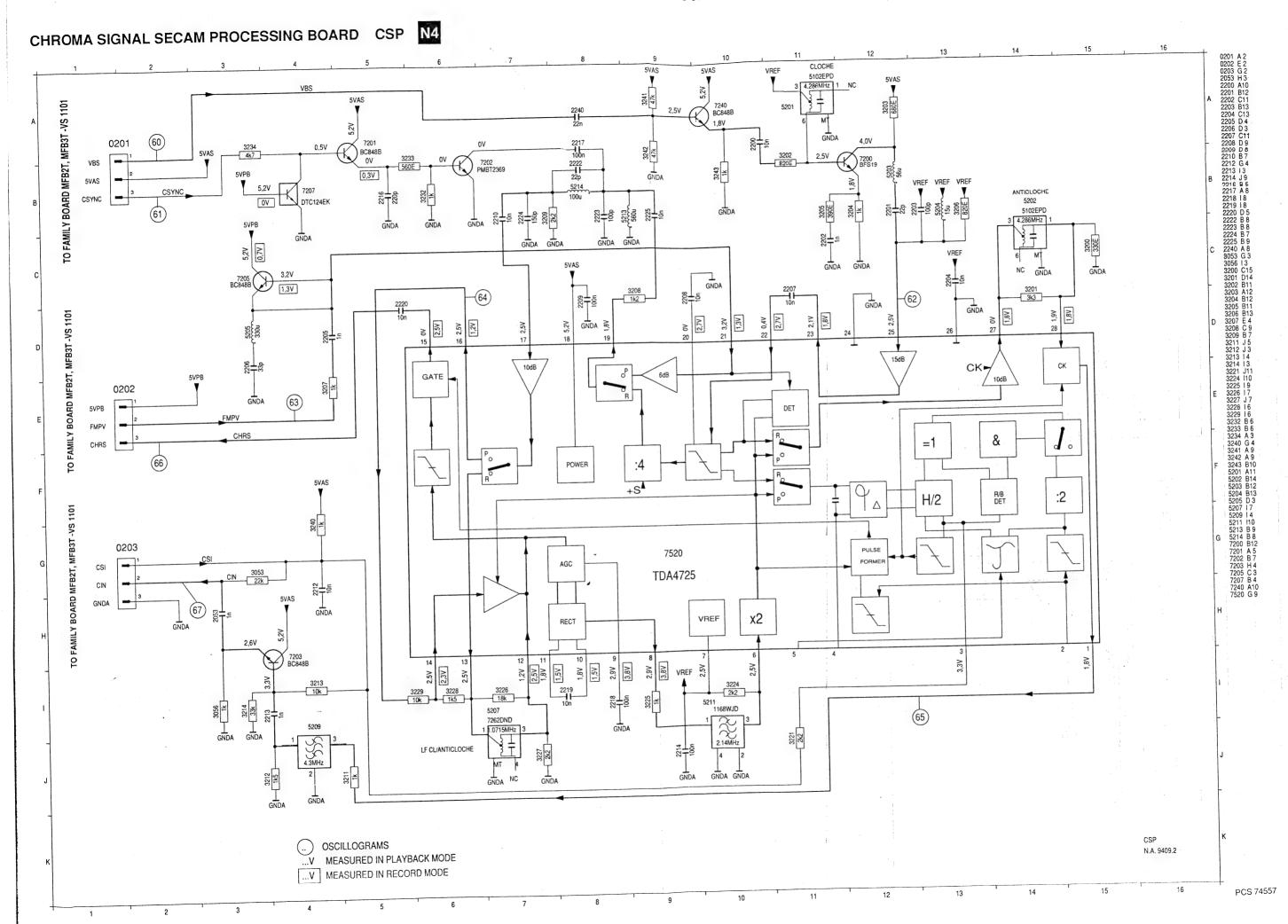
CHROMA SIGNAL SECAM PROCESSING BOARD CSP NO



OSCILLOGRAMS CHROMA SECAM PRINT CSP

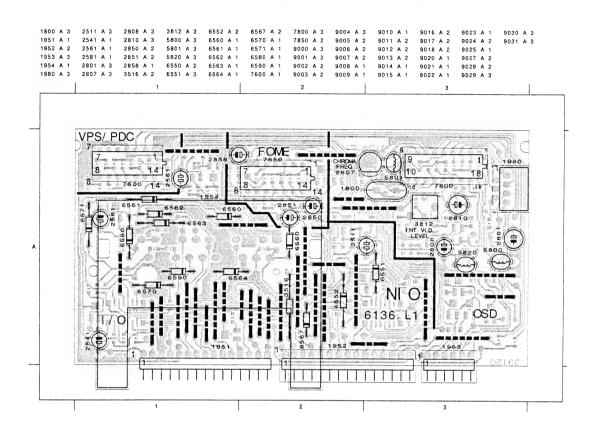


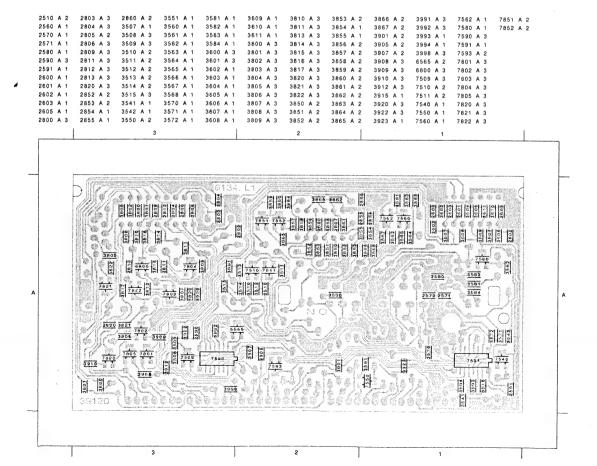
MARKS:			
CONTRACTOR OF CONTRACTOR CONTRACT			_
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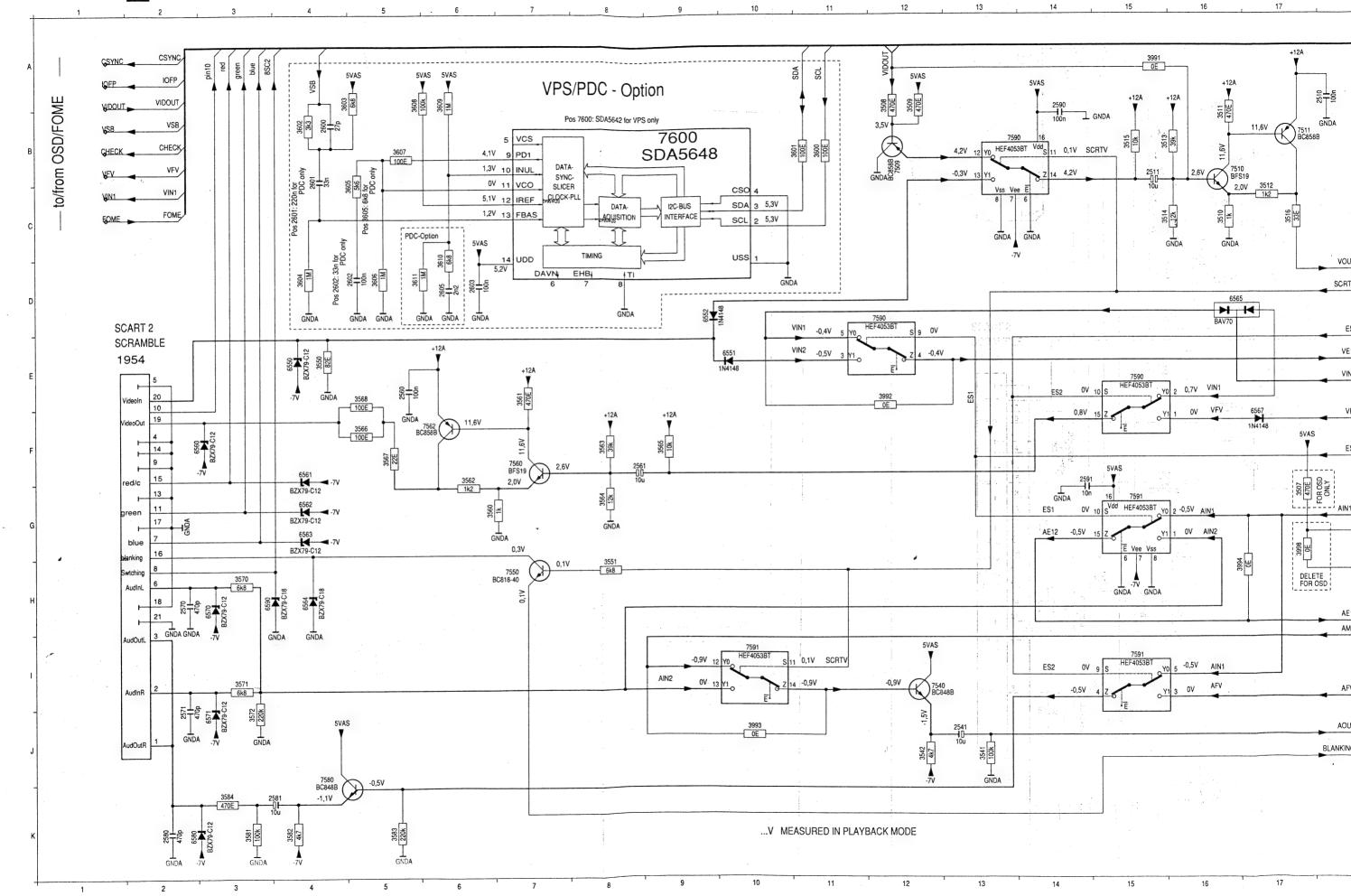


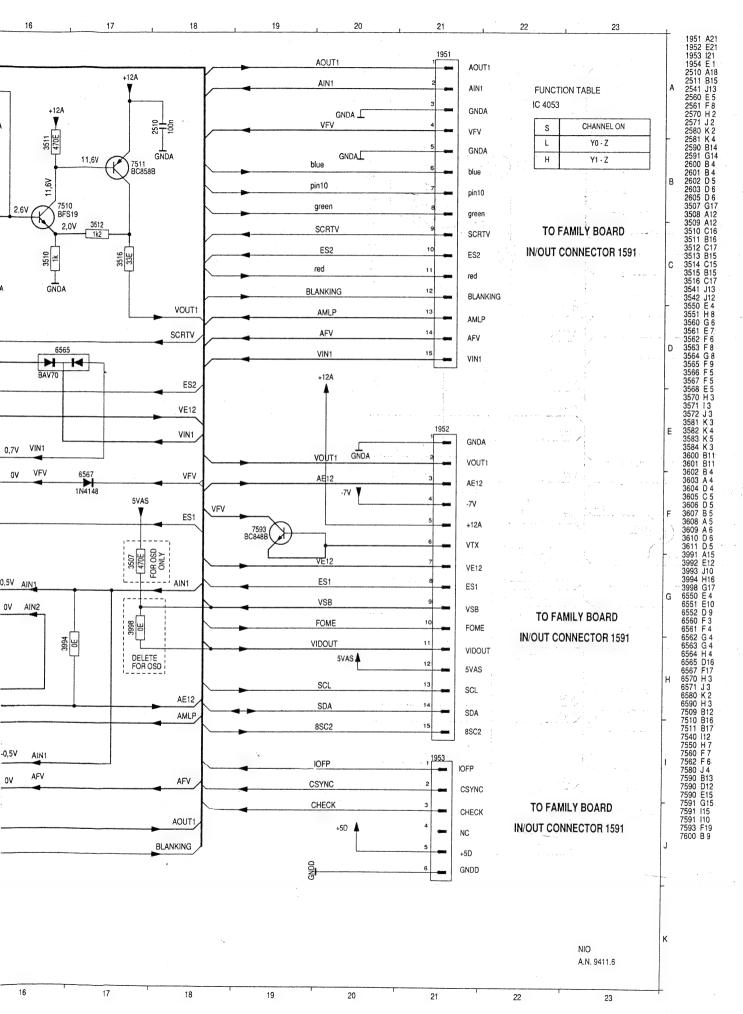
NIO BOARD N4

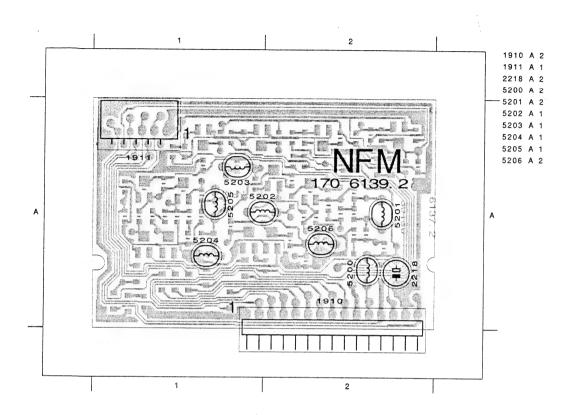
NIO-09

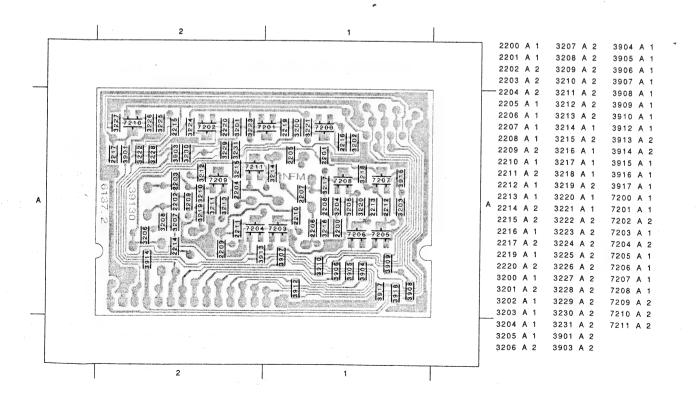


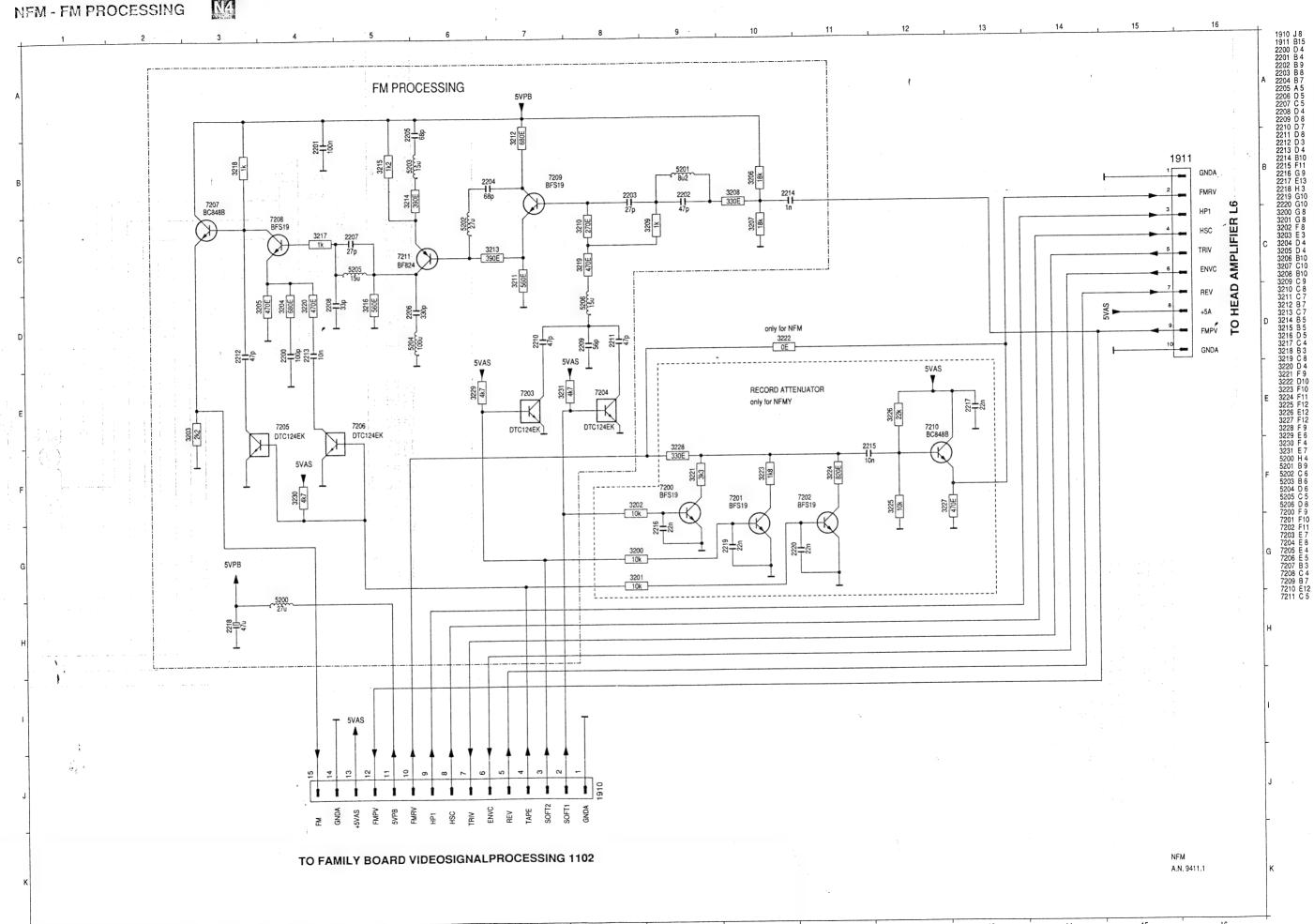












216 A 1 3914 A 2 217 A 1 3915 A 1 218 A 1 3916 A 1 219 A 2 3917 A 1

207 A 2 3904 A 1

213 A 2 3910 A 1 3912 A 1

215 A 2 3913 A 2

3905 A 1

3908 A 1

3909 A 1

208 A 2

209 A 2 210 A 2

211 A 2 212 A 2

214 A 1

7200 A 1 221 A 1 7201 A 1 222 A 2 7202 A 2 3223 A 2 7203 A 1 3224 A 2 7204 A 2

3225 A 2 7205 A 1 3226 A 2 7206 A 1

3227 A 2 7207 A 1 3228 A 2 7208 A 1 3229 A 2 7209 A 2 3230 A 2 7210 A 2

3231 A 2 7211 A 2 3901 A 2 3903 A 2

4. DRIVE ASSEMBLY

This tape deck has three motors; one providing precision drive for the scanner unit; the second providing direct drive for the capstan and belt drive for the reel tables; the third motor drives the lift and tape threading/dethreading operations.

Special features are:

Quick start Short winding time Automatic cleaning of video heads by cleaning roller

To obtain a high repair standard we have developped a range of service kit's. These kit's covers the spare parts which are engaged together.



Before repairing a deck assembly the top and bottom covers should be removed.

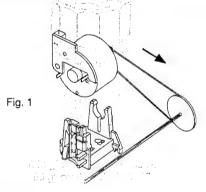
The procedure for the removal and refitting of the following parts is described; only the lift, the scanner, the capstan motor and the A/C head are fixed by screws.

All the other deck assembly parts are held only by snap hooks.

Manual extraction of cassette:

If, after the Eject button has been pressed, the drive does not unthread and eject the cassette, the dethreading/eject operation can also be carried out manually by turning the wheel at the rear of the threading motor.

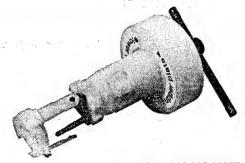
To avoid slack tape, alternate this action with the movement of the capstan motor (counter-clockwise), until the tape is completely taken into the cassette.



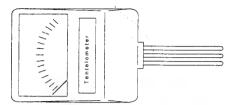
IMPORTANT:

After each repair has been carried out in the drive assembly, the first operation after repairing must be to bring the cassette compartment into "eject" position by hand.

Auxiliary tools for deck adjustment:



Tool for removing the head disc 4822 395 90977



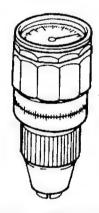
Tentelometer 4822 395 90584



Tool for tapetension adjustment 4822 395 50188

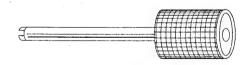


Handle 4822 256 90493



Torquemeter:

600 gf-cm 4822 395 90232 90 gf-cm 4822395 80196



Post adjustment screwdriver 4822 395 50275

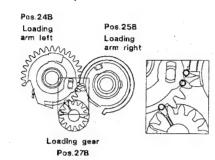
Testcassette 4822 397 30103 Nylon gloves 5322 395 94022

4.1.1 Deck lay out diagram

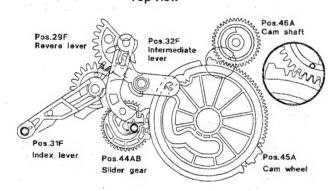
Deck in position "threaded out".

The following diagrams indicate the relative position of the gearwheels and levers when the deck is in the threaded out (cassette compartment down) position.

Top view



Top view



4.1.2 The Lift

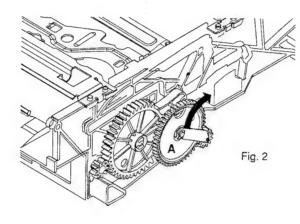
. Refitting the lift compartment:

Ensure the lift compartment is down and gear A is rotated one click stop anticlockwise from the down position.

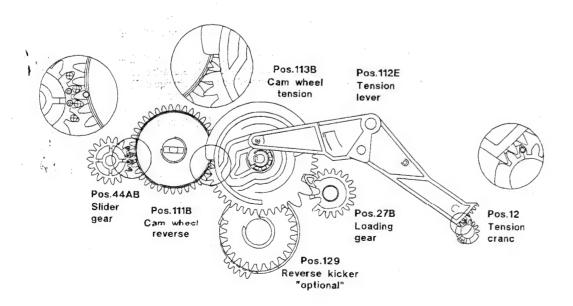
The removal and refitting of the lift can be carried out in all deck positions with the exception of "eject" (ensure that gears 103/105 are free).

To remove the lift

- Free the holding bracket (Fig. 2) by rotating it up and back from the upper end.
- Unscrew the 4 screws on the underside of the deck.
- Carefully remove the lift vertically, noting the position of the record protect operating lever;



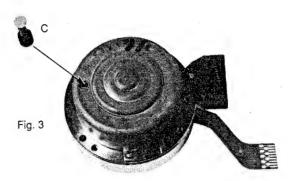
Underside view



4.1.3 Head disc replacement

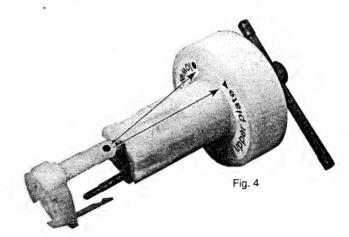
Removal:

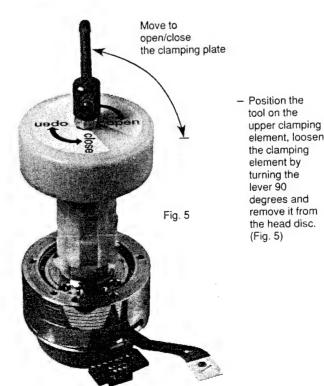
- Nylon gloves should be worn when handling the head disc.
- Turn the headdisc until the long hole of the rotor appears in the bigger hole of the scannermotor
- Insert the reference pin C (included with each service head disc) through the bigger hole of the lid of the scanner motor until the pin snaps in the long hole of the rotor. (Fig. 3)



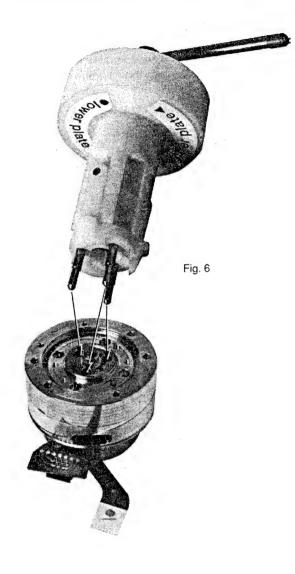
Important:

Choose Installation/Removal of the upper/lower clamping element by turning and attaching the reference element to the tool. (Fig. 4)



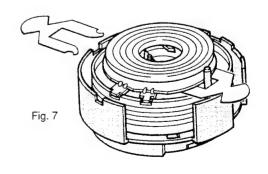


 Prepare the tool for the lower clamping element. Position the tool on the head disc and make sure that all 3 pins are snapped in the the lower clamping element. Loosen the clamping element by turning the lever 90 degrees and remove the head disc plus the tool from the scanner spindle. (Fig.6)

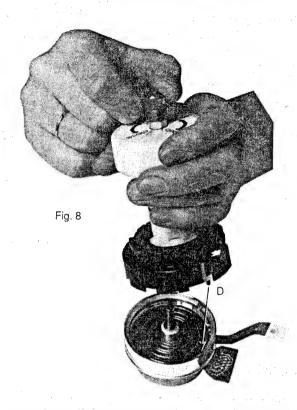


Installation:

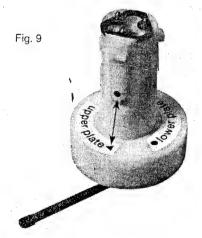
- Before carrying out the installation of the new head disc make sure that the scanner motor spindle is clean and undamaged. (The spindle has to be free of grease and must not be touched with bare hands)
- Insert the 2 Mylar foils (included with each head disc) in the head disc. (Fig.7)



- Position the tool (reference: lower clamping element) on the new headdisc (with protective cover) and loosen the lower clamping element.
- Position the head disc so that pin D of the protective cover engages in the hole of the stator (the arrow on the protective cover must point towards the scanner print). (Fig. 8)



- Reach the exact position through pressing the tool down with a force of 1 N. and fix the lower clamping element by turning the lever towards "close".
- Remove the tool.
- Change the tool to "upper clamping element" and position the clamping element exactly. (Fig. 9)

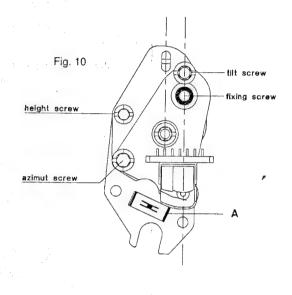


- Tighten the clamping element through turning the lever towards "open".
- Position the tool planely on the head disc and fix the clamping element. (Fig.5 "close")
- Remove the protecting cap from the head disc, withdraw the two Mylar foils and remove the reference pin C.

4.1.4 A/C Head (Combi head) (Pos. 36)

- Remove fixing spring (A) (Fig. 10).
- Remove the fixing screw and replace the A/C head.
- Use a new fixing spring (included with new A/C head) for reassembly.

After the A/C head has been replaced, all adjustments described in paragraph 4.2.1.2 and paragraph 4.2.1.3 have to be carried out.



4.1.5 Threading motor (Pos. 38)

- Remove the belt and disconnect the connector plug.
- Remove the threading motor from the motor supports (Fig. 11).

During reassembly ensure that the threading motor is correctly located in the front and rear supports.

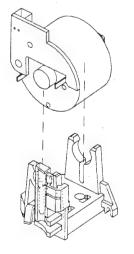


Fig. 11

After replacing the head disc, carry out the following adjustments and checks:

- Head switching pulse (gap position, chapter 3)
- Write current adjustments (chapter 3)
- Check tape path alignment. (see paragraph 4.2.1.)

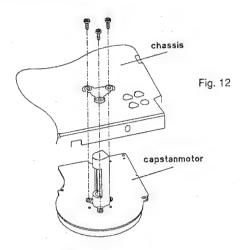
4.1.6 Capstan motor (Pos. 127)

- Set the drive assy to "Eject" position.

 Remove the belt (pos.126) on the underside; then free the pin from the sensor print (see section 4.1.10). Lift sensor print part vertically (it is plug and socket connected to the capstan motor print). Move both sections of the sensor print clear of the capstan motor.

 Remove the three capstan motor fixing screws (Fig. 12) and withdraw the capstan motor downward from the drive assy.

The reassembly is carried out in reverse order. Make sure that the capstan is free of grease.

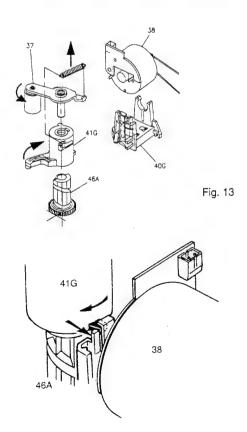


4.1.7 Pressure roller (Pos. 37)

- Set the drive assy to "Eject" position.

- Unhook and remove the pressure roller tension spring.

 Release the pressure roller guide (pos. 41G) from the guide in the threading motor holder by pressing the top of the motor guide rearwards and rotating the pressure roller guide assembly clockwise by approximately a quarter of a turn.(see Fig. 13) The pressure roller and guide can now be lifted clear.



Ensure that no grease from the pressure roller guide gets to the capstan or pressure roller.

The reassembly is carried out in reverse order.

4.1.8 Roller unit right (Pos. 26)

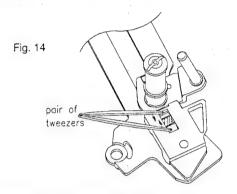
- Set the drive assy to "Eject" position.

 Compress the two snap hooks by means of a pair of tweezers and remove the roller assy from the roller unit right (Fig. 14).

 Unhinge the loading arm right from the holding plate and push the latter towards the front of the deck to remove from the guide (right).

NOTE: During reassembly ensure the link from 25B is engaged in the hole of the holder plate 26

After replacing the roller unit (right), the tape path has to be checked, and adjusted if necessary (paragraph 4.2.1).



4.1.9 Roller unit left (Pos.23)

- Set the drive assy to "Eject" position.

 Unhook the tension arm spring (pos. 11), to avoid the tension arm spring being pre-loaded.

 At the bottom side of the drive assy, partially unhinge the sensor mounting print and remove the tension lever (pos.112).

 Compress the two snap hooks by means of a pair of tweezers (Fig. 9)and remove the roller assy (A) from the plate (B).

 Unhinge the loading arm (left) from the holding plate and remove the latter downward from the drive assy through the recess in the chassis (Fig. 15).

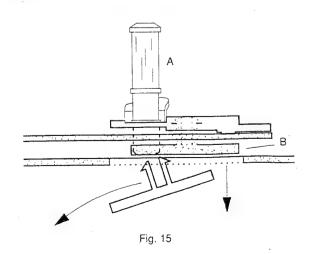
The reassembly is carried out in reverse order.

NOTE: During reassembly

1. Place the carriage holding plate in the assembly with the half-round cutout nearest the rear of the deck.

2. When the loading arm is refitted ensure the pin on the underside of 23 is through the link of 24B.

After replacing the roller unit (left) the tape path has to be checked (paragraph 4.2.1.), and adjusted if necessary.



4.1.10 Sensor print assy (Pos. 118)

For circuit diagram and electrical data see deck electronics (chapter 3).

If a part of the sensor print is defective the whole sensorprint has to be replaced.

Proceed as follows:

- Remove the deck assembly from the set.
- Lift the sensor print vertically, it is plug and socket connected to the capstan motor print.
- All other parts are attached by means of snap hooks and are easily freed.

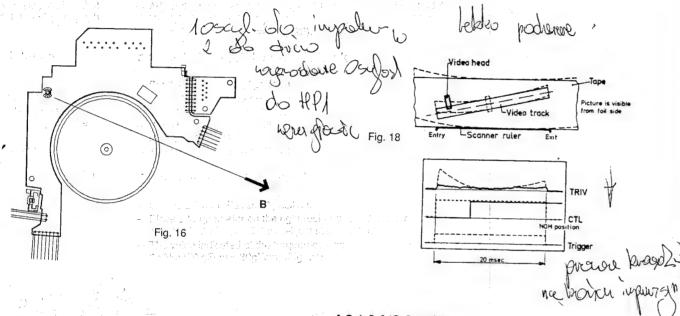
Reassembly is carried out by snapping the snap hooks into place, and inserting the rivet B.

- 1. Pressing the auto tracking button and watch the tape sync pulse move to the left in relation to the TRIV signal.
- Note the extreme left hand position reached by the sync pulse, repeat as necessary.
- 3. Stop the movement of the pulse when the TRIV signal reduces to 1/2 to 2/3 maximum amplitude by pressing the normal play button. A noisy picture (disturbances) is visible on the TV set and the CTL pulse should be to the left of the display.

The machine will retain this position in memory until an eject is carried out. This condition works only if X-distance is adjusted.

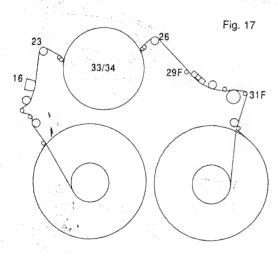
Adjustment:

Adjust the left and right roller units to make the tracking signal TRIV straight and flat as possible (Fig. 18).



4.2 Adjustments

4.2.1 Tape path



4.2.1.1 Roller left unit/roller unit right

Preparation:

- Connect one input of a dual trace oscilloscope to observe the tape sync pulse CTL. The other input (DC coupled) to observe the tracking information TRIV.
- Trigger the oscilloscope externally on the head pulse HP1.
- Playback the black and white section of the alignment test tape.
- Set the deck in the condition where the video heads are running along the upper edge of the tracks only by:

4.2.1.2 A/C Combi head

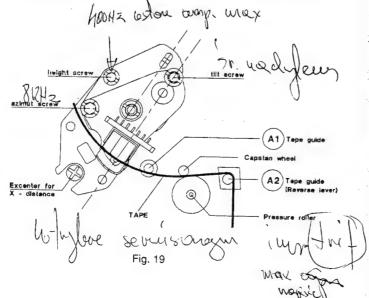
Tilt angle adjustment

Set the drive to feature mode (e.g. +7)
 Adjustment with tape guide A1:

 By means of the tilt angle adjusting screw move the tape until the lower edge just touches the tape guide A1 (see Fig. 19) the tape must not be distorted at the lower edge (by pressing onto guide).

Adjustment without tape guide A1:

 By means of the tilt angle adjustment screw move over the tape until the lower edge just touches the tape guide A2 (see fig.19) (by pressing onto guide). After that turn the tilt angle adjustment screw anticlockwise for 60°-90° (The tape must not touch guide A2).



Adjustment of the azimuth angle and the head height

- Connect an oscilloscope to the linear Audio output.
- Play the section of the test cassette with the audio signal 400 Hz.
- Adjust for maximum output voltage by means of the height adjustment screw
- Play the section of the test cassette with the audio signal 8 kHz.
- Adjust to maximum output voltage by means of the azimuth adjustment screw (Fig. 19).
- If necessary, repeat this procedure
- Check the tilt angle adjustment

If the tape path was completely out of adjustment or if several components in the tape path have been replaced, it is possible, that the adjustments described in paragraph 4.2.1.1 and paragraph 4.2.1.2 have to be repeated several times.

4.2.2 Adjustment of the horizontal distance (x-distance)

- Before this adjustment is carried out, insert the test cassette (start from Eject position). Call the service test program (tracking value will take up its nominal position) and press the "play" button.
- Playback the black/white part of the test cassette.)
- Display the TRIV signal on an oscilloscope (DC-coupled) and adjust for maximum voltage by means of the eccentric screw (Fig. 19).

4.2.3 Brake band adjustment

- Set the drive to "Play"
- Adjust the brake band by means of adjusting tool (from the underside of the drive), until the edge of the elbow of the tape tension arm overlaps with the left inner edge of the left guide by 0.5mm (see Fig. 20)

4.2.4 Tape tension adjustment

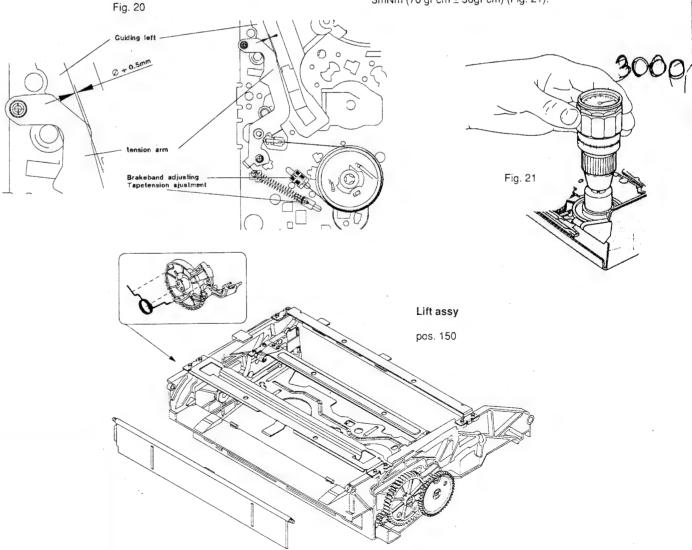
- Play a VCR cassette (E 180) starting from the beginning of the tape
- Measure the tape tension before the roller unit left by means of a tentelometer.
- Adjust the tension arm spring (pos.11) to a tape tension of 0,24 N ± 0,02 N (24 g ±2 g) by means of the adjustment tool (from the underside of the drive, Fig. 20).

4.2.5 Friction clutch control check

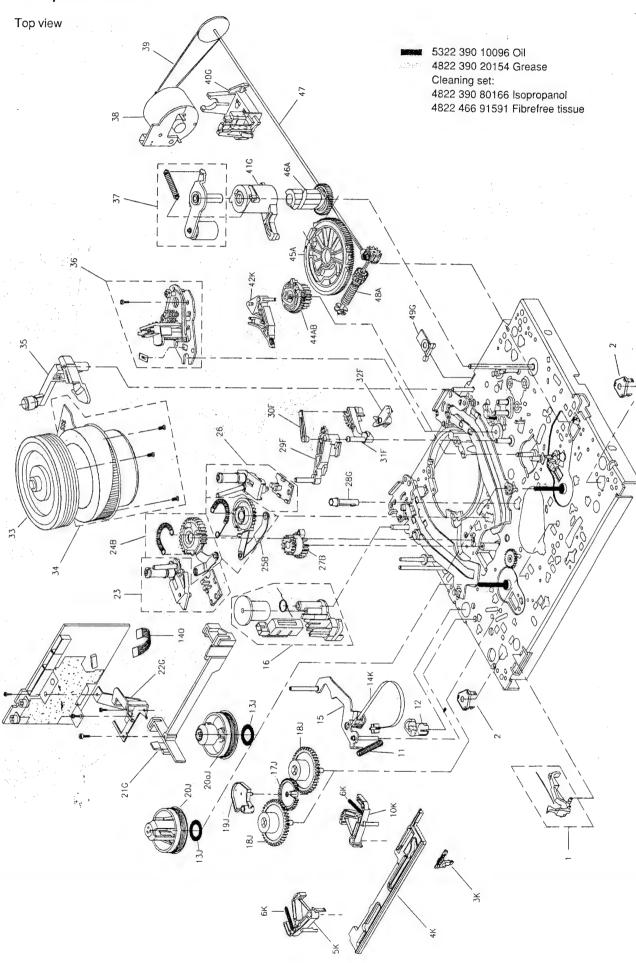
- Set the drive to "Play" position.
- Place the torquemeter on the right reel.
- Turn the capstan motor to move the right reel clockwise.
- Keep turning, until the indication at the torquemeter no longer changes (Fig. 21)
- The torque has to be 10,5 mNm ± 25% (105gFcm ±25%)

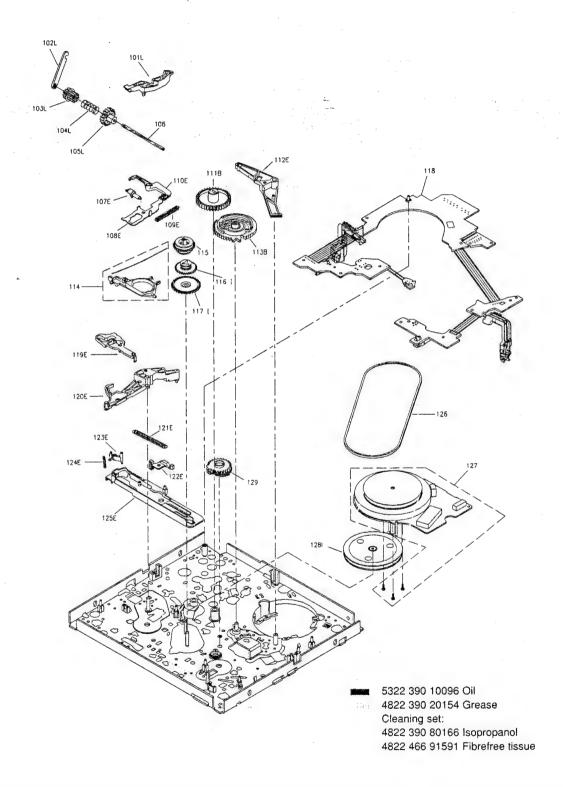
4.2.6 Reverse brake control

- Set the drive to "Reverse" position.
- Place a torquemeter on the right reel and turn the latter counterclockwise, until the reel just starts to flip.
- The value indicated at the torquemeter has to be 7mNm± 3mNm (70 gFcm ± 30gFcm) (Fig. 21).



4.3 Exploded view





4.4 Partslist

Pos.	Description	KIT's								Code number 4822	Pos.		
		A	В	E	F	G	1	J	K	L	4022		
1	Rec.protection lever										403 70546	45	Ċ
	(with spring)	L										46	C
2	Chassis mounting										492 71022	47	P
	spring (2x)											48	V
3	Trigger lever			_					K			49	C
4	Trigger slider	_							K			101	C
5_	Main brake left					_			K			102	C
6	Main brake spring (2x)			_					K			103	C
10	Main brake right				_				K			104	(
11	Tension arm spring	L							<u> </u>		492 33317	105	(
12	Tension crank			_	_				1		403 70551	106	5
13	Slip ring			L	_			J		L		107	F
14	Tension band						L		K			108	F
15	Tension arm										403 70547	109	F
16	Erase head			L							249 40293	110	F
17	Swivelling gear	_						J				111	(
18	Brake gear (2x)		_					J				112	1
19	Swivelling plate		_					J				113	(
20 ^	Reel table (S)		_	L				J				114	(
20a	Reel table (T)	_	_	-				J					Ĺ
21	Headamplifier holder	_				G						115	(
22	Bracket					G						116	C
23	Roller unit left	_	_	<u></u>						_	528 70771	117	[
24	Loading arm left		В									118	S
25	Loading arm right		В			_							(
26	Roller unit right	_		_							528 70772	119	1
27	Loading gear	_	В									120	C
28	Light prism					G						121	S
29	Index lever			_	F							122	C
30	Reverse clip				F							123	5
31	Reverse lever			L	F							124	5
32	Intermediate lever	_			F							125	N
33	Head disc 2/0			_	_					_	691 20926	126	
33	Head disc 3/0										691 20937	127	C
33	Head disc 4/0		_		_						691 20938		(
34	Scanner motor 2/0										361 21548	128	C
	(with screws)					_						129	F
34	Scanner motor 3/0										361 21549		tr
34	(with screws) Scanner motor 4/0		-		-			-			361 10658	140	F
57	(with screws)										307 10000	150	L
35	1										528 70773		1
36	A/C Head (with clip										249 10468	KIT	A
	and screws)	_		L		_						KIT	E
37	Pressure roller (with spring)										528 70774	KIT	E
38	Threading motor										361 21486	KIT	F
39	Threading belt										358 20421	KIT	C
40	Motor holder	ļ		<u>_</u> .		G						KIT	1
41	Pressure roller guide	<u> </u>				G						KIT	J
42	Reverse brake	_			_				K			KIT	K
44	Slider gear	Α	В		L							KIT	L

Pos.	Description	KIT's							Code number 4822		
		Α	В	Ε	F	G	1	J	К	L	
45	Cam wheel	A	_	Ī	Ė	_	Ė	Ť	<u> </u>	Ī	
46	Cam shaft	Α	-							-	
47	Pulley shaft	1		<u> </u>							528 81462
48	Worm shaft	Α									
49	Chassis mounting clip	Ť				G				_	
101	Casette loader trigger									L	
102	Clip		-						-	L	
103	Casette loader gear 1									L	
104	Casette loader spring									L	
105	Casette loader gear 2	 								L	
106	Spindle	_	-							Ē	535 93277
107	Pulse roller		-	Ε							000 00277
108	Pulse slider			E						-	
109	Pulse slider spring	-	-	E						_	
110	Pulse lever			E							
111	Cam wheel reverse		В		-			-	-	-	
112	Tension lever		0	Е						-	
113	Cam wheel tension		В	-						-	
114	Clutch lever		U			\dashv					403 70549
117	(with spring)										403 70343
115	Clutch									-	528 20736
	Changing gear	-					ī				320 20730
117	Double gear	-			\dashv	\dashv	<u>'</u>				
118	Sensor print S-VHS		-		\dashv	-	-				214 60155
110	(with stud and rivet)										214 00 100
119	Main slider lever	-	_	E							
120	Cam wheel lever			E	-	\dashv		\dashv	-		-
121			-	E	-	\dashv		-			
122	Slider spring		-		-		-		-		
	Clutch slider			E	-	-		-			
123	Slider lever trigger			E					-		
124	Slider lever spring			E							
125	Main slider			Ε	-						050 04400
126	Driving belt					-		-			358 31166
127	Capstan motor										361 21484
100	(with screws)				-	+	\dashv	-	-		
128	Gear pulley	- 2			-	-	1	-			500.00.15
129	Reverse kicker (with transmission gears) *)										522 20451
140	Flex cable				+	+	-+	-+			320 40287
150	Lift				-	+	+		-		443 64112
,50	LIII.				+	+	\dashv		-	-	770 04112
	TO SEE WELL									.	
KIT	A	\vdash	\dashv		+	+			+	-	310 31064
KIT	В		-	+	+	+	\dashv	+	\dashv		310 31954 310 31955
KIT	E			-	-	+	+	-			
KIT	F		-		-	+	+		-		310 31958
KIT	G	-		-	-	+	+		\dashv		310 31959
KIT	I	-	-		+	-	+	-+	+	-	310 31961
KIT	1			+	+	+	+		\dashv		310 31963
KIT	K		-	+	+	+	+		-	-	310 31996
			-	+	+	+	-	-+	\dashv		310 31997
KIT	L										310 32116

For getting a high repairstandard all spare parts included in a kit have to be replaced with the exception of kit ${\sf E}$ and kit ${\sf G}$.

What are the benefits of service kits:

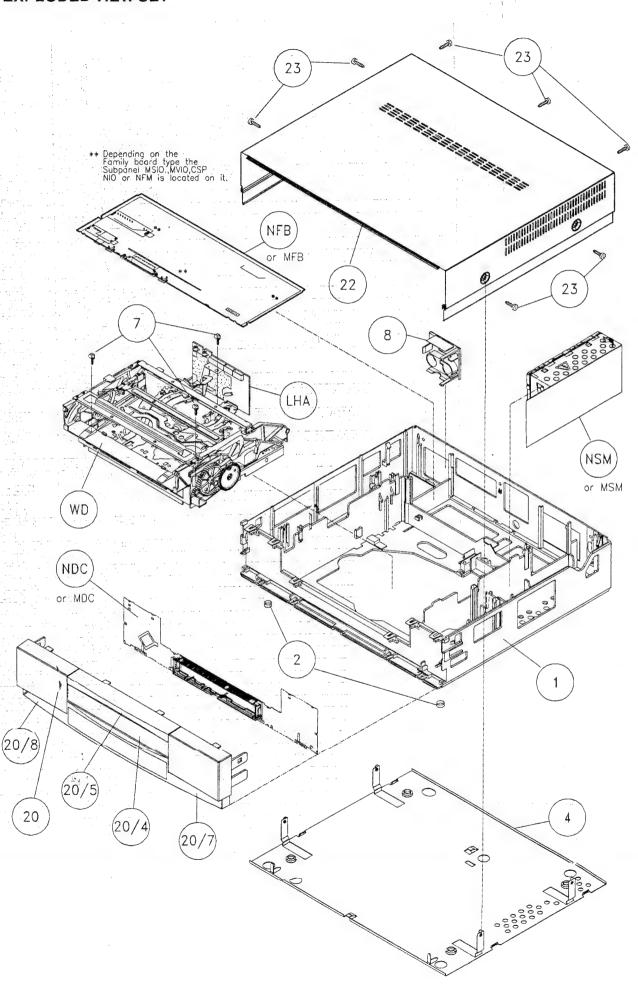
A better quality of repair (not only the defect part is replaced but also the related affected parts).

A faster repair (all the parts are already collected and are focussed on the problem).

A cheaper repair (parts are manufactured with the same parrts used for production, giving a high quality for lowest price).

^{*)} optional

EXPLODED VIEW SET



PARTSLIST

Position	12 NC 4822	Description	Set
1	464 50961	FRAME	all sets
2	462 41806	FOOT	all sets
4	443 51235	воттом	all sets
7	502 13884	SCREW 3.4 x 16	all sets
8	443 63842	COVER	VR241/02/10, VR242/02, VR243/01/13, VR347/02/10, VR247/01/02/06, VR447/02
8	443 63843	COVER	VR2410/19, VR2419/39, VR2469/39, VR3419/39, VR3469/39, VR3479/39, VR4469/39,
0	440 00040	3372.1	VR4479/39, 24DV10/19, 2SB41/11, 2SB410/18, 2SB419/38, 2SB469/38, 3SB47/11, 3SB419/38, 3SB469/38
20/4	443 64156	LIFT FLAP	VR241/02, VR242/02,
20/4	443 64158	LIFT FLAP	VR241/10, VR247/01, VR247/02, VR247/06
20/4	443 64167	LIFT FLAP	VR243/01, VR243/13
20/4	443 64168	LIFT FLAP	VR347/02, VR3469/39
20/4	443 64174	LIFT FLAP	VR347/10
20/4	443 64172	LIFT FLAP	VR447/02, VR3419/39
20/4	443 64184	LIFT FLAP	VR2419/39, VR2410/19, VR2469/39
20/4	443 64187	LIFT FLAP	VR4479/39
20/4	443 64185	LIFT FLAP	2SB41/11, 2SB410/18, 2SB419/38, 2SB469/38, 3SB47/11, 24DV10/19
20/4	443 64188	LIFT FLAP	VR3479/39
20/4	443 64195	LIFT FLAP	VR4469/39
20/4	443 64199	LIFT FLAP	3SB419/38, 3SB469/38
20/4	492 70896	LEG SPRING	all sets
20/5	462 42091	FOOT RIGHT	VR347/02, VR347/10, VR3419/39, VR3469/39, VR3479/39
20/7	462 42109	FOOT RIGHT	VR447/02, VR4479/39, VR4469/39
	462 42092	FOOT LEFT	VR347/02, VR347/10, VR3419/39, VR3469/39, VR3479/39
20/8	462 42111	FOOT LEFT	VR447/02, VR4479/39, VR4469/39
20/8		FLAP (CINCH)	VR447/02
20/9	443 64173	FLAP (CINCH)	VR4479/39, VR4469/39
20/9	443 64186	COVER	VR241/02/10, VR242/02, VR243/01/13, VR247/01/02/06, VR2419/39,
22	444 60853	LAQUERED	VR2410/19, VR2469/39 VR347/02, VR347/10, VR3419/39, VR3469/39, VR3479/39, VR447/02, VR4479/39,
22	443 64166	COVER LAQUERED	VR4469/39 2SB41/11, 2SB410/18, 2SB419/38, 2SB469/38, 3SB419/38, 3SB469/38, 3SB47/11,
22	443 64194	COVER LAQUERED	24DV10/19
23	502 13173	SCREW	ail sets
168	459 10923	WORDMARK SCHNEIDER	24DV10/19
168	459 10896	WORDMARK RADIOLA	2SB410/18, 2SB419/38, 2SB469/38, 3SB419/38, 3SB469/38
168	459 10912	WORDMARK SBR	2SB41/11, 3SB47/11
20	443 41367	CONTROL PANEL	VR241/02, VR242/02
20	443 41371	CONTROL PANEL	VR241/10
20	443 41375	CONTROL PANEL	VR243/01, VR243/13
20	443 41377	CONTROL PANEL	VR247/01
20	443 41382	CONTROL PANEL	VR247/02
20	443 41404	CONTROL PANEL	VR247/06
20	443 41376	CONTROL PANEL	VR347/02
20	443 41381	CONTROL PANEL	VR347/10
20	443 41379	CONTROL PANEL	VR447/02
20	443 41392	CONTROL PANEL	VR2410/19
20	443 41384	CONTROL PANEL	VR2419/39
20	443 41388	CONTROL PANEL	VR2469/39
20	443 41383	CONTROL PANEL	VR3419/39
20	443 41389	CONTROL PANEL	VR3469/39
20	443 41395	CONTROL PANEL	VR3479/39
20	443 41397	CONTROL PANEL	VR4469/39
20	443 41387	CONTROL PANEL	VR4479/39
20	443 41402	CONTROL PANEL	2SB41/11
20	443 41396	CONTROL PANEL	2SB410/18
20	443 41385	CONTROL PANEL	2SB419/38
- U			2SB469/38
	443 41399	CONTROL PANEL	
20	443 41399		
20 20	443 41398	CONTROL PANEL	3SB419/38
20			

POWER SUPPLY NSM

CONNECTORS	
↑ 4822 267 31064	MAINS CONNECTOR
4822 462 71855	COVER
4822 265 41251	CONNECTOR 15 P.
	<u> </u>
MISCELLANEOUS	
4822 256 30514	FUSE HOLDER
1050 1 4822 070 31252	FUSE T1.25A
The second section of the sect	Tan V. Tan
CAPACITORS	
2015 4822 121 70482	2,7 nF 100V
2015 4822 121 70482 2023 4822 121 70462	220 nF 63V
2025 5322 121 42386	100 nF 50V
2027 5322 121 42386	1100 nF 63V
2030 4822 124 80873 2040 4822 121 70481	47 nF 25V
2040 4822 121 70481 2050 \(\triangle \) 4822 121 70163	100 nF 250V
2060 🛆 4822 122 33284	100 pF 250V
2061 1 4822 122 33284	100 pF 250V
2062 🛕 4822 124 80872	1 nF 400V
2064 🛕 4822 124 80872	1 nF 400V
2065 🛕 4822 121 70163	100 nF 250V
2070 <u>1</u> 4822 124 42104 2085 5322 121 42386	100 nF 50V 24 44 51 5 74 44 45
2102 4822 124 80874	47 UF 50V
2104 4822 124 80874	47 μF 50V
2110 4822 121 41856	22 nF 250V
2112 4822 121 51305 2114 4822 121 51299	1 nF 50V
2130 4822 122 31116	2,2 nF 400V
2134 4822 124 40739	680 µF 25V
2157 4822 124 80875 2185 4822 124 40739	220 μF 25V 680 μF 25V
2103 4022 124 40700	осо р. 20°
RESISTORS	
2005 4922 116 52292	4,7 kΩ 0,5W
3005 4822 116 52283 3007 4822 117 11167	820 kΩ 0,5W
3011 4822 117 11166	360 kΩ 0,5W
3020 4822 116 52215	220 Ω 0,5W
3022 4822 116 52269 3027 4822 116 52233	3,3 kΩ 0,5W 10 kΩ 0,5W
3035 4822 116 52283	4,7 kΩ 0,5W
3040 4822 116 52233	10 kΩ 0,5W
3042 4822 116 52233	10 kΩ 0,5W 10 kΩ 0,5W
3044 4822 116 52233 3046 \(\triangle \) 4822 053 21395	10 kΩ 0,5W 3,9 MΩ 0,5W
3048 1 4822 053 21395	3,9 MΩ 0,5W
3052 4822 116 52297	68 kΩ 0,5W
3054 4822 116 52297	68 kΩ 0,5W
3056 4822 116 52297 3058 4822 116 52297	68 kΩ 0,5W 68 kΩ 0,5W
3083 4822 116 52215	220 Ω 0,5W
3084 4822 116 52228	680 Ω 0,5W
3085 4822 116 52231	820 Ω 0,5W
3090 4822 100 12163 3092 4822 116 52224	470 Ω 0,5W 470 Ω 0,5W
3095 4822 116 52224	470 Ω 0,5W
3123 4822 117 11169	4,7 Ω 0,5W
3125 4822 116 52175	100 Ω 0,5W
COILS	
5042 4822 157 60147	2,2 μΗ
5050 🛕 4822 157 70682	MAINS FILTER
5070 14822 146 21781	TRANSFORMER
5123 4822 157 52684 5130 4822 157 60147	10 μH 2,2 μH
5132 4822 157 53006	22 μH
	•

5160 🗥	4822 157 53005	1	
5162	4822 157 50961	22 μH	
5182	4822 157 53252	22 μΗ	
5184	4822 157 53252	22 μΗ	
DIODES	\$		
6027	4822 130 30842	BAV21	
6040	4822 130 30042	BYT52M	
6070	5322 209 12018	DF08M	
6100	4822 130 82885	BYT52M	-
6105	4822 130 82885	BYT52M	
6130	4822 130 32961	BYV28-200	
6155	4822 130 32961	BYV28-200	
6180	4822 130 32715	SB340	
TRANS	ISTORS & IC's		
7005 *	4822 209 31528	TDA4605-3	
7005	4822 209 31551	SPH4690	
	4822 130 62753	IRFBC30	
	4822 130 83676	SOC1012T/K1150PG	
7080 🔼	4822 209 33323	TL431CLPRM	
7005	4022 203 33323	124010211111	
 	substitute for SPI	H4690	

POWER SUPPLY MSM

CONNE	CTORS		
	4822 265 41251	15 pin	
\triangle	4822 267 31064	Mains connecto	r · · · ·
MISCEI	LANEOUS		
MISCEL	LANLOGS		-
\lambda	4822 256 30274 4822 462 71855	Fuse holder MSM Cover	
101	4822 070 32002	Fuse 2A/250V	
CAPAC	ITORS		
2101 🛆	4822 121 70163	100 nF 250	V
2103 🛕	4822 122 33284	470 pF 400	V
2105 🛝	4822 122 33284	470 400	
	4822 122 33284	470 400	
	4822 121 70163	100 nF 250	
2112	4822 124 42104	68 μF 385	
	4822 122 33284	470 pF 400	
2114	5322 121 10472	f	F25V
2115	4822 121 42408	220 nF 63V	
2116	4822 121 70162	10 nF 400 100 nF 63V	
2117 . 2118	5322 121 42386 4822 124 80402	1 nF	
2119	4822 121 51299	1 nF 50V	
2121	5322 121 42386	100 nF 63V	
2201	4822 124 80267	47 μF 50V	
2204	4822 124 40739	680 μF 25V	•
2206	4822 124 40739	680 μF 25V	
2207	4822 124 40739	680 μF 25V	
2209	4822 124 80267	47 μF 50V	
2210	4822 121 41856	22 nF 250	
2211	4822 121 41856	22 nF 250 100 nF 63V	
2212 2214	5322 121 42386 4822 124 40199	100 nF 63V 680 μF 16V	
2215	4822 124 40199	680 μF 16V	
2217	5322 121 42386	100 nF 63V	′
RESIS	TORS		
3102 🛆	4822 053 21395	3,9 MΩ 0,5	w
3103 🛆	4822 053 21395	3,9 M Ω 0,5	W
3104	4822 116 52224	470 Ω 0,5	
3106 🗥	4822 053 21225	2,2 MΩ 0,5	
3106 <u>/</u> 3109	4822 053 21225 4822 053 30338	2,2 M Ω 0,5 3,3 Ω 2,5	W
3106 <u>/</u> 1 3109 3112	4822 053 21225 4822 053 30338 4822 116 52271	2,2 $M\Omega$ 0,5 3,3 Ω 2,5 33 $k\Omega$ 0,5	W W
3106 <u>/</u> 1 3109 3112 3119	4822 053 21225 4822 053 30338 4822 116 52271 4822 116 52271	2,2 M Ω 0,5 3,3 Ω 2,5 33 k Ω 0,5 33 k Ω 0,5	W W W
3106 <u>1</u> 3109 3112 3119 3120	4822 053 21225 4822 053 30338 4822 116 52271 4822 116 52271 4822 116 52215	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	W W W W
3106 A 3109 3112 3119 3120 3121	4822 053 21225 4822 053 30338 4822 116 52271 4822 116 52271 4822 116 52215 4822 050 13302	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	W W W W
3106 A 3109 3112 3119 3120 3121 3122	4822 053 21225 4822 053 30338 4822 116 52271 4822 116 52271 4822 116 52215 4822 050 13302 4822 117 10314	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	W W W W
3106 A 3109 3112 3119 3120 3121 3122 3123	4822 053 21225 4822 053 30338 4822 116 52271 4822 116 52271 4822 116 52215 4822 050 13302 4822 117 10314 4822 117 10161	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	W W W W
3106 A 3109 3112 3119 3120 3121 3122 3123 3124	4822 053 21225 4822 053 30338 4822 116 52271 4822 116 52271 4822 116 52215 4822 050 13302 4822 117 10314 4822 117 10161 4822 116 52269	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	W W W W
3106 A 3109 3112 3119 3120 3121 3122 3123 3124 3125	4822 053 21225 4822 053 30338 4822 116 52271 4822 116 52271 4822 116 52215 4822 050 13302 4822 117 10314 4822 117 10161 4822 116 52269 4822 116 52215 4822 116 52215	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	W W W W W
3106 A 3109 3112 3119 3120 3121 3122 3123 3124 3125 3126	4822 053 21225 4822 053 30338 4822 116 52271 4822 116 52271 4822 116 52215 4822 050 13302 4822 117 10161 4822 117 10161 4822 116 52269 4822 116 52215 4822 116 52233 4822 116 52191	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	W W W W W
3106 A 3109 3112 3119 3120 3121 3122 3123 3124 3125 3126 3127	4822 053 21225 4822 053 30338 4822 116 52271 4822 116 52271 4822 116 52215 4822 050 13302 4822 117 10314 4822 117 10161 4822 116 52269 4822 116 52215 4822 116 52215 4822 116 52233 4822 116 52191 4822 116 52284	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	W W W W W W W W
3106 A 3109 3112 3119 3120 3121 3122 3123 3124 3125 3126 3127 3129	4822 053 21225 4822 053 30338 4822 116 52271 4822 116 52271 4822 116 52215 4822 050 13302 4822 117 10314 4822 117 10161 4822 116 52269 4822 116 52215 4822 116 52215 4822 116 522191 4822 116 52284 4822 116 52284	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	W W W W W W W W W
3106 A 3109 3112 3119 3120 3121 3122 3123 3124 3125 3126 3127 3129 3130 3131	4822 053 21225 4822 053 30338 4822 116 52271 4822 116 52271 4822 116 52215 4822 050 13302 4822 117 10314 4822 117 52269 4822 116 52269 4822 116 52215 4822 116 52215 4822 116 52233 4822 116 52234 4822 116 52284 4822 116 52284 4822 050 24708	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	W W W W W W W W W W
3106 A 3109 3112 3119 3120 3121 3122 3123 3124 3125 3126 3127 3129 3130 3131 3132	4822 053 21225 4822 053 30338 4822 116 52271 4822 116 52271 4822 116 52215 4822 050 13302 4822 117 10314 4822 117 10161 4822 116 52269 4822 116 52215 4822 116 52233 4822 116 52291 4822 116 52284 4822 116 52284 4822 116 52284 4822 116 52284 4822 116 52175	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	W W W W W W W W W W W
3106 A 3109 3112 3119 3120 3121 3122 3123 3124 3125 3126 3127 3129 3130 3131 3132 3203	4822 053 21225 4822 053 30338 4822 116 52271 4822 116 52271 4822 116 52215 4822 050 13302 4822 117 10314 4822 117 52269 4822 116 52269 4822 116 52215 4822 116 52233 4822 116 52291 4822 116 52284 4822 116 52175 4822 116 52224	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	W W W W W W W W W W W
3106 A 3109 3112 3119 3120 3121 3122 3123 3124 3125 3126 3127 3129 3130 3131 3132 3203 3204	4822 053 21225 4822 053 30338 4822 116 52271 4822 116 52271 4822 116 52215 4822 105 13302 4822 117 10314 4822 117 52269 4822 116 52269 4822 116 52215 4822 116 52215 4822 116 52291 4822 116 52284 4822 116 52284 4822 050 24708 4822 116 52175 4822 116 52224 4822 116 52224	2,2 MΩ 0,5 3,3 Ω 2,5 33 kΩ 0,5 220 Ω 0,5 680 kΩ 560 kΩ 3,3 kΩ 0,5 220 Ω 1/8 10 kΩ 0,5 33 Ω 0,5 47 kΩ 0,5 47 kΩ 0,5 470 Ω 0,5 470 Ω 0,5	W W W W W W W W W W W W
3106 A 3109 3112 3119 3120 3121 3122 3123 3124 3125 3126 3127 3129 3130 3131 3132 3203 3204 3205	4822 053 21225 4822 053 30338 4822 116 52271 4822 116 52271 4822 116 52215 4822 050 13302 4822 117 10314 4822 117 10161 4822 116 52269 4822 116 52215 4822 116 52215 4822 116 52233 4822 116 52284 4822 116 52284 4822 116 52284 4822 116 52284 4822 116 52284 4822 116 52224 4822 116 52224 4822 116 52224 4822 116 52224 4822 116 52224	2,2 MΩ 0,5 3,3 Ω 2,5 33 kΩ 0,5 220 Ω 0,5 3,3 kΩ 0,4 680 kΩ 560 kΩ 3,3 kΩ 0,5 220 Ω 1/8 10 kΩ 0,5 47 kΩ 0,5 47 kΩ 0,5 47 kΩ 0,5 47 Ω 0,6 100 Ω 0,5 470 Ω 0,5	W W W W W W W W W W W W
3106 A 3109 3112 3119 3120 3121 3122 3123 3124 3125 3126 3127 3129 3130 3131 3132 3203 3204 3205 3206	4822 053 21225 4822 053 30338 4822 116 52271 4822 116 52271 4822 116 52215 4822 050 13302 4822 117 10314 4822 117 10161 4822 116 52269 4822 116 52215 4822 116 52215 4822 116 52233 4822 116 52284 4822 116 52284 4822 050 24708 4822 116 52175 4822 116 52224 4822 116 52224 4822 116 52224 4822 116 52224 4822 116 52224 4822 116 52224 4822 116 52224	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	W W W W W W W W W W W W W
3106 A 3109 3112 3119 3120 3121 3122 3123 3124 3125 3126 3127 3129 3130 3131 3132 3203 3204 3205 3206 3207	4822 053 21225 4822 053 30338 4822 116 52271 4822 116 52271 4822 116 52215 4822 050 13302 4822 117 10314 4822 117 10161 4822 116 52269 4822 116 52215 4822 116 52215 4822 116 52215 4822 116 52215 4822 116 52284 4822 116 52284 4822 116 52284 4822 116 52228 4822 116 52224 4822 116 52224 4822 116 52224 4822 116 52228 4822 116 52228 4822 116 52228 4822 116 52228	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	W W W W W W W W W W W W W W W W W W W
3106 A 3109 3112 3119 3120 3121 3122 3123 3124 3125 3126 3127 3129 3130 3131 3132 3203 3204 3205 3206	4822 053 21225 4822 053 30338 4822 116 52271 4822 116 52271 4822 116 52215 4822 050 13302 4822 117 10314 4822 117 10161 4822 116 52269 4822 116 52215 4822 116 52215 4822 116 52233 4822 116 52284 4822 116 52284 4822 050 24708 4822 116 52175 4822 116 52224 4822 116 52224 4822 116 52224 4822 116 52224 4822 116 52224 4822 116 52224 4822 116 52224	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	W W W W W W W W W W W W W W W W W W W

COILS		
E402 A	4822 218 21101	
		TRANSFORMER
•	4822 148 81322	TRANSPONICE
5203	4822 157 53006	0.33
	4822 157 53005	0,33 μΗ
5207	4822 157 53252	22 μH 22 μH
	4822 157 53252 4822 157 53252	22 μΠ 22 μΗ
5210	4622 157 55252	22 μπ
DIODE	S	·
		415000
6110	4822 130 80858	1N5062
6111	4822 130 80858	1N5062 1N5062
6112	4822 130 80858 4822 130 80858	1N5062
6113 6114	4822 130 80858	1N5062
6115	4822 130 83307	UG06B
6116	4822 130 30621	1N4148
6201	4822 130 83121	BYD73C
6203	4822 130 81516	MUR410
6204	4822 130 81516	MUR410
6206	4822 130 83121	BYD73C
6210	4822 130 32715	SB340
TRAN	SISTORS & IC's	
	4822 209 31551	SPH4690
7105		· ·
7105 7110 *	4822 209 31528	TDA4605-3
	4822 130 62753	BUZ90A
7110		BUZ90A
7110 * 7135 *	4822 130 62753	BUZ90A

OPERATING PANELS MDCP3/VPT, NDCP2/UG, NDCB1/UG, NDCB1/PECO

CONN	IECTORS					
	4822 265 30988 4822 265 30991 4822 265 92023 only	15 pi Displ Displ	ay h	older	28GK,15-N	//T-63GK
2	4822 276 11349	Keys				
MISC	ELLANEOUS					
1001 1002	5322 242 73697 4822 242 72892	Cryst Cryst		MHz 2,768 kH	z	
CAPA	CITORS					~:
2003 2004 2005 2010 2020 2030 2031 2037 2051 2052 2999	4822 122 33847 4822 122 10462 4822 125 50412 4822 121 51387 4822 124 80229 4822 121 51387 4822 121 42408 4822 121 42386 4822 121 42408 4822 121 42408 4822 124 80238	10 15 7,5 10 22 10 220 10 100 220 470	PF P	16V 16V 16V 63V 63V	only	for ECO
RESIS	STORS	!				
3001 3002 3006 3007 3011 3012 3028 3029 3030 3031 3032 3033 3035 3036 3037 3050 3053 3053 3053	4822 116 52233 4822 116 52233	4,7 10 10 22 2,2 4,7 15 for Disp 10	kΩ kΩ	0,5W 0,5W	8GK,15-M	IT-63GK
3302 3400 3401 3402 3403	4822 116 52233 4822 116 52263 4822 116 52263 4822 116 52263 4822 116 52263	2,7 2,7 2,7	kΩ kΩ kΩ kΩ kΩ	0,5W	** ** ** ** MDCP./.,	NDCP./.

DIODI	ES	
6010	4822 130 30621	1N4148
6011	4822 130 30621	1N4148
6012	4822 130 30621	1N4148
6031	4822 130 30621	1N4148
6032	4822 130 30621	1N4148
6050	4822 130 34197	BZX79-C12
6050	4822 130 30862	BZX79-C9V1
		y for Display 14-BT-28GK,15-MT-63GK
6099	4822 130 31983	BAT85
<u> </u>		
TRAN	SISTORS & IC's	
7030	5322 130 60068	BC558C
7031	4822 130 40937	BC548B
7101	4822 209 33355	TMP87CH70AF NDCP2-1U
7101	4822 209 32296	only for NDCP2/UG TMP87CH70AF MDCP3-1U
7101	4022 209 32296	only for MDCP3/VPT, MDCB1/VPT
7101	4822 209 33494	TMP87CH70AF NDCQ1-2P
/101	4022 209 33494	only for NDCB1/PECC
7101	4822 209 33489	TMP87CK70AF NDCB2-1U
7 101	4022 203 00403	only for NDCB1/UG
7102	4822 130 91144	16-MT-44GK10R
1102	1002 100 0 1 1 4 4	only for NDCP2/UG
7102	4822 130 91213	14-BT-28GK
		DCP3/VPT, MDCB1/P, NDCB1/PECO
7102	4822 130 91363	15-MT-63GK
		only for NDCB1/UG
7103	4822 214 33534	IR-receiver TFMO-4036MM
7103	4822 212 30842	IR-receiver TFMS5360
		only for NDCB1/PECC

OPERATING PANEL NDCP4/UBG

CONN	ECTORS	
	4822 267 41161 4822 267 41163 4822 267 51281 4822 256 92072 4822 265 30991 4822 276 11349	4 pin 5 pin 15 pin IR-holder Display holder Keys
MISCE	ELLANEOUS	
1001 1002 1104 1106 1107	5322 242 73697 5322 242 73682 4822 267 31773 4822 267 31775 4822 267 31774	Crystal 8.00 MHz Crystal 32,768 kHZ HSJ1452 Edit JPJ2022 chinch YELLO JPJ2022 chinch WHITE
CAPA	CITORS	
2003 2004 2005 2010 2011 2020 2030 2031 2037 2051 2052 2401 2403 2999	4822 122 31971 4822 122 32504 4822 125 50412 4822 125 32442 4822 124 42152 4822 124 80729 4822 122 32442 4822 121 42408 4822 122 31947 4822 126 13219 4822 126 13219 4822 122 31727 4822 124 42408 4822 124 80238	10 pF 63V 15 pF 63V 7,5 pF 10 nF 50V 220 μF 16V 10 nF 50V 220 nF 63V 10 nF 50V 100 nF 50V 100 nF 63V 100 nF 63V 100 nF 63V 220 nF 63V 220 nF 63V 220 nF 63V 220 nF 63V 220 nF 63V 220 nF 63V
RESIS	STORS	
3011 3012 3028 3029 3030 3031 3032 3033 3034 3035 3050 3053 3300 3301 3302 3401 3402 3502 3901 3903	4822 051 10103 4822 051 10472 4822 051 10221 4822 051 10223 4822 051 10472 4822 051 10102 4822 051 10102 4822 051 10102 4822 051 10472 4822 051 10103 4822 051 10103 4822 051 1023 4822 051 10478 4822 051 10103 4822 051 10105 4822 051 10008	10 kΩ 0,25W 4,7 kΩ 0,25W 220 Ω 0,25W 220 κΩ 0,25W 22 kΩ 0,25W 4,7 kΩ 0,25W 2,2 kΩ 0,1W 4,7 kΩ 0,25W 10 kΩ 0,25W 10 kΩ 0,25W 2,2 kΩ 0,5W 2,2 kΩ 0,5W 2,2 kΩ 0,5W 4,7 Ω 0,25W 10 kΩ 0,25W 2,0 kΩ 0,25W 10 kΩ 0,25W
COILS	3	
5000 5001	4822 157 52286 4822 157 52285	22 μH 6,8 μH

DIODE	ES		
6010	4822 130 30621	1N4148	
6011	4822 130 30621	1N4148	
6012	4822 130 30621	1N4148	
6031	4822 130 30621	1N4148	
6032	4822 130 30621	1N4148	
6050	4822 130 34197	BZX79-B12	
6099	4822 130 31983	BAT85	
6401	4822 130 30621	1N4148	
6501	4822 130 34278	BZX79-B6V8	
6502	4822 130 34278	BZX79-B6V8	
TRAN	SISTORS & IC's		

TMP87CN71F NDCP4-1U

FIP16BM10R

4822 130 42513 BC858C 5322 130 41982 BC848B

4822 209 33495

4822 130 91144 4822 212 30842

COILS

5000 4822 157 52286 22 μH

FAMILY BOARD





			1		3	
CONN	ECTORS		2028	5322 122 33861	120 pF	50V
		The second of th	2029 2032	5322 122 32452 4822 124 40242	47 pF	63V
	4822 267 51163	10 pin	2033	5322 122 33861	1 μF 120 pF	63V 50V
	4822 267 51281 4822 267 51281	15 pin 2.3 4.3 4.3 4.3 4.3 4.3 4.3 4.3 4.3 4.3 4	2034	5322 122 32654	22 nF	63V
	4822 267 51261	16 pin not for ECO	2035	4822 124 40242	1 μF	63V
	4822 267 51281	15 pin only for ECO	2038	4822 122 33514	68 pF	50V
	4822 267 41062	6 pin	2039 2040	5322 122 33538 4822 124 22826	150 pF 10 μF	63V 16V
	4822 265 30989	3 pin Scart plug	2041	4822 124 40248	10 μF	63V
*	4822 267 60333 4822 265 30987	7 pin	2042	4822 126 10002	100 nF	25V
I	4822 267 40696	3 pin: 4.15 % ** 4. 4. 4. 4. 4. 4.	2043	5322 122 32654	22 nF	63V
1			2045 2046	4822 121 51387 4822 124 40242	10 nF 1 μF	16V 63V
MISCE	LLANEOUS		2047	4822 124 40242	1 μF	63V
7	20 to 1 to 1 to 1	9-91 - 20-00 per per per 12-00	2048	4822 122 33797	47 : nF	50V
1400 🛆	4822 071 56301	Fuse 630mA	2050	5322 122 31946	27 pF	50V
1401	4822 242 73809	Crystal 10 MHz	2052 2055	4822 124 40248 4822 126 10002	10 μF 100 nF	63V 25V
1402 🗥	4822 071 58009	Fuse 80mA	2056	4822 122 33797	47 nF	50V
1500	4822 214 33719	MDLK6D915A for PAL BG	2057	4822 122 33797	47 nF	50V
1500	4822 214 33773	MDLK6E917A for PAL BG/SEC DK	2058	4822 124 41576	2,2 μF	50V
1500	4822 214 33718	MDLK6B776A for DALL	2059 2061	4822 124 40242 5322 122 34123	1 μF 1 nF	63V 50V
1501	4000 157 00100	College PALLS ECO.	2063	5322 122 34123	1 nF	,50V
1501 1601	4822 157 60192 4822 242 81067	Coil not for PAL I & ECO Crystal 4.433 619 MC	2064	4822 122 31947	100 nF	63V
	4822 071 58001	Fuse 800mA	2065	4822 122 33514	68 pF	50V
1603	4822 320 40168	Delay line	2066 2067	5322 122 31946 5322 122 32659	27 pF 33 pF	50V 50V
1701	4822 116 90869	Resistor network only for VST	2068	4822 122 33514	68 pF	50V
1720	4822 210 10498	UV917 for VST & PAL BG	2069	5322 122 31946	27 pF	50V
1720	4822 210 10498	UV917 for VST & PAL BG/SEC DK	2070	4822 122 33515	82 pF	63V
1720	4822 210 10498	UV917 for VST & PAL I-VHF (Irland)	2071	5322 122 32452 5322 122 34123	47 pF 1 nF	63V 50V
1720	4822 210 10452	UV943 for VST & PAL I	2079	4822 122 33177	10 nF	50V
/1720	4822 210 10392	UV916E for PLL & PAL BG	2080	5322 122 34123	1 nF	50V
1720	4822 210 10392	UV916E for PLL & PAL BG/SEC DK	2082	4822 124 41576	2,2 μF	only for PAL BG/SEC DK
1720	4822 210 10392	UV916E for PLL & PAL I-VHF (Irland)	2083 2084	4822 124 41576 4822 124 41577	2,2 μF 4,7 μF	only for PAL BG/SEC DK
1720	4822 210 10393	UV944C for PLL & PAL I	2085	4822 124 41643	4,7 μF 100 μF	only for PAL BG/SEC DK only for PAL BG/SEC DK
1721	4822 242 81261	OFWG1966M for PAL BG	2086	5322 122 34123	1 nF	only for PAL BG/SEC DK
1721	4822 242 72197	OFWK2950M for PAL BG/SEC DK	2087	5322 122 31863	330 pF	50V
1721	4822 242 70936	OFWJ1952M for PAL I	2088 2090	4822 122 31947 4822 126 10002	100 nF	63V 25V
1722	4822 242 72586	TPS5,5MB for PAL BG	2095	5322 122 32452	47 pF	63V
1722	4822 242 81572	TPS6,0MB for PAL I	2096	5322 122 32452	47 pF	63V
1722	4822 242 72586	TPS5,5MB for PAL BG/SEC DK	2099	5322 122 34123	1 nF	50V
1700	4822 242 72914	SFSH5,5MDB not for PAL I	2100 2101	4822 122 31825 5322 122 33861	27 pF 120 pF	63V 50V
1723 1724	4822 242 72914	SFSH5,5MDB not for PAL I SFSH6,5MDB for PAL BG/SEC DK	2106	5322 122 32481	15 pF	50V
1724	4822 242 70279	SFE6,0MB for PAL I	2107	4822 122 33177	10 nF	50V
			2211	5322 122 32452	47 pF	63V only for LP 50V only for VPS
CAPAC	CITORS		2300	5322 122 31946 4822 122 33342	27 pF 33 nF	50V only for VPS 63V only for VPS
	1	the state of the s	2302	4822 126 10002	100 nF	25V only for VPS
2000	5322 1,22 32659	33 pF 50V	2303	4822 122 33177	10 nF	50V only for VPS
2001 2002	4822 122 33514 1 4822 126 10326	68 pF 50V	2402 2403	4822 124 40433 4822 126 10002	47 μF 100 nF	25V 25V
2002	4822 122 33575	180 pF 63V 220 pF 50V	2404	5322 126 10223	4,7 nF	63V
2004	5322 122 32965	18 pF 50V	2405	5322 122 32658	22 pF	50V
2009	4822 124 40248	10 μF 63V	2406	5322 122 32658	22 pF	50V
2010 2011	5322 122 34123 4822 124 40248	1 nF 50V 10 μF 63V	2407 2408	4822 126 10002 4822 126 10002	100 nF 100 nF	25V 25V
2012	4822 124 80705	1 μF 50V	2409	4822 122 33177	10 nF	50V
2013	4822 122 33172	390 pF 50V	2410	4822 124 40433	47 μF	25V
2014	5322 122 32452	47 pF 63V	2411	5322 126 10223	4,7 nF	63V
2015 2017	4822 122 33177 5322 122 31946	10 nF 50V 27 pF 50V	2412 2413	4822 124 41643 5322 122 34123	100 μF 1 nF	16V 50V
2018	5322 122 32531	100 pF 50V	2414	4822 122 31981	33 nF	50V
2019	5322 122 33538	150 pF 63V	2415	4822 122 31947	100 nF	63V
2020	5322 122 32531	100 pF 50V	2416	4822 126 10002	100 nF	25V
2021	4822 124 41643 4822 124 22263	100 μF 16V 10016V	2417 2418	4822 124 40433 4822 122 33177	47 μF 10 nF	25V 50V
2024	4822 124 40433	47 μF 25V	2419	5322 126 10223	4,7 nF	63V
2025	4822 126 10002	100 nF 25V	2420	4822 124 40433	47 μF	25V
2026 2027	5322 122 32531 4822 122 33515	100 pF 50V 82 pF 63V	2423 2500	4822 122 31947 5322 126 10223	100 nF 4,7 nF	63V 63V
	.022 122 00010	52 pr 00 v	2000	JULE 140 10223	4,/ IIF	00 v

FAMILY BOARD M N2

3041
3119 4822 051 20101 100 Ω 0,1W 3121 4822 051 10102 1 $k\Omega$ only for PAL BG/SEC DK 3128 4822 051 20223 22 $k\Omega$ 0,1W
$\begin{array}{cccccccccccccccccccccccccccccccccccc$

FAMILY BOARD NI N2

				_		-
FΛ	MIII	V	BO	Λ	D	n



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											-
3417 3418 3419 3420 3421 3422 3423 3424 3425 3426 3427 3428 3429 3430 3431	4822 116 52283 4822 051 10333 4822 051 20472 4822 051 20471 4822 051 10101 4822 051 20182 4822 051 20682 4822 051 20822 4822 051 20562 4822 051 10102 4822 116 52283 4822 051 20471 4822 051 20471	4,7 kΩ 4,7 kΩ 470 Ω	2 0,25W 0,1W 0,1W 0,25W 2 0,25W 2 0,1W 2 0,1W 2 0,1W 2 0,1W 2 0,5W 2 0,5W 2 0,1W 0,5W 0,1W		3515 3516 3517 3520 3521 3600 3601 3602 3603 3604 3605 3606 3607 3608	4822 051 20759 4822 116 52289 4822 116 52233 4822 051 20333 4822 155 20479 4822 051 20563 4822 051 20331 4822 051 20123 4822 051 20394 4822 051 20101 4822 100 11843 4822 051 20105 4822 051 20105	75 5,6 10 33 100 47 56 330 12 390 10 1 1 47	kΩ kΩ Ω κΩ) Ω kΩ) Ω kΩ) κΩ) κΩ	0,5W 0,1W 0,5W 0,1W 0,1W 0,1W 0,1W 0,1W 0,1W	only for LP	
3432 3433 3434 3435 3436 3437 3438 3439 3440 3441 3442 3443 3444	4822 051 10102 4822 050 11002 4822 051 20103 4822 116 52257 4822 051 20823 4822 051 20103 4822 051 20103 4822 051 10223 4822 051 20158 4822 116 52233 4822 050 11002 4822 051 10102 4822 051 20103	22 kΩ 82 kΩ 10 kΩ 3,9 kΩ 22 kΩ 1,5 Ω 10 kΩ 1 kΩ 1 kΩ 10 kΩ	2 0,4W 2 0,1W 2 0,5W 3 0,1W 2 0,1W 2 0,1W 3 0,1W 4 0,25W 0,1W 4 0,25W 0,4W 4 0,25W 0,4W 4 0,25W 0,4W 4 0,25W		3609 3610 3611 3612 3613 3614 3615 3616 3617 3618 3619 3620 3621 3623	4822 051 20104 4822 051 20123 4822 051 10333 4822 051 20183 4822 051 20153 4822 051 20471 4822 051 2023 4822 051 2023 4822 051 2023 4822 051 20158 4822 051 20158 4822 051 20103 4822 051 20103	100 12 33 18 9,1 15 470 1 22 100 1,5 47 10	kΩ kΩ kΩ kΩ kΩ kΩ	0,1W 0,25W 0,1W 0,1W 0,1W 0,1W 0,25W 0,1W	only for LP	
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7031	5322 130 41982	BC848B	
7032	5322 130 41982	BC848B	
7036	5322 130 41982	BC848B	
7037	4822 130 61495	DTA124EK	not for 4 head
7038	4822 130 61495	DTA124EK	not for 4 head
7051	4822 209 32155	LA7191	
7053	4822 209 60177	LM339D	
7301	4822 209 32728	SDA5642	only for VPS
7402	4822 209 30146	L2722	0.11, 101 VI 0
7403	5322 130 41982	BC848B	
7404	5322 130 41982	BC848B	
7405	4822 130 60145	DTC124E/25	
7406	4822 130 60145	DTC124E/25	not for ECO
7407	5322 130 41982	BC848B	not for EGG
7408	4822 130 60089	BD436	
7410	4822 209 32736	TMP91C642N	MMTD4 411
7410	4822 209 33493		NTDQ1-1U only ECO
7410			NTDQ1-10 only ECO
	4822 209 30836	SAA1310/N2	and the VCT
7412	4822 209 62098	ST24C02CP	only for VST
7412	4822 209 62098	ST24C02CP	only for ECO
7412	4822 209 32709	ST24C04CB1	only for PLL
7413	4822 130 61495	DTA124EK	
7500	5322 130 41983	BC858B	
7501	5322 130 41983	BC858B	
7502	5322 130 41982	BC848B	
7504	4822 209 30692	MSM7403RS	to DAL DO/OTO DV
7505	5322 130 41983	BC858B only	for PAL BG/SEC DK
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7505 7506 7506 7551 7602 7603 7604 7605 7606 7607 7700 7701 7702 7703 7704 7705 7706 7707 7709 7710 7711 7713	5322 130 41983 5322 130 41983 5322 209 10576 4822 209 31548 5322 130 41982 4822 130 41715 4822 130 41715 4822 130 41715 4822 130 41715 4822 209 60175 5322 209 10421 4822 209 31532 4822 209 1532 4822 209 10273 5322 130 41983 4822 130 60089 5322 130 41982 5322 130 41982 5322 130 41982 5322 130 41983 5322 130 41983 5322 130 41983 5322 130 41983 5322 130 41983	BC858B only BC858B HEF4053BD LA7282 BC848B BC337-40 BC328-40 DTC124E/25 BC848B BC328-40 LM358D HEF4094BD TDA9800/V3 TL431CLPST HEF4104BD BC858B BD436 BC848B BC848B BC848B BC848B BC848B BC848B BC848B BC848B BC848B BC858B BC857B	only for VST only for VST only for VST not for PAL I & PLL not for PAL I & PLL not for PAL I & PLL not for PAL I & PLL only for VST

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FAW	IL Y	BOARD	

FAMILY	BOARD	NFZ-	Nā

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CONNE	CTORS		2048	4822 122 32542	47 nF	63V	
00,1112	.070710		2050	5322 122 31946	27 pF	50V	
	4822 267 51163	10 pin	2052	4822 124 40248	10 μF	63V	
	4822 267 51281	15 pin	2055	4822 122 31947	100 nF	63V	
	4822 267 51164	16 pin	2056	4822 122 33797	47 nF	50V	not for SECAM L
	4822 267 51281	15 pin	2057	4822 122 33797	47 nF	50V	
	4822 267 41062	6 pin	2058	4822 124 80227	2,2 μF	35V	
	4822 265 30989	3 pin	2059	4822 124 40242	1 μF	63V	anti for TVT
	4822 265 30987	7 pin	2060	4822 122 32442	10 nF	E01/	only for TXT
	4822 267 40696	3 pin	2061 2063	5322 122 34123 5322 122 34123	1 nF	50V 50V	not for SECAM L not for SECAM L
	4822 267 31513	SCART	2063	4822 122 31947	100 nF	63V	HOLIOL SECAMIL
			2065	4822 122 33514	68 pF	50V	
MICCE	LLANEOUS	12.3	2066	5322 122 31946	27 pF	50V	
MISCEI	LLANEOUS		2067	5322 122 32659	33 pF	50V	
1400 🗥	4822 071 56301	Fuse 630mA	2068	4822 122 33514	68 pF	50V	
1401	4822 242 73809	Crystal 10 MHz	2069	5322 122 31946	27 pF	50V	
· A	4822 071 58009	Fuse 80mA	2070	4822 122 33515	82 pF	63V	
1500	4822 214 33713	MDLK6D906A only for PAL BG	2070.	4822 122 33514	68 pF		only for LP
1500	4822 214 33718	MDLK6B776A only for PAL I	2070	4822 126 10004	120 pF		only for SECAM L
1500	4822 214 33417	Boster only for SECAM L & Multistd.	2071	5322 122 32452	47 pF	63V	
1501	4822 157 60192	Group Delay not for PAL I	2072	5322 122 34123	1 nF	50V	.,
1601	4822 242 81067	Crystal 4.433 619 MHZ	2079	4822 122 33177	10 nF	50V	not for SECAM L
Α.	4822 071 58001	Fuse 800mA	2080	5322 122 34123	1 nF	50V	not for SECAM L
1603	4822 320 40168	Delay line not for SECAM L	2087	5322 116 80853	560 pF	63V	**!· f== 0504141
1701	4822 210 10392	UV916E	2087	4822 122 33575	270 pF	601/	only for SECAM L
1701	4822 210 10393	UV944C only for PAL I	2088	4822 122 31947	100 nF 100 nF	63V	
1720	4822 242 81261	OFWG1966M only for PAL BG	2090 2095	4822 122 31947	100 nF 47 pF	63V 63V	
1720	4822 242 81436	OFWK3953M only for SECAM L	2095	4822 122 31772 5322 122 32452	47 pF	63V	
1720	4822 242 70936	OFWJ1952 only for PAL I	2096	5322 122 32452	27 pF	50V	not for SECAM L
1721	4822 242 81259	OFWK6256 only for Multistandard	2101	5322 122 31946	120 pF	50V	not for SECAM L
1740	4822 242 72586	TPS5,5MB-TF20 only for PAL BG	2106	5322 122 32481	15 pF	50V	not for SECAM L
1745	4822 242 72914	SFSH5,5MDB only for PAL BG	2107	4822 122 33177	10 nF	50V	not for 4 head
1745	4822 242 72577	SFE6,0MB only for PAL I	2211	5322 122 32452	47 pF	501	only for 4 head
1821	4822 242 81423	OFWL9453M only for Multistandard	2402	4822 124 40433	47 µF	25V	5, .5. 1
			2403	5322 122 32654	22 nF	63V	
CAPAC	ITORS	to the state of th	2404	5322 126 10223	4,7 nF	63V	
			2405	5322 122 32658	22 pF	50V	
2000	5322 122 32659	33 pF 50V	2406	5322 122 32658	22 pF	50V	
2001	4822 122 33514	68 pF 50V	2407	4822 122 31947	100 nF	63V	
2002	4822 126 10326	180 pF 63V	2408	4822 122 31947	100 nF	63V	
2003	4822 122 33575	220 pF 50V	2409	4822 122 33177	10 nF	50V	
2004	5322 122 32965	18 pF 50V	2410	4822 124 40433	47 μF	25V	
2009	4822 124 80454	10 μF 16V	2411	5322 126 10223	4,7 nF	63V	
2010	5322 122 34123	1 nF 50V	2412 2413	4822 124 40433 5322 122 34123	47 μF 1 nF	25V	
2011	4822 124 40248	10 μF 63V	2413	4822 122 33811	33 nF	50V 50V	
2012	4822 124 80705	1 μF 50V	2414	4822 126 10002	100 nF	25V	
2013	4822 122 31771	390 pF 63V	2415	4822 126 10002	100 nF	25V	
2014	5322 122 32452	47 pF 63V	2417	4822 124 40433	47 μF	25V	
2015	4822 122 33177	10 nF 50V	2418	4822 122 33177	10 nF	50V	
2018 2019	5322 122 32531 5322 122 33538	100 pF 50V 150 pF 63V	2419	5322 126 10223	4,7 nF	63V	
2020	5322 122 32531	100 pF 63V	2420	4822 124 40433	47 μF	25V	
2021	4822 124 41584	100 pr 30V 100 µF 10V	2423	4822 122 31947	100 nF	63V	
2022	5322 122 31946	27 pF 50V	2424	5322 122 32654	22 nF	63V	
2023	4822 124 40196	220 μF 16V	2506	4822 126 10002	100 nF	25V	
2024	4822 124 40433	47 μF 25V	2507	4822 124 22826	10 μF	16V	
2025	5322 122 32654	22 nF 63V	2530	5322 122 32268	470 pF	50V	
2026	5322 122 32531	100 pF 50V	2531	5322 122 32268	470 pF	50V	
2027	4822 122 33515	82 pF 63V not for SECAM L	2540	5322 122 32268	470 pF	50V	
	5322 122 33861	120 pF 50V	2592	4822 122 33177	10 nF	50V	
	5322 122 32452	47 pF 63V	2600	5322 122 34123	1 nF	50V	and the state of t
029	1000 100 0	100 nF 63V	2601 2602	5322 122 31865 4822 122 31947	1,5 nF 100 nF		only for 4 head
029 030	4822 122 31947				100 nF 10 nF	50V	only for 4 head
029 030 032	4822 124 23053	1 μF 50V		4822 122 333 / /		- /1. / V	
2029 2030 2032 2033	4822 124 23053 5322 122 33861	1 μF 50V 120 pF 50V ποι for ΘΕCAM L	2604	4822 122 33177 4822 124 40433			
2029 2030 2032 2033 2034	4822 124 23053 5322 122 33861 5322 122 32654	1 µF 50V 120 pF 50V not for SECAM L 22 nF 63V	2604 2605	4822 124 40433	47 μF	25V	
2029 2030 2032 2033 2034 2035	4822 124 23053 5322 122 33861 5322 122 32654 4822 124 40242	1 μF 50V 120 pF 50V ποι for SECAM L 22 nF 63V 1 μF 63V	2604 2605 2606	4822 124 40433 4822 122 31947	47 μF 100 nF	25V 63V	
2029 2030 2032 2033 2034 2035 2038	4822 124 23053 5322 122 33861 5322 122 32654 4822 124 40242 4822 122 33514	1 μF 50V 120 pF 50V ποι for SECAM L 22 nF 63V 1 μF 63V 68 pF 50V	2604 2605 2606 2607	4822 124 40433 4822 122 31947 4822 122 31947	47 μF 100 nF 100 nF	25V 63V 63V	
2029 2030 2032 2033 2034 2035 2038 2039	4822 124 23053 5322 122 33861 5322 122 32654 4822 124 40242 4822 122 33514 5322 122 33538	1 μF 50V 120 pF 50V ποι for SECAM L 22 nF 63V 1 μF 63V 68 pF 50V 150 pF 63V	2604 2605 2606	4822 124 40433 4822 122 31947 4822 122 31947 5322 122 32268	47 μF 100 nF 100 nF 470 pF	25V 63V 63V 50V	
2029 2030 2032 2033 2034 2035 2038 2039 2040	4822 124 23053 5322 122 33861 5322 122 32654 4822 124 40242 4822 122 33514 5322 122 33538 4822 124 22826	1 μF 50V 120 pF 50V ποι for ΘΕCAM L 22 nF 63V 1 μF 63V 68 pF 50V 150 pF 63V 10 μF 16V	2604 2605 2606 2607 2608	4822 124 40433 4822 122 31947 4822 122 31947	47 μF 100 nF 100 nF 470 pF 47 μF	25V 63V 63V 50V 25V	
2029 2030 2032 2033 2034 2035 2038 2039 2040 2041	4822 124 23053 5322 122 33861 5322 122 32654 4822 124 40242 4822 122 33514 5322 122 33538 4822 124 22826 4822 124 22826	1 μF 50V 120 pF 50V ποι for ΘΕCAM L 22 nF 63V 1 μF 63V 68 pF 50V 150 pF 63V 10 μF 16V 10 μF 16V	2604 2605 2606 2607 2608 2609	4822 124 40433 4822 122 31947 4822 122 31947 5322 122 32268 4822 124 40433	47 μF 100 nF 100 nF 470 pF	25V 63V 63V 50V	
2029 2030 2032 2033 2034 2035 2038 2039 2040 2041 2042	4822 124 23053 5322 122 33861 5322 122 32654 4822 124 40242 4822 122 33514 5322 122 33538 4822 124 22826	1 μF 50V 120 pF 50V ποι for ΘΕCAM L 22 nF 63V 1 μF 63V 68 pF 50V 150 pF 63V 10 μF 16V	2604 2605 2606 2607 2608 2609 2610	4822 124 40433 4822 122 31947 4822 122 31947 5322 122 32268 4822 124 40433 4822 122 33175	47 μF 100 nF 100 nF 470 pF 47 μF 2,2 nF	25V 63V 63V 50V 25V 50V	only for 4 head
2029 2030 2032 2033 2034 2035 2038 2039 2040 2041 2042 2043	4822 124 23053 5322 122 33861 5322 122 32654 4822 124 40242 4822 122 33514 5322 122 33538 4822 124 22826 4822 124 22826 4822 126 10002	1 μF 50V 120 pF 50V ποι for ΘΕCAM L 22 nF 63V 1 μF 63V 68 pF 50V 150 pF 63V 10 μF 16V 10 μF 16V	2604 2605 2606 2607 2608 2609 2610 2611 2612 / 2613	4822 124 40433 4822 122 31947 4822 122 32268 4822 124 40433 4822 122 33175 4822 124 40433 5322 124 23254 4822 122 32442	47 μF 100 nF 100 nF 470 pF 47 μF 2,2 nF 47 μF 22 nF 10 nF	25V 63V 63V 50V 25V 50V 25V	only for 4 head
2043 2045 2046	4822 124 23053 5322 122 33861 5322 122 32654 4822 124 40242 4822 122 33514 5322 122 33538 4822 124 22826 4822 124 22826 4822 124 22826 4822 122 31797	1 μF 50V 120 pF 50V ποι for ΘΕCAM L 22 nF 63V 1 μF 63V 68 pF 50V 150 pF 63V 10 μF 16V 10 μF 16V 100 μF 25V 22 nF 63V	2604 2605 2606 2607 2608 2609 2610 2611 2612	4822 124 40433 4822 122 31947 4822 122 31947 5322 122 32268 4822 124 40433 4822 122 33175 4822 124 40433 5322 122 32654	47 μF 100 nF 100 nF 470 pF 47 μF 2,2 nF 47 μF 22 nF	25V 63V 63V 50V 25V 50V 25V	only for 4 head

2616	4822 124 40433	47 μF 25V		3065	4822 051 10102	1 $k\Omega$	0,25W	·
2617	4822 124 40433	47 μF 25V		3066	4822 051 20122	1,2 kΩ	0,1W	W. C.
2618	5322 122 31863	330 pF 50V		3067	4822 051 20391	390 Ω	0,1W	
2619	4822 124 40433	47 μF 25V	*	3068	4822 051 10102	1 kΩ	0,25W	
2620	4822 121 43526	47 nF 250V		3069	4822 051 20561	560 Ω	0,1W	
2621	5322 122 34123	1 nF 50V		3070	4822 051 20391	390 Ω	0,1W	
2622	5322 121 42489	33 nF 250V		3071	4822 051 20681	680 Ω	0,1W	
2623	4822 122 32442	10 nF 50V		3072	4822 051 20271	270 Ω 1 kΩ	0,1W 0,25W	4.
2624	4822 124 40242	1 μF 63V		3073 3075	4822 051 10102	1 kΩ 470 Ω	0,25VV 0,1W	
2701	4822 122 31947	100 nF 63V 10 nF 50V		3075	4822 051 20471 4822 051 20331	330 Ω	0,1W	
2702 2703	4822 122 33177 5322 124 41431	22 μF 35V		3077	4822 051 20183	18 $k\Omega$	0,1W	
2703	4822 124 40242	1 μF 63V		3078	4822 051 20183	18 kΩ	0,1W	
2720	5322 122 32531	100 pF 50V		3089	4822 051 10102	1 kΩ	0,25W	not for SECAM L
2725	4822 122 31947	100 pF 63V		3090	4822 051 10102	1 kΩ	0,25W	
2726	5322 122 32654		not for SECAM L	3091	4822 051 20562	5,6 kΩ		not for SECAM L-
2727	4822 124 80227		not for SECAM L	3092	4822 051 20332	3,3 kΩ	0.1W	not for SECAM L
2728	4822 124 40248	, I	not for SECAM L	3093	4822 051 20152	1,5 kΩ	0,1W	not for SECAM L
2737	4822 122 31947	100 nF 63V		3094	4822 051 20222	2,2 k Ω	0,1W	not for SECAM L
2738	5322 124 41431	22 μF 35V		3095	4822 116 52283	4,7 k Ω	0,5W	only for SECAM
2739	4822 122 31947	100 nF 63V		3096	4822 100 12157	10 kΩ		not for SECAM L
2740	4822 124 41576	2,2 μF 50V		3097	4822 051 20332	3,3 kΩ	0,1W	
2741	4822 122 31947	100 nF 63V		3099	4822 100 12155	2,2 kΩ		
2790	4822 124 80228	4,7 μF 25V		3100	4822 051 10102	1 kΩ	0,25W	and the second
2791	4822 124 80231	47 μF 16V		3104	4822 051 20472	$4.7 k\Omega$	0,1W	
2833	4822 122 31765		CAM L & Multist.	3105	4822 051 20122	1,2 kΩ	0,1W	
2843	4822 124 22826	10 μF 16V		3106	4822 051 20271	270 Ω	0,1W	
	*			3109	4822 051 20561	560 Ω	0,1W	
RESIS	TORS			3111	4822 051 20471	470 Ω 2.2 kΩ	0,1W	
TILOTO	710710			3112 3116	4822 051 20222 4822 116 52233	2,2 kΩ 10 kΩ	0,1W 0,5W	
3001	4822 116 52224	470 Ω 0,5W		3117	4822 051 20183	18 kΩ	0,5W	
3007	4822 051 20101	100 Ω 0,1W		3119	4822 116 52175	100 Ω	0,5W	
3002	4822 051 20222	2,2 kΩ 0,1W	only for TXT	3128	4822 051 20223	22 kΩ	0,1W	not for SECAM L
3003	4822 051 20472	4,7 kΩ	only for TXT	3132	4822 051 20681	680 Ω	0,1W	not for SECAM L
3004	4822 051 20104	100 kΩ 0,25W	only for TXT	3135	4822 051 20104	100 kΩ	0,1W	
3005	4822 117 10834	47 kΩ	only for TXT	3137	4822 051 10102	1 $k\Omega$	0,25W	
3006	4822 051 20472	4,7 $k\Omega$	only for TXT	3218	4822 051 20103	10 kΩ	0,1W	only for 4 head
3008	4822 051 20223	22 k Ω 0,1W		3407	4822 051 20222	$2,2$ k Ω	0,1W	
3009	4822 051 20222		not for SECAM L	3410	4822 116 52199	Ω 88	0,5W	
3010	4822 100 12156	4,7 kΩ		3411	4822 116 52199	68 Ω	0,5W	
3011	4822 117 10833	10 kΩ 0,1W		3412	4822 051 20472	$4,7 k\Omega$	0,1W	
3014	4822 051 10104	100 kΩ 0,25W		3413	4822 116 52283	$4,7 k\Omega$	0,5W	
3017	4822 117 10833	10 kΩ 0,1W		3414	4822 051 20472	4,7 kΩ	0,1W	
3018	4822 100 12156	4,7 k Ω 6,8 k Ω 0,25W		3415	4822 050 11002	1 kΩ	0,4W	
3019 3020	4822 051 10682 4822 100 12156	4,7 kΩ		3416 3417	4822 116 52234 4822 116 52283	100 kΩ 4,7 kΩ	0,5W 0,5W	
3021	4822 051 20272	$2.7 k\Omega 0.1W$		3417	4822 051 20333	$33 k\Omega$	0,3 V V	
3022	4822 051 20821	820 Ω 0,1W		3419	4822 051 10102	1 kΩ	0,25W	
3023	4822 051 10102	1 kΩ 0,25W		3420	4822 116 52224	470 Ω	0,5W	
3025	4822 051 20104	100 kΩ 0,1W		3421	4822 051 10101	100 Ω	0,25W	not for LP
3026	4822 051 20472	$4,7$ k Ω $0,1$ W		3422	4822 051 20182	1,8 kΩ	0,1W	
3027	4822 051 20681	680 Ω 0,1W		3423	4822 051 20182	1,8 kΩ	0,1W	
3028	4822 051 20472	$4,7$ k Ω $0,1$ W		3424	4822 117 10833	10 kΩ	0,1W	
3029	4822 051 20472	4,7 kΩ 0,1W		3425	4822 117 10833	10 kΩ	0,1W	1.0
3030	4822 051 20222	2,2 kΩ 0,1W		3426	4822 051 20682	6,8 kΩ	0,1W	4.5
3031	4822 051 20333	33 kΩ 0,1W		3427	4822 051 20122	1,2 kΩ	0,1W	and the second section of
3033	4822 051 10102	1 $k\Omega$ 0,25W		3428	4822 116 52283	4,7 kΩ	0,5W	The second second
3034 3036	4822 051 20222 4822 051 20392	2,2 k Ω 0,1W 3,9 k Ω 0,1W		3429	4822 116 52283	4,7 kΩ	0,5W	•
3037	4822 051 20122	$1,2 k\Omega 0,1W$		3430 3431	4822 116 52224 4822 050 11002	470 Ω 1 kΩ	0,5W 0,4W	
3038	4822 051 20392	$3,9 \text{ k}\Omega = 0,1\text{W}$		3432	4822 050 11002	1 kΩ	0,4W	
3039	4822 100 12157	10 kΩ		3433	4822 117 10833	10 kΩ	0,1W	
3040	4822 051 20222	2,2 kΩ 0,1W		3434	4822 051 10223	22 kΩ	0,25W	
3041	4822 051 20472	$4,7$ k Ω $0,1$ W		3435	4822 051 20823	82 kΩ	0,1W	
3043	4822 051 20561	560 Ω 0,1W		3436	4822 051 20223	22 kΩ	0,1W	-
3044	4822 051 10102	1 kΩ 0,25W		3437	4822 117 10833	10 kΩ	0,1W	
3045	4822 051 20472	$4,7$ k Ω $0,1$ W		3438	4822 051 20392	-3,9 kΩ	0,1W	
3046	4822 051 20222	2,2 kΩ 0,1W		3439	4822 051 20223	22 k Ω	0,1W	
3051	4822 051 20105	1 MΩ 0,1W		3440	4822 051 10158	1,5 Ω	0,25W	
3052	4822 051 20822	8,2 kΩ 0,1W		3441	4822 116 52233	10 kΩ	0,5W	•
3054	4822 051 20562	$5,6$ k Ω $0,1W$		3442	4822 050 11002	1 kΩ	0,4W	
3055 3061	4822 051 20821 4822 051 20008	820 Ω 0,1W 0 Ω 0,1W 1	not for SECAM L	3443	4822 051 10102	1 kΩ	0,25W	
3062	4822 051 20008		not for SECAM L	3444 3445	4822 051 10103 4822 051 10223	10 kΩ 22 kΩ	0,25W 0,25W	
3063	4822 051 20271	,	not for SECAM L	3446	4822 051 10223	$1 k\Omega$	0,25W	
3064	4822 051 20561		not for SECAM L	3447	4822 116 52233	10 kΩ	0,5W	
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4822 051 20104

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4822 051 20393

4822 051 20472

4822 051 20471

4822 051 20221

4822 051 20472

4822 051 20472

4822 051 20472

4822 116 52233

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4822 051 10102

4822 116 52224

4822 051 20472

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4822 116 52256

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4822 051 20158

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4822 050 11002

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4,7 kO

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10 kΩ

470 Ω

220 Ω

100 Ω

100 Ω

100 Ω

470 Ω

470 Ω

10 kΩ

4,7

4,7 kΩ

1.8

4.7

4,7 kΩ

2,2

10

8,2 kΩ

10

1,5 Ω

1,5

10 kO.

4,7 kΩ

4,7 kΩ

2,2 kΩ

1.8

4,7

2,2 kΩ

10

10

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47

3,3 kΩ

3.3

4,7 kO.

1,5 kΩ

82 Ω

6.8 kΩ

6,8 $k\Omega$

220 kΩ

470 Ω

47 Ω

330 Ω

12 kΩ

390 kΩ

100 Ω

100 kΩ

kΩ

kO.

10 kΩ

47 kΩ

10

33

15

4,7 kΩ

12 kΩ

470 Ω

560 Ω

100 kQ 0.1W

4,7 kΩ

kΩ 0.1W

MΩ 0.1W

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100 kΩ 0.1W

kΩ

kΩ

kΩ

4.7 kΩ 0.1W

kΩ

kΩ

kΩ

kΩ

kΩ

kO

kΩ

kQ.

kΩ

kΩ

kΩ

kΩ

330 Ω

-24

FAMILY BOARD NE

F000	4822 157 53253	27	μН	
5002	4822 157 53253 4822 157 53265	100	μΗ	
5003	4822 157 52842	15	μΗ	
5004 5005	4822 157 53253	27	μН	not for SECAM L
5005	4822 157 53253	8,2	μН	
5008	4822 157 53251	8,2	μН	
5008	4822 157 53251	15	μН	
5010	4822 157 53253	27	μН	
5010	4822 157 52842	15	μН	
5011	4822 157 53265	100	μН	
5012	4822 157 52842	15	μH	
5013	4822 157 53251	8,2	μН	
5015	4822 157 63676	56	μН	
5016	4822 157 53253	27	μH	
5017	4822 157 63676	56	μН	
5021	4822 157 50961	22	μH	
5022	4822 157 52285	6,8	μH	
5023	4822 157 63675	330	μH	not for SECAM L
5024	4822 157 63678	560	μH	not for SECAM L
5025	4822 157 53253	27	μН	
5401	4822 157 52286	22	μH	
5402				
5501	4822 157 52285	6,8	μН	
5601	4822 157 70038			
5602	4822 158 10525	LAL	04T 331K	
5603	4822 157 53531			
5604	4822 157 62681			
5700	4822 157 70402	33	μH	
5701	4822 157 52285	6,8	μН	
5720	4822 157 62681			
5725	4822 157 70017			
5727	4822 157 63717	6,8	μН	
5740	4822 157 63343			

6001	4822 130 30621	1N4148	
6002	4822 130 30621	1N4148	
6004	4822 130 31983	BAT85	
6005	4822 130 30621	1N4148	
6006	4822 130 30621	1N4148	
6401	4822 130 30621	1N4148	
6402	4822 130 30621	1N4148	
6503	4822 130 30621	1N4148	
6504	4822 130 31024	BZX79-B18	
6508	4822 130 34278	BZX79-B6V8	
6520	4822 130 34197	BZX79-B12	
6522	4822 130 34197	BZX79-B12	
6530	4822 130 34197	BZX79-B12	
6531	4822 130 34197	BZX79-B12	
6540	4822 130 34197	BZX79-B12	
6703	5322 130 32076	BAT18	
6790	4822 130 34278	BZX79-B6V8	
6791	4822 130 34233	BZX79-B5V1	

TRAN	SISTORS & IC's		
7000	5322 130 41983	BC858B	
7001	5322 130 41982	BC848B	only for TXT
7002	5322 130 41982	BC848B	only for TXT
7006	5322 130 41983	BC858B	
7007	5322 130 41982	BC848B	
7008	4822 130 61495	DTA124EK	only for 4 head
7010	5322 130 41983	BC858B	
7014	5322 130 41982	BC848B	
7015	4822 130 61495	DTA124EK	not for SECAM L
7016	5322 130 41982	BC848B	not for SECAM L
7017	4822 130 42353	BSF19-F2	1
7018	4822 130 60383	BF824	
7019	4822 130 42353	BSF19-F2	
7020	4822 130 61495	DTA124EK	only for 4 head
7025	5322 130 41982	BC848B	not for SECAM L
7026	5322 130 41982	BC848B	not for SECAM L
	5322 130 41982	DC040D	HOLIGI GEOMINE

7029	4822 130 42353	BSF19-F2	
7030	5322 130 41982	BC848B	
7031	5322 130 41982	BC848B	
7032	5322 130 41982	BC848B	
7036	5322 130 41982	BC848B	
7037	4822 130 61495	DTA124EK	not for 4 head
7038	4822 130 61495	DTA124EK	not for 4 head
7051	4822 209 32155	LA7191	
7053	4822 209 60177		for Multistandard
7402	4822 209 30146	L2722	
7403	5322 130 41982	BC848B	
7404	5322 130 41982	BC848B	
7406	4822 130 60089	BD436	
7407	5322 130 41982	BC848B	=00
7408	4822 130 60729	DTC124EEKT-96	not for ECO
7409	4822 130 60729	DTC124EEKT-96	not for ECO
7410	4822 209 32736	TMP91C642N-MM	ITD4-4U
7411	4822 209 30836	SAA1310/N2	
7412	4822 209 32283	ST24C08CB1	only for TXT
7412	4822 209 32709	ST24C04CB1	
7413	4822 130 61495	DTA124EK	
7500	5322 130 41983	BC858B	
7501	5322 130 41983	BC858B	
7504	4822 209 30692	MSM7403RS	
7506	5322 130 41982	BC848B	
7592	5322 209 14481	HEF4053BT	,
7601	4822 209 31548	LA7282	
7602	5322 130 41982	BC848B	
7603	4822 130 41344	BC337-40	
7604	4822 130 41715	BC328-40	
7605	5322 130 41982	BC848B	
7606	5322 130 41982	BC848B	
7607	4822 130 41715	BC328-40	
7720	4822 209 31532	TDA9800/V3	not for SECAM L
7721	5322 130 41982	BC848B	not for SECAM L
7723	5322 130 41983	BC858B	HOURDI SECAMIL
7761	5322 130 41983	BC858B	
7790	4822 130 44283	BC636	
7791	5322 130 41982	BC848B	
7792	5322 130 41982	BC848B	

4822 130 44283

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⚠Safety		1	11. 1 . 1	
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FAMILY BOARD

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not for ECO

not for ECO

not for ECO

only for 4 head

not for ECO

not for ECO

not for 4 head

not for 4 head

only for 4 head

only for 4 head

4822 051 20223

4822 100 12159

4822 051 10158

4822 117 10834

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4822 117 10833

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4822 051 20479

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4822 117 10833

4822 051 20224

4822 116 52175

4822 116 52175

4822 051 10008

4822 116 52257

4822 051 20103

4822 117 10833

4822 051 20154

4822 051 20682

4822 051 20223

4822 051 20152

4822 051 20823

4822 051 20332

4822 051 20183

4822 051 20183

4822 116 52256

4822 117 10834

4822 100 12158

4822 051 20271

4822 051 20561

4822 051 20561

4822 051 20223

4822 051 20222

4822 100 12156

4822 051 20222

4822 051 20222

4822 116 52176

4822 051 20331

4822 116 52176

4822 051 10102

4822 051 20101

4822 051 20681

4822 051 20101

4822 051 20391

4822 051 10008

4822 051 10008

4822 051 10008

4822 051 10102

4822 051 10102

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22 kΩ 0,1W

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270 Ω

560 Ω

560 Ω

0.1W

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0,1W

0.25W

0.1W

0,1W

0.1W

0.5W

0.5W

0,25W

not for PAL BG

not for SECAM L

0.1W not for SECAM L

0,1W not for SECAM L

0,1W not for SECAM L

0,5W not for SECAM L

0,1W not for SECAM L

0.1W not for SECAM L

not for TXT

only for TXT

not for SECAM L

0.5W

0.1W

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0.1W only for SECAM L

not for 4 head

only for TXT

only for TXT

not for TXT

not for TXT

only for PAL

only for 4 head

100 kΩ

1,5 Ω

47

22

47

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3,3

33

47 Ω

3,3 $k\Omega$

10

22

10 kΩ

10

22 kΩ

1,5 kΩ

82

3,3 kΩ

18

18

2,2 47

22

22

2.2 kΩ

4,7 kΩ

2,2

2,2

10 Ω

10 Ω

330 Ω

100 Ω

680 Ω

100 Ω

390 O

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220 kΩ

100 Ω

100 Ω

150 kΩ

6,8 kΩ

FAMILY BOARD



CON	NECTORS					2059	4822 124 23053	3 ! 1	μF	50V
	4822 267 51281 4822 267 51164 4822 267 41062 4822 267 41162 4822 265 30987 4822 267 40696 4822 267 41161 4822 267 41163 4822 267 31513	15 pin 16 pin 6 pin 3 pin 7 pin 3 pin 4 pin 5 pin SCART				2061 2063 2073 2076 2079 2080 2081 2088 2090 2095	5322 122 34123 5322 122 34123 5322 122 34123 4822 122 32442 4822 122 33177 5322 122 34123 5322 122 34123 4822 126 13219 4822 126 10002 5322 122 32452	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	nF nF nF nF nF nF nF pF	50V 50V 50V 50V 50V 50V 25V 25V 63V
					<u> </u>	2096 2098	5322 122 31946		pF pF	63V 50V
MISC	ELLANEOUS					2101 2106	5322 122 33861 5322 122 32481	120 15		50V 50V
1002 1400 1401 1402 1500 1500 1601 1701 1720 1721 1740 1741 1745 1746 1821	4822 071 58001 4822 071 56301 4822 242 73809 4822 071 58009 4822 214 33417 4822 214 33713 4822 242 81067 4822 320 40168 4822 210 10392 4822 242 81261 4822 242 81259 4822 242 81259 4822 242 81572 4822 242 81265 4822 242 81265 4822 242 81265	Crystal 4 Delay lin UV916E OFWG19 OFWK62 TPS5,5M TPS6,0M SFSH5,5	0mA 10 MHz mA on DLK6D906A 1.433 619 MHz e 966M 256K on MB-TF20	only for ly for SE ly for SE	r PAL CAM	2402 2403 2404 2405 2406 2407 2408 2410 2411 2412 2413 2414 2415 2416 2417 2418	5322 124 41431 4822 126 10002 5322 126 10223 5322 122 32658 5322 122 32658 4822 124 41584 4822 126 10002 4822 122 33177 4822 124 40433 5322 126 10223 4822 124 40433 5322 126 10223 4822 122 3191 4822 126 10002 4822 124 40433 4822 126 10002 4822 124 40433 4822 124 40433 4822 122 33177	22 100 4,7 22 22 100 100 47 4,7 4 1 1 3 3 100 47 100 47	, до по	35V 25V 63V 50V 10V 25V 25V 25V 25V 25V 25V 25V 25V 50V
CAPA	CITORS	-24 N.		35	F.3 (2419 2420	5322 126 10223 4822 124 40433	4,7	nF μF	63V 25V
2000 2001 2002 2003 2004 2009 2010 2011 2012 2013 2014 2015 2017 2018 2021 2022 2021 2023 2025 2027 2028 2029 2033 2034 2035 2038 2039 2040 2040 2040 2041 2043 2044 2044 2044 2044 2045 2046 2047 2048 2049 2040 2040 2040 2040 2040 2040 2040	5322 122 32659 4822 126 10326 4822 122 33575 5322 122 32965 4822 124 80535 5322 122 34123 4822 124 22826 4822 124 80705 4822 126 13222 5322 122 32452 4822 122 32531 4822 122 32531 4822 122 32531 4822 122 32531 4822 122 32531 4822 122 32531 5322 122 32531 5322 122 32531 5322 122 33515 5322 122 33515 5322 122 33651 5322 122 33651 5322 122 33651 5322 122 33538 6822 124 40196 5322 122 33515 5322 122 33515 5322 122 33515 5322 122 33515 5322 122 33515 5322 122 33651 5322 122 33651 5322 122 33654 4822 124 23053 4822 124 23053 4822 124 23053 4822 124 3053 4822 124 3053 4822 122 33514 5322 122 33514 5322 122 33514 5322 122 33538 4822 124 3053 4822 124 3053 4822 124 3053 4822 124 3053 4822 124 3053 4822 124 3053 4822 124 3053 4822 124 3053 4822 124 3053 4822 124 3053 4822 124 3053 4822 124 3053 4822 124 3053 4822 124 3053 4822 124 3053 4822 124 3053 4822 124 3053 4822 124 3053 4822 124 3053 4822 124 3053 4822 124 3053 4822 124 3053 4822 124 3053 4822 124 3053 4822 124 3053 4822 124 3053 4822 124 3053 4822 124 3053	33 pF	50V 50V 50V 50V 16V 50V 16V 50V 63V 50V 63V 63V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 50V 63V 50V 63V 50V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 63V 50V 50V 50V 50V 50V 50V 50V 50			2421 2422 2423 2428 2431 2504 2505 2506 2507 2530 2531 2540 2601 2602 2604 2605 2606 2607 2608 2609 2610 2611 2612 2613 2614 2615 2616 2617 2618	4822 122 33177 4822 124 40433 4822 126 10002 5322 122 32654 4822 126 10002 4822 126 10002 4822 126 10002 4822 126 10002 4822 126 10002 4822 122 32268 5322 122 32268 5322 122 32268 5322 122 34123 5322 122 321865 4822 126 10002 4822 126 10002 4822 126 10002 4822 122 33177 4822 124 40433 4822 122 32268 4822 122 3268 4822 124 40433 4822 124 40433 4822 124 40433 5322 122 3268 4822 124 40433 5322 122 3268 4822 124 40433 5322 122 3268 4822 124 40433 5322 122 3268 4822 124 40433 4822 124 40433 4822 124 40433 5322 122 34123 4822 121 43526 5322 122 34123 4822 121 43526 5322 122 34123 4822 121 4357 4822 121 43187 4822 121 43187 4822 121 40433 4822 121 43187 4822 121 40433 4822 121 43187 4822 121 40433 4822 121 43187 4822 121 40433 4822 121 43187 4822 124 40433 4822 121 40433 4822 121 40433 4822 121 40433 4822 121 40433 4822 121 40433 4822 121 40433 4822 121 40433 4822 121 40433 4822 121 40433 4822 121 40433 4822 121 40433 4822 124 40433 4822 124 40433	10 47 100 22 4,7 100 100 100 470 470 47 11,5 100 100 470 47 2,2 47 47 47 47 47 47 47 47 11 12 10 11 33 27 10 47 11 11 11 11 11 11 11 11 11 11 11 11 11		25V 25V 25V 25V 25V 25V 25V 25V 25V 25V

FAMILY BOARD



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5322 122 34123 4822 122 33177 5322 122 32658 5322 122 34123 5322 122 32531 4822 122 33177 5322 122 34123 5322 122 32659 4822 122 33177 4822 122 33177 4822 122 33177 4822 122 33177 4822 122 33177 4822 122 33177 4822 122 331947 4822 122 33575 4822 122 31947 4822 122 31947 4822 122 331947 4822 122 33177 4822 122 33177 4822 122 33177 4822 122 33177 4822 122 33538 5322 122 33538 4822 122 33177 4822 122 33538	1 10 22 1 100 10 1 33 10 10 10 10 10 100 100 10		50V 50V 50V 50V 50V 50V 50V 50V	
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4822 051 20223 4822 051 10102 4822 051 20331 4822 051 20332 4822 051 20821 4822 051 20681 4822 051 20391 4822 051 20391 4822 051 20391 4822 051 20122 4822 051 20122 4822 051 20122 4822 051 20122 4822 051 20152 4822 051 20152 4822 051 20152 4822 051 20152 4822 051 20222 4822 051 20222 4822 051 20222 4822 051 20222 4822 051 20222 4822 051 20183 4822 051 20222 4822 051 20152 4822 051 20152 4822 051 20152 4822 051 20152 4822 051 20152 4822 051 20152 4822 051 20152 4822 051 20152 4822 051 20153 4822 051 20473 4822 051 20473 4822 051 20473 4822 051 20473 4822 051 20008 4822 051 20008	22 1 330 3,3 820 680 1 390 820 1 1,2 2,2 1 1,5 10 33 2,2 2,2 1,5 10 1 560 4,7 1 47 47 10 0	(2)(3)(4)(4)(5)(6)(7)(8)(8)(8)(8)(8)(8)(8)(8)(8)(8)(8)(8)(8)(8)(8)(8)(8)(8)(8)(8)(8)(8)(8)(8)(8)(8)(8)(8)(8)(8)(8)(8)(8)(8)(8)(8)(8)(8)(8)(8)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)(9)	0,1W 0,25W 0,1W 0,1W 0,1W 0,1W 0,25W 0,1W 0,25W 0,1W 0,1W 0,1W 0,1W 0,1W 0,1W 0,1W 0,1	
4822 157 63661 4822 157 63661 4822 157 63676 4822 157 52842 4822 157 63675 4822 157 63659 4822 156 21456 4822 156 21459 4822 157 63678 4822 157 53265	56 15 330 560 100	дН дН дН дН		
	5322 122 34123 4822 122 33177 5322 122 32658 5322 122 34123 5322 122 34123 5322 122 34123 5322 122 34123 5322 122 34123 5322 122 33177 4822 122 33177 4822 122 33177 4822 122 33177 4822 122 33177 4822 122 33177 4822 122 33177 4822 122 33177 4822 122 33177 4822 122 33177 4822 122 331947 4822 122 331947 4822 122 331947 4822 122 331947 4822 122 331947 4822 122 331947 4822 122 331947 4822 122 33197 4822 122 33197 4822 122 33197 4822 122 33177 4822 122 33177 4822 122 33177 4822 122 33658 5322 122 32658 5322 122 32658 5322 122 32631 5322 122 36681 4822 051 2023 4822 051 20331 4822 051 20331 4822 051 20331 4822 051 20821 4822 051 20821 4822 051 20821 4822 051 20821 4822 051 20102 4822 051 20102 4822 051 20102 4822 051 20103 4822 051 20122 4822 051 10102 4822 051 20222 4822 051 10102 4822 051 20222 4822 051 10102 4822 051 20222 4822 051 10102 4822 051 20222 4822 051 10102 4822 051 20222 4822 051 10102 4822 051 20222 4822 051 10102 4822 051 20222 4822 051 20103 4822 051 20222 4822 051 20103 4822 051 20222 4822 051 20103 4822 051 20222 4822 051 20103 4822 051 20473 4822 051 20473 4822 051 20473 4822 051 20473 4822 051 20473 4822 051 20473 4822 051 20473 4822 051 20473 4822 051 20473 4822 051 20473 4822 051 20473 4822 051 20473 4822 051 20473 4822 051 20473 4822 051 20473 4822 051 20473 4822 051 20473 4822 051 20473 4822 051 20473 4822 051 20473 4822 051 20473 4822 051 20473 4822 051 20473 4822 051 20473 4822 051 20473 4822 157 63661 4822 157 63665 4822 157 63676 4822 157 63676 4822 157 63676 4822 157 63676 4822 157 63676 4822 157 63676 4822 157 63676 4822 157 63676 4822 157 63676 4822 157 63676 4822 157 63676 4822 157 63676 4822 157 63676 4822 157 63676 4822 157 63676 4822 157 63676 4822 157 63676 4822 157 63676 4822 157 63676	5322 122 34123 1 4822 122 33177 10 5322 122 32658 22 5322 122 34123 1 5322 122 32531 100 4822 122 33177 10 4822 122 33177 10 4822 122 33177 10 4822 122 33177 10 4822 122 33177 10 4822 122 33177 10 4822 122 33177 10 4822 122 33177 10 4822 122 33177 10 4822 122 33177 10 4822 122 33177 10 4822 122 33177 10 4822 122 33177 10 4822 122 33177 10 4822 122 33177 10 4822 122 33177 10 4822 122 331947 100 4822 122 31947 100 4822 122 31947 100 4822 122 31947 100 4822 122 31947 100 4822 122 31947 100 4822 122 31947 100 4822 122 31947 100 4822 122 31947 100 4822 122 3177 10 4822 122 3177 10 4822 122 3177 10 4822 122 3177 22 **TORS** 4822 051 20223 22 4822 051 10102 1 4822 051 20331 330 4822 051 20331 330 4822 051 20331 330 4822 051 20331 330 4822 051 20331 330 4822 051 20331 330 4822 051 20331 330 4822 051 20331 330 4822 051 20331 330 4822 051 20331 390 4822 051 20331 390 4822 051 20331 390 4822 051 10102 1 4822 051 20391 390 4822 051 20391 390 4822 051 20122 1,2 4822 051 10102 1 4822 051 20122 2,2 4822 051 10102 1 4822 051 20222 2,2 4822 051 10102 1 4822 051 20222 2,2 4822 051 20133 13 4822 051 20222 2,2 4822 051 20130 10 4822 051 20152 1,5 4822 051 20163 10 4822 051 20163 10 4822 051 20163 10 4822 051 20170 10 4822 051 20183 18 4822 051 20222 2,2 4822 051 10102 1 4822 051 20163 10 4822 051 20170 10 4822 051 20183 18 4822 051 20183 18 4822 051 20183 19 4822 051 20183 19 4822 051 20183 10 4822 051 20183 10 4822 051 20183 10 4822 051 20183 10 4822 051 20183 10 4822 051 20183 10 4822 051 20183 10 4822 051 20183 10 4822 051 20088 0 4822 051 20088 0 4822 051 20088 0 4822 157 63661 4822 157 63661 4822 157 63661 4822 157 63665 4822 157 63676 56 4822 157 63676 56 4822 157 63676 56 4822 157 63676 56 4822 157 63676 56 4822 157 63676 56 4822 157 63676 56 4822 157 63676 56 4822 157 63676 56 4822 157 63676 56 4822 157 63676 56 4822 157 63676 56 4822 157 63676 56 4822 157 63676 56 4822 157 63676 56 4822 157 63676 56 4822 157 63676 56 4822 157 63676 56 4822 157 63676 56 4822 157 63676 56 4822 157 63676 56 4822 157 63676 56	5322 122 34123 1 nF 5322 122 32658 22 pF 5322 122 34123 1 nF 5322 122 32531 100 pF 4822 122 33177 10 nF 5322 122 32659 33 pF 4822 122 33177 10 nF 4822 122 31947 100 nF 4822 122 3051 100 pF 5322 122 32658 22 pF 5322 122 32658 22 pF 5322 122 32531 100 pF 4822 051 2023 22 kΩ 4822 051 2022 22 kΩ 4822 051 10102 1 kΩ 4822 051 20331 330 Ω 4822 051 20422 1,2 kΩ 4822 051 20162 1 kΩ 4822 051 20163 1 kΩ 4822 051 20163 1 kΩ 4822 051 20172 1,5 kΩ 4822 051 20183 18 kΩ 4822 051 20160 1 l kΩ 4822 051 20172 1,5 kΩ 4822 051 20183 18 kΩ 4822 051 20160 1 l kΩ 4822 051 20160 0 l kΩ 4822 051 2000 0 l kΩ	Saz2 122 34123

TRAN	SISTORS & IC's	
7200	4822 130 42353	BSF19-F2
7201	5322 130 41982	BC848B
7202	4822 209 73852	PMBT2369
7203	5322 130 41982	BC848B
7205	5322 130 41982	BC848B
7207	4822 130 60145	DTC124E/25
7240	5322 130 41982	BC848B
7520	4822 209 73599	TDA4725/V2

IN/OUT, VPS MSIO/VPS PANEL

	4822 265 41295	15 p	in		
	4822 265 51352	Scar			
CAPA	CITORS				
2510 2511	4822 122 31947 4822 124 80454	100 10	nF μF	16V	
2541	4822 124 40248	10	μF	63V	
2561	4822 124 80454	10	μF	16V	
25.70 2571	4822 122 31727 4822 122 31727	470 470	pF pF	63V 63V	
2580	4822 122 31727	470	pF	63V	
2581	4822 124 40248	10	μF	63V	
2590 2591	4822 122 31947 4822 122 32442	100 10	nF nF	63V 50V	
2600	4822 122 31825	27	pF	30 V	only for VPS
2601	4822 122 31981	33	'nF		only for VPS
2602 2603	4822 122 31947 4822 122 31947	100 100	nF nF	63V	only for VPS
	TOCK 122 0134/	100	111		
RESIS	TORS	,			
3508	4822 051 10471	470	Ω	0,25W	
3509 . 3511	4822 051 10561 4822 051 10471	560 470	Ω	0,25W	
3512	4822 051 10471	1,2	kΩ		
3513	4822 051 10123	12	$k\Omega$		
3514 3515	4822 051 10123 4822 051 10103	12 10	kΩ kΩ	0,25W 0,25W	
3515 3516	4822 051 10103	22	Ω	0,23**	
3517	4822 051 10101	100	Ω		
3518 3541	4822 051 10101 4822 051 10104	100 100	Ω k Ω	0,25W	
3541 3542	4822 051 10472	4,7	kΩ	0,25 W	
3550	4822 051 10829	82	Ω	0,25W	
3551 3560	4822 051 10682 4822 051 10102	6,8 1	kΩ kΩ	0,25W	
3561	4822 051 10471	470	Ω		
3562	4822 051 10122	1,2	kΩ		
3563 3564	4822 051 10123 4822 051 10123	12 12	kΩ kΩ	0,25W	
3565	4822 051 10123	10	kΩ	0,25W 0,25W	
3566	4822 051 10101	100	Ω		
3567 3568	4822 051 10101 4822 051 10229	100 22	Ω		
3570	4822 051 10682	6,8	kΩ	0,25W	
3571	4822 051 10682	6,8	$k\Omega$	0,25W	
3572 3581	4822 051 10224 4822 051 10104	220 100	kΩ kΩ	0,25W 0,25W	
3581 3582	4822 051 10104	4,7	kΩ	0,25W 0,25W	
3583	4822 051 10224	220	$k\Omega$	0,25W	
3584	4822 051 10471	470	Ω	0,25W	only for VDS
3600 3601	4822 051 10101 4822 051 10101	100 100	Ω	1/8 W 1/8 W	only for VPS only for VPS
3602	4822 051 10332	3,3	$k\Omega$	1/8W	only for VPS
3603 3604	4822 051 10682	6,8 1	kΩ.	1/8W 1/8W	only for VPS only for VPS
3604 3605	4822 051 10105 4822 051 10562	5,6	MΩ kΩ	1/8W	only for VPS
3606	4822 051 10105	1	ΩM	1/8W	only for VPS
3607	4822 051 10101	100	Ω	1/8W	only for VPS
3608 3609	4822 051 10104 4822 051 10105	100 1	$M\Omega$	1/8W 1/8W	only for VPS only for VPS
3802	4822 051 10008	0	Ω	0,25W	,
3906	4822 051 10008	0	Ω	0,25W	
3910 3911	4822 051 10008 4822 051 10008	0	Ω	0,25W 0,25W	
3913	4822 051 10008	Õ	Ω	0,25W	

6550 6551 6552 6560 6561 6562 6563 6564 6565 6566 6570 6571 6580 6590	4822 130 34197 4822 130 30621 4822 130 30621 4822 130 34197 4822 130 34197 4822 130 34197 4822 130 31024 4822 130 30621 4822 130 30621 4822 130 34197 4822 130 34197 4822 130 34197 4822 130 34197 4822 130 31024	BZX79-B12 1N4148 1N4148 BZX79-B12 BZX79-B12 BZX79-B12 BZX79-B18 1N4148 1N4148 BZX79-B12 BZX79-B12 BZX79-B12 BZX79-B12 BZX79-B18		
				<u> </u>
TRAN	SISTORS & IC's		1	
7509 7510 7511 7540 7550 7560 7562 7580 7590 7591 7593 7594 7600	5322 130 41983 4822 130 42353 5322 130 41983 5322 130 42616 4822 130 42353 5322 130 41983 5322 130 41982 5322 209 10576 5322 209 10576 5322 130 41982 5322 130 41982 5322 130 41982 4822 209 73306	BC858B BFS19 BC858B BC848B BC818-40 BFS19 BC858B BC848B HEF4053BD HEF4053BD BC848B BC848B VPS IC SDAS	5642	only for VPS

IN/OUT, TELETEXT, VPS MVIO PANEL

CONN	IECTORS :	
	4822 265 41295 4822 265 31086 4822 265 51352	15 pin 6 pin Scart
MISCE	LLANEOUS	
1000 1200 1300	4822 242 81099 4822 242 81471 4822 320 40186	Crystal 12 MHz Crystal 27 MHz Delay line
CAPA	CITORS	
2001 2002 2003 2004 2005 2010 2020 2030 2200 2201 2202 2204 2206 2207 2208 2209 2210 2211 2252 2303 2301 2302 2303 2304 2305 2306 2307 2308 2307 2308 2309 2311 2302 2303 2304 2305 2306 2307 2308 2307 2308 2309 2310 2311 2302 2303 2304 2305 2306 2307 2308 2307 2308 2309 2310 2311 2312 2308 2309 2310 2311 2312 2312 2313 2314 2350 2351 2370 2371 2372 2400 2401 2402 2403 2404 2405 2511 2550 2551 2561 2570 2571 2580 2581 2590 2581 2590 2581 2590 2581 2590 2581 2590 2581 2590 2581 2590 2581 2590 2581 2590 2581	4822 122 31947 5322 122 32658 5322 122 32658 4822 122 33177 5322 122 34123 4822 122 31947 4822 122 31947 4822 122 31947 4822 122 31947 4822 122 31947 4822 122 31947 5322 122 34123 5322 122 3448 5322 122 31947 4822 122 31947 4822 122 31947 4822 122 31947 4822 122 31947 4822 122 31947 4822 122 31947 4822 122 31947 4822 122 31947 4822 122 31947 4822 122 31947 4822 122 31947 4822 122 31947 4822 122 31947 5322 122 34123 5322 122 34123 5322 122 34123 5322 122 34123 5322 122 34123 5322 122 34123 5322 122 34123 5322 122 34123 5322 122 34123 5322 122 34123 5322 122 34123 5322 122 34123 5322 122 34123 5322 122 34123 5322 122 34123 5322 122 31947 5322 122 31947 5322 122 32661 4822 124 80227 4822 124 80227 4822 124 80227 4822 124 80227 4822 124 80231 4822 122 31947 5322 122 32481 4822 122 31947 5322 122 32481 4822 122 31947 5322 122 32481 4822 122 31947 5322 122 32481 4822 122 31947 5322 122 32481 4822 122 31947 5322 122 32481 5322 122 32481 5322 122 32482 5322 122 32481 5322 122 32481 5322 122 32481 5322 122 32482 5322 122 32481 5322 122 32481 5322 122 32482 5322 122 32482 5322 122 32482 5322 122 32482 5322 122 32482 5322 122 32482 5322 122 32482 5322 122 32482 5322 122 32482 5322 122 32482 5322 122 32482 5322 122 32482 5322 122 32482 5322 122 32482 5322 122 32482 5322 122 32482 5322 122 32482	100 nF 63V 22 pF 50V 22 pF 50V 10 nF 50V 11 nF 50V 11 nF 50V 11 nF 63V 1100 nF 50V 1100 nF 63V 1100 nF

RESIS	TORS				 -
3000	4822 051 20472	4,7	kΩ	0,1W	
3001	4822 051 20472	4,7	kΩ	0,1W	
3003 3004	4822 051 20472 4822 051 20472	4,7 4,7	kΩ kΩ	0,1W 0,1W	
3010	4822 051 10102	1	kΩ	0,1 VV 0,25W	
3011	4822 051 20181	180	Ω	0,1W	
3200	4822 051 20101	100	Ω	0,1W	
3201	4822 051 20101	100	Ω	0,1W	
3202 3203	4822 051 20681 4822 051 20332	680 3,3	Ω k Ω	0,1W 0,1W	
3204	4822 051 20151	150	Ω	0,1W	
3205	4822 051 20273	27	kΩ	0,1W	
3300	4822 051 10822	8,2	kΩ	0,25W	
3301	4822 051 20222	2,2	kΩ	0,1W	
3302 3303	4822 051 20224 4822 051 10563	220 56	kΩ kΩ	0,1W 0,25W	
3304	4822 051 10102	1	kΩ	0,25W	
3305	4822 051 20681	680	Ω	0,1W	
3306	4822 051 10102	1	kΩ	0,25W	
3307 3340	4822 051 10102 4822 051 20105	1	kΩ MΩ	0,25W 0,1W	
3341	4822 051 20103	3,3	kΩ	0,1 VV	
3350	4822 051 20471	470	Ω	0,1W	
3351	4822 051 20471	470	Ω	0,1W	
3352 3370	4822 051 20221 4822 051 20101	220 100	Ω	0,1W	
3370	4822 051 20101	47	Ω	0,1W 0,1W	
3372	4822 051 20271	270	Ω	0,1W	
3373	4822 051 20225	2,2	$M\Omega$	0,1W	
3374	4822 051 20223 4822 051 20103	22	kΩ	0.414/	
3400 3401	4822 051 20103	10 10	kΩ kΩ	0,1W 0,1W	
3403	4822 051 20472	4,7	kΩ	0,	
3404	4822 051 20221	220	Ω	0,1W	
3405 3406	4822 051 10102 4822 051 10102	1	kΩ kΩ	0,25W	
3407	4822 051 20122	1,2	kΩ	0,25W	
3408	4822 051 10102	1	kΩ	0,25W	
3409	4822 051 20472	4,7	kΩ	0,1W	
3451 3452	4822 051 20472 4822 051 20472	4,7 4,7	kΩ kΩ		
3501	4822 051 20222	2,2	kΩ	0,1W	
3510	4822 051 10102	1,0	$k\Omega$		
3511	4822 051 20471	470	Ω		
3512 3513	4822 051 20122 4822 051 20123	1,2 12	kΩ kΩ		
3514	4822 051 20123	12	kΩ	0,1W	
3515	4822 051 20103	10	kΩ	0,1W	
3516	4822 051 20471	470	Ω		
3517 3518	4822 051 20471 4822 051 20229	470 22	Ω		
3519	4822 051 20101	100	Ω		
3520	4822 051 20101	100	Ω		
3530 3541	4822 051 20109 4822 051 10104	10	Ω	0.05\4/	
3542	4822 051 10104	100 4,7	kΩ kΩ	0,25W 0,1W	
3550	4822 051 20829	82	Ω	0,1W	
3560	4822 051 20229	22	Ω		
3561 3562	4822 051 10102 4822 051 20122	1,0 1,2	kΩ kΩ		
3563	4822 051 20123	12	kΩ		
3564	4822 051 20123	12	kΩ	0,1W	
3565	4822 051 20103	10	kΩ	0,1W	
3566 3567	4822 051 20471 4822 051 20101	470 100	Ω		
3568	4822 051 20101	100	Ω		
3570	4822 051 20682	6,8	$k\Omega$		
3571	4822 051 20682	6,8	kΩ	0.114	
3572 3580	4822 051 20224 4822 051 20471	220 470	kΩ Ω	0,1W 0,1W	
3581	4822 051 20104	100	kΩ	0,1W	
3582	4822 051 20472	4,7	kΩ	0,1W	
3583 3590	4822 051 20224 4822 051 10682	220 6,8	kΩ kΩ	0,1W 0,25W	
5550	7022 031 10002	0,0	N22	U,234V	

COILS	3	
		47
5000	4822 157 70503 4822 157 70503	4,7 μH 4,7 μH
5010		4,7 μn 4,7 μH
5020	4822 157 70503	
5030	4822 157 70503	4,7 μΗ
5200	4822 157 63343	47
5201	4822 157 70503	4,7 μΗ
5250	4822 157 53265	100 μΗ
5300	4822 157 63343	
5301	4822 157 53252	22 μΗ
5370	4822 157 70131	
5400	4822 157 63343	/
DIODE	S	
6550	4822 130 33699	
6551	5322 130 34331	BAV70
6560	4822 130 33699	BZX84-C12
6561	5322 130 34331	BAV70
6570	4822 130 33699	BZX84-C12
6571	4822 130 33699	BZX84-C12
6580	4822 130 33699	BZX84-C12
6590	4822 130 33699	BZX84-C12
6592	4822 130 33699	BZX84-C12
TRAN	SISTORS & IC's	
7000	4822 209 32331	SAB 8032 WITH SOCKET
7020	5322 209 31276	PC74HCT573T
7030	4822 209 31553	FCB61C65LL-70T
7200	4822 209 32328	SAA5248GP/E
7250	4822 209 31553	FCB61C65LL-70T
7300	4822 209 71415	MC1377P
7340	5322 130 41982	BC848B
7350	5322 130 41982	BC848B
7351	5322 130 41982	BC848B
7370	5322 130 41982	BC848B
7371	5322 130 41982	BC848B
7372	5322 130 41982	BC848B
7400	4822 209 32327	BA7605N
7401	5322 130 41983	BC858B
7402	4822 130 61495	DTA 124EK
7403	5322 130 41982	BC848B
7404	4822 209 73852	PMBT 2369
7405	5322 130 41983	BC858B
7511	5322 130 41983	BC858B
7512	4822 130 42353	BFS19
7512 7513	5322 130 42353	BC858B
7540	5322 130 41983	BC848B
7561 7562	5322 130 41982	BC848B
7562 7562	4822 130 42353	BFS19
7563	5322 130 41983	BC858B
7580	5322 130 41982	BC848B
7590	5322 209 14481	HEF4053BT
7591	5322 209 14481	HEF4053BT
7595	4822 130 42616	BC818-40

PCS 74583

IN/OUT, OSD, VPS N	10	PANEL
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			111/0	31,000,0		1011111111	
					2055	1000 051 00102	10 ! kΩ 0,1
MISC	ELLANEOUS				3855 3856	4822 051 20103 4822 051 20222	$2,2 k\Omega 0,1$
	4822 265 41295	15 pi	n d		3857 3858	4822 051 20472 4822 051 20222	2,2 kΩ 0,1
CAPA	CITORS				3859 3860	4822 051 20008 4822 051 20472	0 Ω 0,1 4,7 $k\Omega$ 0,1
	4822 126 10002	100	nF 25V		3861 3862	4822 051 20223 4822 051 20153	22 kΩ 0,1 15 kΩ 0,1
2510 2511	4822 124 80535	10	μF 16V		3863 3864	4822 051 20104 4822 051 20472	100 kΩ 0,1 4,7 kΩ 0,1
2541 2560	4822 124 22826 4822 126 10002	10 100	μF 16V nF 25V		3865 3866	4822 051 20472 4822 051 20475	4,7 k Ω 0, 4,7 M Ω 0,
2561 2570	4822 124 80535 5322 122 32268	10 470	μF 16V pF 50V		3867	4822 051 20475	4,7 MΩ 0,
2571	5322 122 32268	470	pF 50V		3901 3905	4822 051 20008 4822 051 10008	$\begin{array}{cccc} 0 & \Omega & 0, \\ 0 & \Omega & 0, \end{array}$
2580 2581	5322 122 32268 4822 124 22826	470 10	pF 50V μF 16V		3907 3912	4822 051 10008 4822 051 10008	$\begin{array}{cccccccccccccccccccccccccccccccccccc$
2590 2591	4822 126 10002 4822 122 33177	100 10	nF 25V nF 50V		3915	4822 051 20008	0 Ω 0,
2600	5322 12231946	27	pF 50V	only for VPS only for PDC	3922 3923	4822 051 10008 4822 051 10008	$\begin{array}{cccc} 0 & \Omega & 0, \\ 0 & \Omega & 0, \end{array}$
2602 2603	4822 126 10002 4822 126 10002	100	nF 50V	only for VPS			
2850 2851	4822 124 22826 4822 124 22826	10	μF 16V μF 16V		DIODI	ES	.,
2852 2853	5322 126 10223 5322 116 80853	4,7 560	nF 63V pF 63V		6550 6551	4822 130 34197 4822 130 30621	BZX79-B12 1N4148
2854 2855	5322 126 10223 5322 116 80853	4,7 560	nF 63V pF 63V		6552	4822 130 30621 4822 130 34197	1N4148 BZX79-B12
2858	4822 124 22826	10	μF 16V nF 25V		6560 6564	4822 130 31024	BZX79-B18
2860	4822 126 10002	100	TIF 25V		6565 6567	5322 130 34331 4822 130 30621	BAV70 1N4148
RESI	STORS				6570 6571	4822 130 34197 4822 130 34197	BZX79-B12 BZX79-B12
3508	4822 051 20471	470	Ω 0,1W		6580 6590	4822 130 34197 4822 130 31024	BZX79-B12 BZX79-B18
3509 3510	4822 051 20471 4822 051 10102	470	Ω 0,1W kΩ 0,25W		0550	4022 100 01024	:
3511	4822 051 20471	470	Ω 0,1W		TRAN	ISISTORS & IC's	
3512 3513	4822 051 20122 4822 051 20393	1,2 39	$k\Omega$ 0,1W $k\Omega$ 0,1W		7509	5322 130 41983	BC858B
3514 3515	4822 051 20123 4822 051 20103	12 10	kΩ 0,1W kΩ 0,1W		7510 7511	4822 130 42353 5322 130 41983	BSF19-F2 BC858B
3516	4822 116 52191 4822 051 20104	33 100	Ω 0,5W $k\Omega$ 0,1W		7540	5322 130 41982	BC848B BC818-40
3541 3542	4822 051 20472	4,7	kΩ 0,1W		7550 7560	4822 130 42616 4822 130 42353	BSF19-F2
3550 3551	4822 051 20829 4822 051 20682	82 6,8	Ω 0,1W $k\Omega$ 0,1W		7562 7580	5322 130 41983 5322 130 41982	BC858B BC848B
3560 3561	4822 051 10102 4822 051 20471	1 470	$k\Omega$ 0,25W Ω 0,1W		7590	5322 209 14481 5322 209 14481	HEF4053BT HEF4053BT
3562	4822 051 20122	1,2	kΩ 0,1W		7591 7593	5322 130 41982	BC848B
3563 3564	4822 051 20393 4822 051 20123	39 12	$k\Omega$ 0,1W $k\Omega$ 0,1W		7600 7851	4822 209 32728 5322 130 41982	SDA5642-5 BC848B
3565 3566	4822 051 20103 4822 051 20101	10 100	kΩ 0,1W Ω 0,1W		7852	5322 130 41982	BC848B
3567	4822 051 20229 4822 051 20101	22 100	Ω 0,1W Ω 0,1W				
3568 3570	4822 051 20682	6,8	kΩ 0,1W				
3571 3572	4822 051 20682 4822 051 20224	6,8 220	$k\Omega$ 0,1W $k\Omega$ 0,1W				
3581 3582	4822 051 20104 4822 051 20472	100 4,7	kΩ 0,1W kΩ 0,1W				
3583	4822 051 20224	220	kΩ 0,1W				
3584 3600	4822 051 20471 4822 051 20101	470 100	Ω 0,1W	only for VPS			
3601 3602	4822 051 20101 4822 051 20332	100 3,3	Ω 0,1W Ω 0,1W	only for VPS only for VPS			
3603	4822 051 20682 4822 051 20105	6,8 1,0	$k\Omega$ 0,1W $M\Omega$ 0,1W	only for VPS only for VPS			
3604 3605	4822 051 20562	5,6	kΩ 0,1W	only for PDC			
3606 3607	4822 051 20105 4822 051 20101	1,0 100	Ω 0,1W Ω 0,1W	only for VPS only for VPS			
3608 3609	4822 051 20104 4822 051 20105	100 1,0	$M\Omega = 0.1W$ $M\Omega = 0.1W$	only for VPS only for VPS			
3850	4822 051 20153	15	kΩ 0,1W				
3851 3852	4822 051 20103 4822 051 20103	10 10	kΩ 0,1W kΩ 0,1W				
3853 3854	4822 051 20103 4822 051 20153	10 15	kΩ 0,1W kΩ 0,1W				
0004	1022 001 20100	. 0	-,		•		

HEAD AMPLIFIERS LHA

4822 214 33714 LHA 2/0 4822 214 33761 LHA 3/0 4822 214 33666 LHA 4/0

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only for VPS

NFM PANEL

4822 214 60168 NFM

CABLES

4822 321 61675	Cable chinch	not for N4
4822 320 50318	Cable B6 - L8	only for N4
4822 320 50293	Cable SM1 - F1	not for N4
4822 321 61675	Cable chipch	only for N4
4822 320 50294	Cable DC1 - F6	not for N4
4822 320 50299	Cable L4 - F2	only for N4
4822 320 50319	Cable L6 - 1911	not for N4
4822 320 50293	Cable SM1 - F1	only for N4
4822 320 50321	Cable L1 - 1915	not for N4
4822 320 50297	Cable L2 - F3	
4822 320 50298	Cable L3 - F8	
4822 320 50299	Cable L4 - F2	not for N4
4822 320 50318	Cable B6 - L8	not for N4
4822 320 50342	Cable 1932 - 1912	only for N4
4822 320 50344	Cable L1 - 1915	only for N4
4822 320 50345	Cable L6 - 1911	only for N4
4822 320 50346	Cable 1103 - 1925	only for N4
4822 320 50343	Cable 1105 - 1922	only for N4